Features

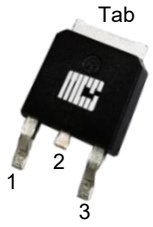
- High Speed Switching with Low Capacitances
- High Blocking Voltage with Low $R_{DS(on)}$
- Easy to parallel
- Simple to drive
- RoHS Compliant

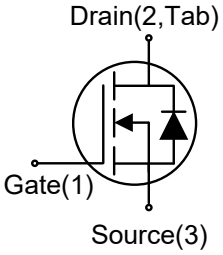
Part NO.		HX2M300065E
V_{DS}	=	650 V
$I_D(T_c=25℃)$	=	13 A
$R_{DS(on)}$	=	270 mΩ

Benefits

- Increased Power Density
- Faster Operating Freequency
- Reduction of Heat Sink Requirements
- Higher Efficiency
- Reduced EMI

TO-252-2L





Applications

- Power Factor Correction Modules
- Switch Mode Power Supplies
- DC-AC Inverters
- High Voltage DC/DC Converters

Halogen

RoHS

Maximum ratings (T_j=25℃ unless otherwise specified)

Symbol	Parameter	Test conditions	Value	Unit
V_{DS}	Drain-Source Voltage		650	V
I_D	Continuous Drain Current	T_c=25°C T_c=100°C	13 9.2	A
I_{DM}	Peak Drain Current	Pulse width t_p limited by T_{jmax}	24	A
V_{GSmax}	Gate-Source Voltage		-8/+22	V
V_{GSop}	Recommend Gate-Source Voltage		-4/+18	V
P_{tot}	Power Dissipation	T_c=25°C T_c=100°C	57 28.5	W
T_j	Operating Junction Temperature		-40~175	°C
T_{stg}	Storage Temperature		-40~175	°C

Electrical Characteristics

Static Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D=100μA, V_{GS}=0V	650			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=650V, V_{GS}=0V		1		μA
I_{GSS}	Gate-Source Leakage Current	V_{DS}=0V, V_{GS}=18V			250	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=1.33mA T_j=25°C T_j=175°C	2	3.1 2.2	4	V
$R_{DS(on)}$	Drain-Source On-State Resistance	V_{GS}=15V, I_D=4.5A T_j=25°C T_j=175°C		370 340	500	mΩ
		V_{GS}=18V, I_D=4.5A T_j=25°C T_j=175°C		270 298	357	

Thermal Characteristics

Symbol	Parameter	Value			Unit
		Min.	Typ.	Max.	
$R_{th(j-c)}$	Thermal Resistance from Junction to Case		2.63		°C/W

Dynamic Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
C_{iss}	Input Capacitance	V_{DS}=600V, f=1MHz, V_{GS}=0V		203		pF
C_{oss}	Output Capacitance			26		pF
C_{rss}	Reverse Transfer Capacitance			3.5		pF
$R_{G(int)}$	Internal Gate Resistance	f=1MHz		3.6		Ω
Q_g	Total Gate Charge	V_{DS}=400V, I_D=4.5A, V_{GS}=-4/18V		11.2		nC
Q_{gs}	Gate to Source Charge			2		nC
Q_{gd}	Gate to Drain Charge			5.2		nC

Switching Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
$t_{d(on)}$	Turn-On Delay Time	V_{DD}=400V, I_D=4.5A, V_{GS}=-4V/18V, R_G=2.5Ω, L=200μH		3		ns
t_r	Rise Time			8		ns
$t_{d(off)}$	Turn-Off Delay Time			6		ns
t_f	Fall Time			11		ns
E_{on}	Turn-On Energy			21		μJ
E_{off}	Turn-Off Energy			5		μJ

Reverse Diode Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
V_{SD}	Diode Forward Voltage	V_{GS}=-4V, I_{SD}=2.5A T_j=25°C T_j=175°C V_{GS}=-4V, I_{SD}=4.5A T_j=25°C T_j=175°C		 4.0 3.6 4.5 4.0		V
I_S	Continuous Diode Forward Current	V_{GS}=-4V T_c=25°C T_c=100°C		 13 6.5		A
t_{rr}	Reverse Recovery Time	V_{GS}=-4V, I_{SD}=4.5A, V_R=400V, di/dt=1000A/μs		10		ns
Q_{rr}	Reverse Recovery Charge			56		nC
I_{rrm}	Peak Reverse Recovery Current			4		A

Typical Performance

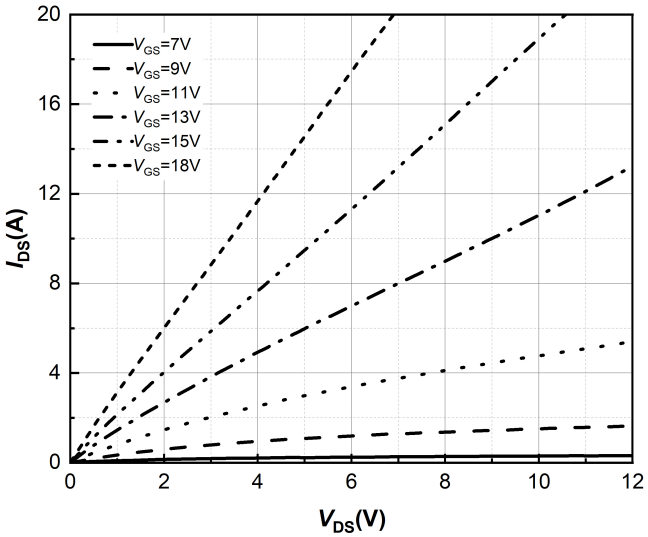


Figure 1. **Output Characteristics T_j=-40°C**

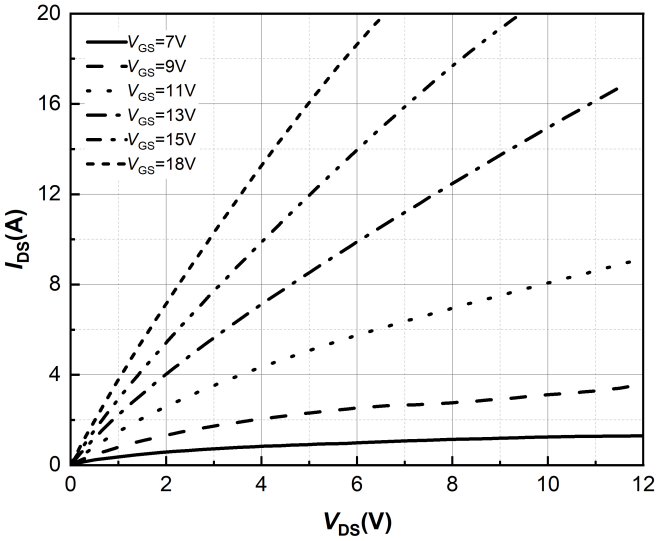


Figure 2 **Output Characteristics**

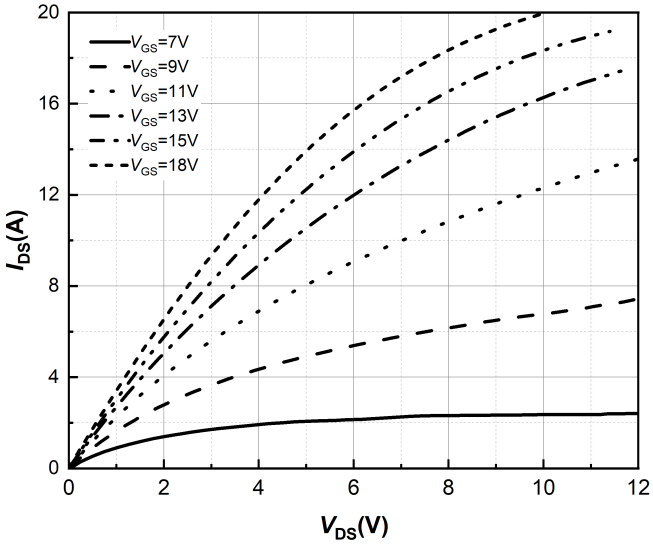


Figure 3. **Output Characteristics T_j=175°C**

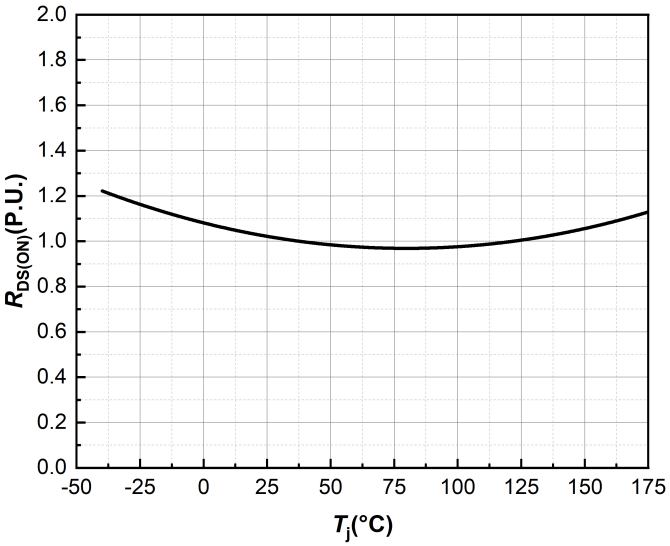


Figure 4. **Normalized On-Resistance vs. Temperature**

Typical Performance

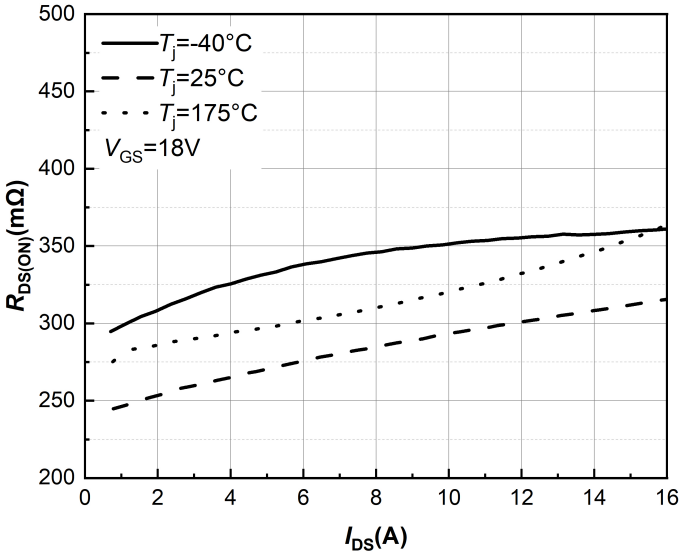


Figure 5. **On-Resistance vs. Drain Current For Various Temperatures**

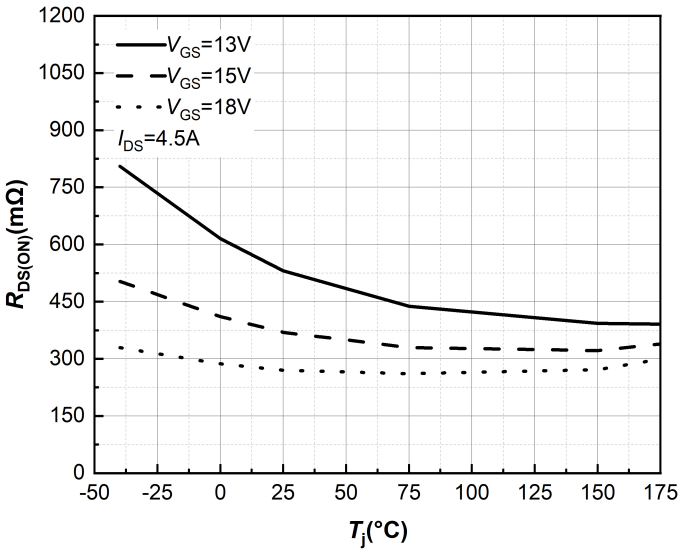


Figure 6. **On-Resistance vs. Temperature For Various Gate Voltage**

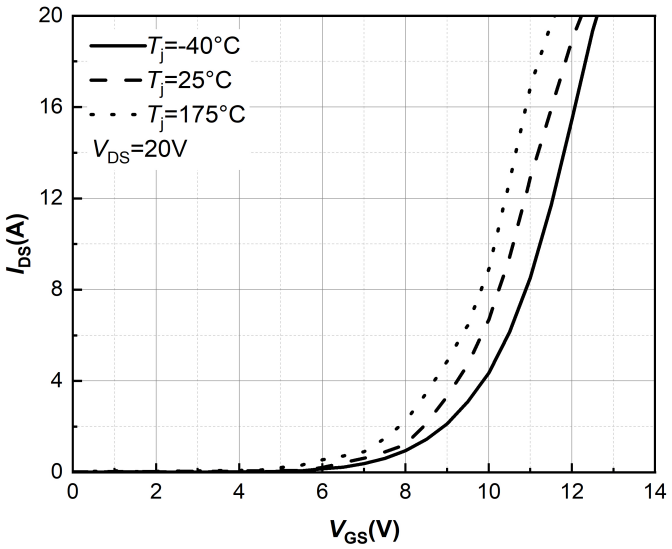


Figure 7. **Transfer Characteristic for Various Junction Temperatures**

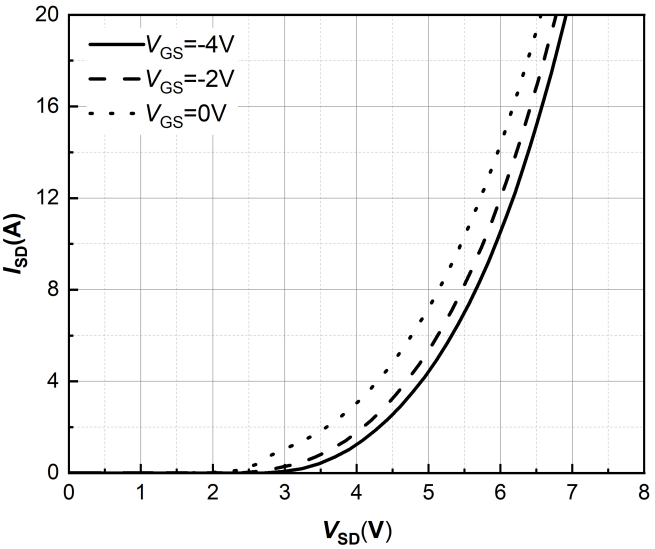


Figure 8. **Body Diode Characteristic T_j=-40°C**

Typical Performance

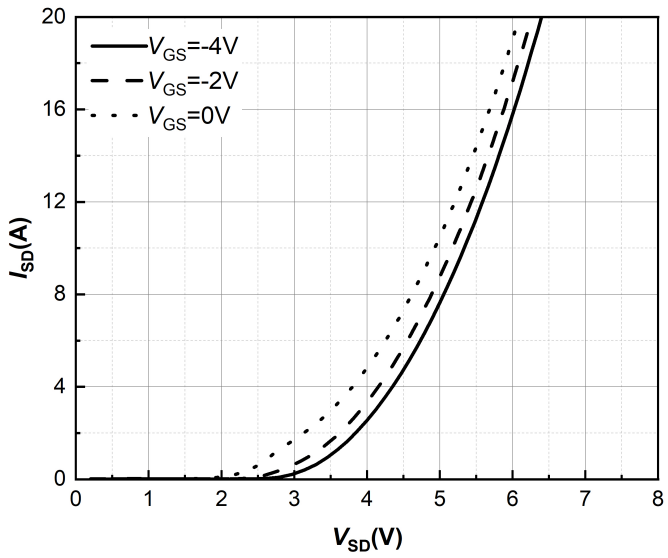


Figure 9. **Body Diode Characteristic T_j=25°C**

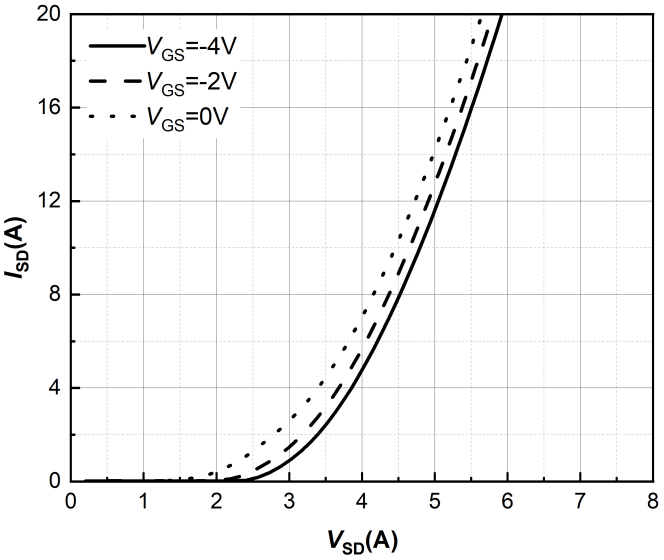


Figure 10. **Body Diode Characteristic T_j=175°C**

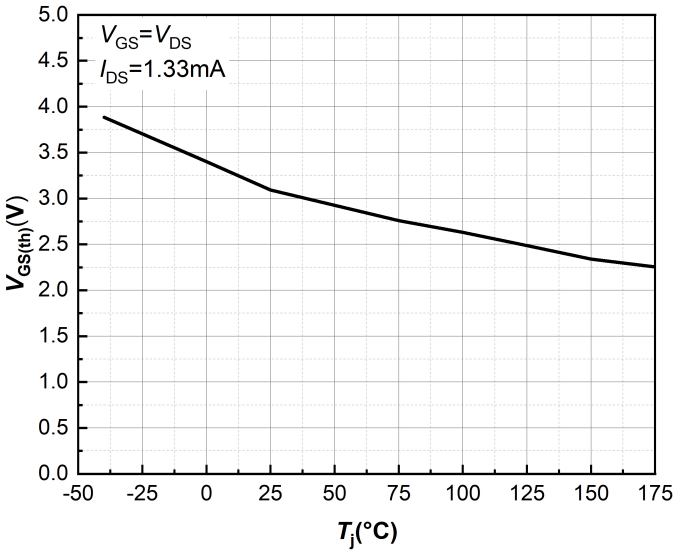


Figure 11. **Threshold Voltage vs. Temperature**

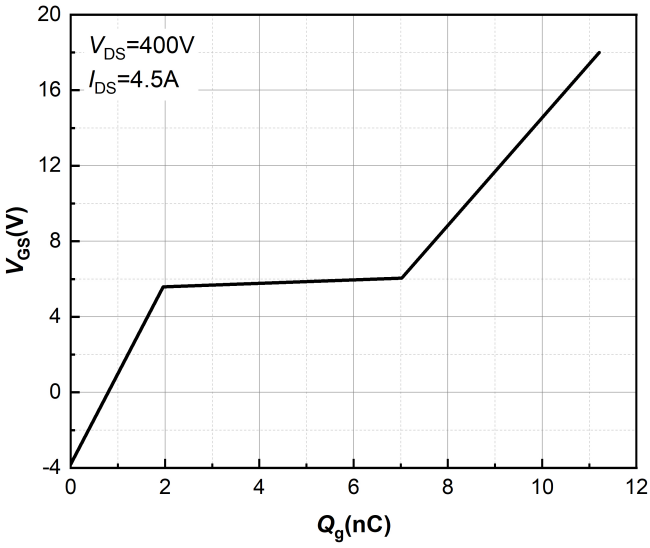


Figure 12. **Gate Charge Characteristics**

Typical Performance

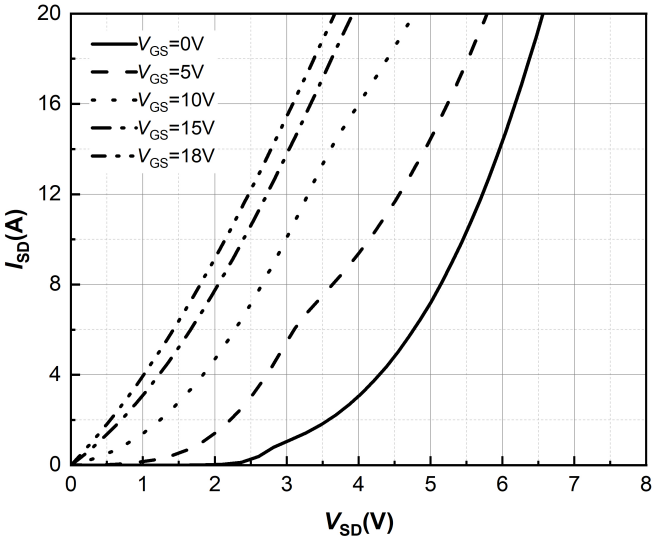


Figure 13. **3rd Quadrant Characteristic T_j=-40°C**

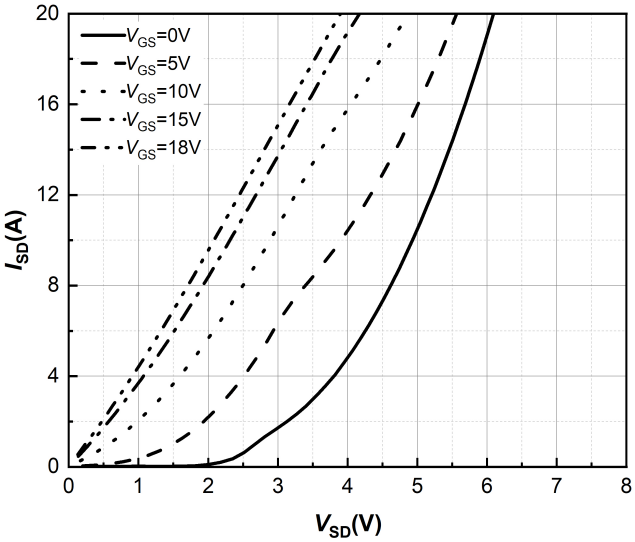


Figure 14. **3rd Quadrant Characteristic T_j=25°C**

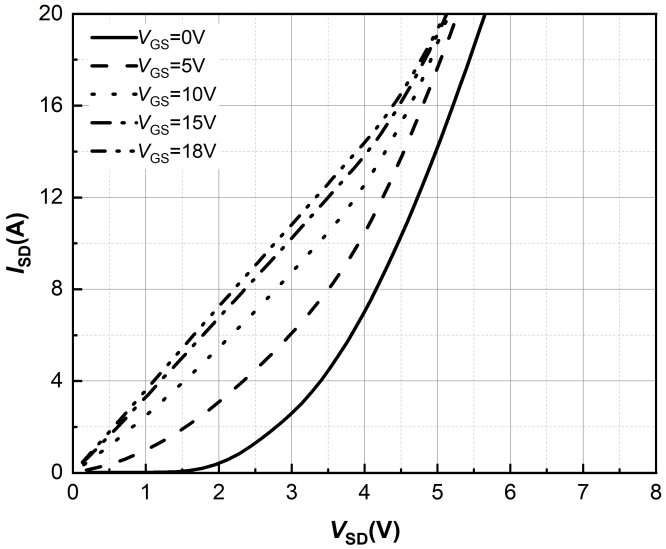


Figure 15. **3rd Quadrant Characteristic T_j=175°C**

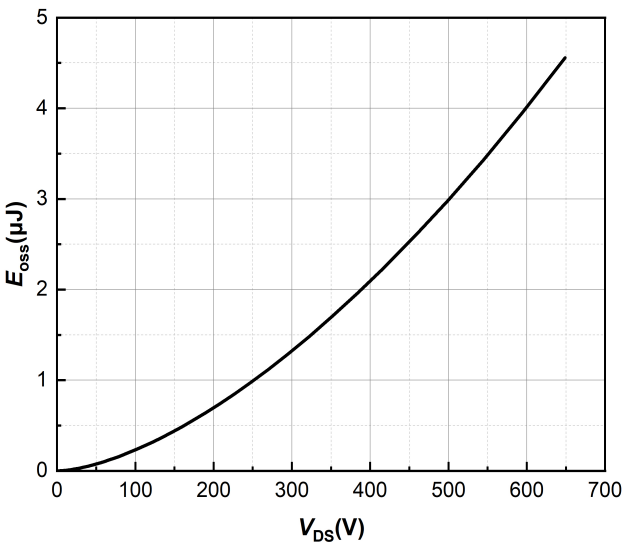


Figure 16. **Output Capacitor Stored Energy**

Typical Performance

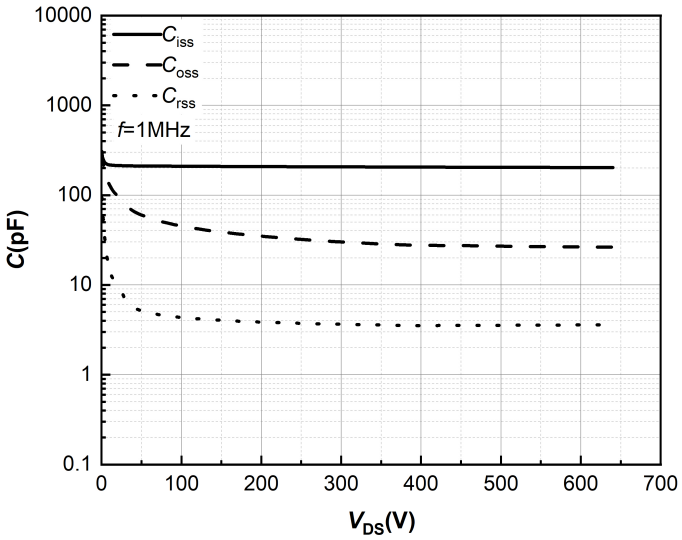


Figure 17. **Capacitances vs. Drain-Source**

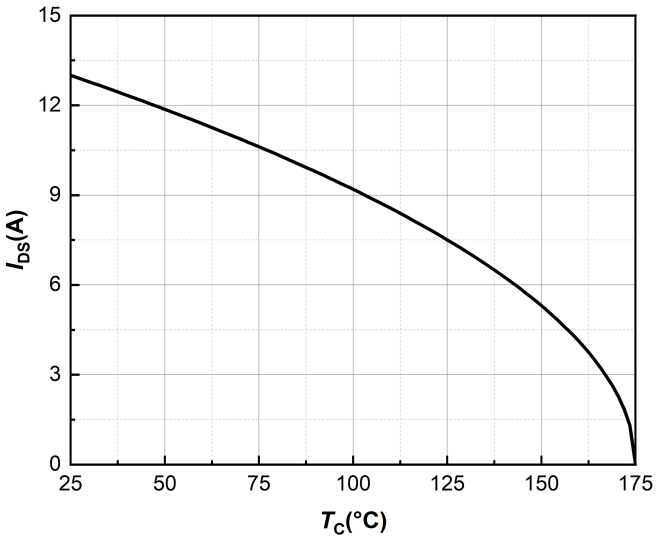


Figure 18. **Continuous Drain Current Derating vs. Case Temperature**

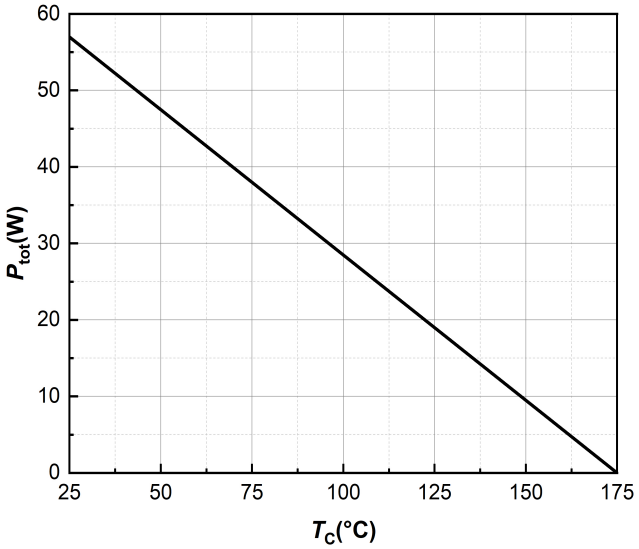


Figure 19. **Maximum Power Dissipation Derating vs. Case Temperature**

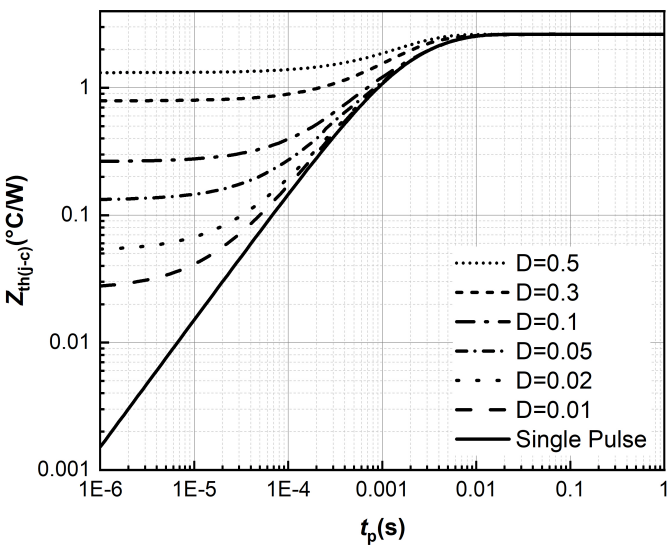


Figure 20. **Transient Thermal Impedance**

Typical Performance

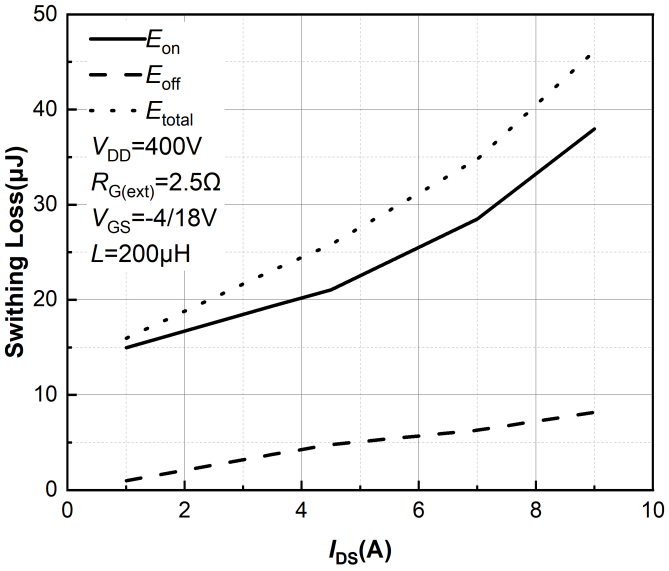


Figure 21. **Clamped Inductive Switching Energy vs. Drain Current**

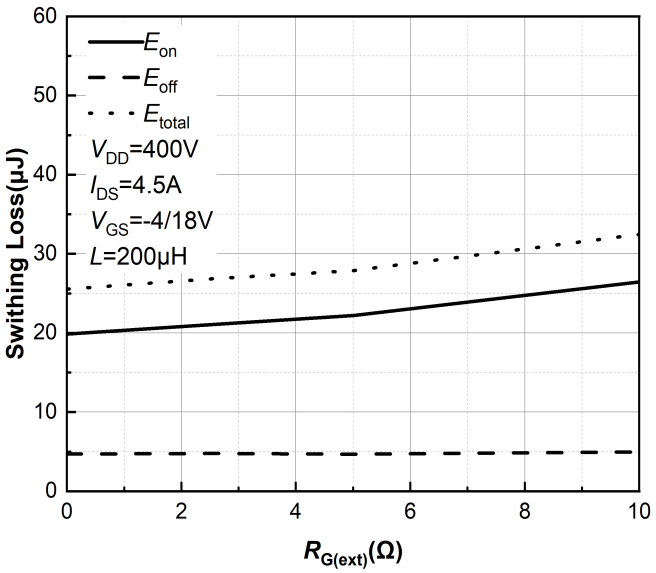


Figure 22. **Clamped Inductive Switching Energy vs. R_G**

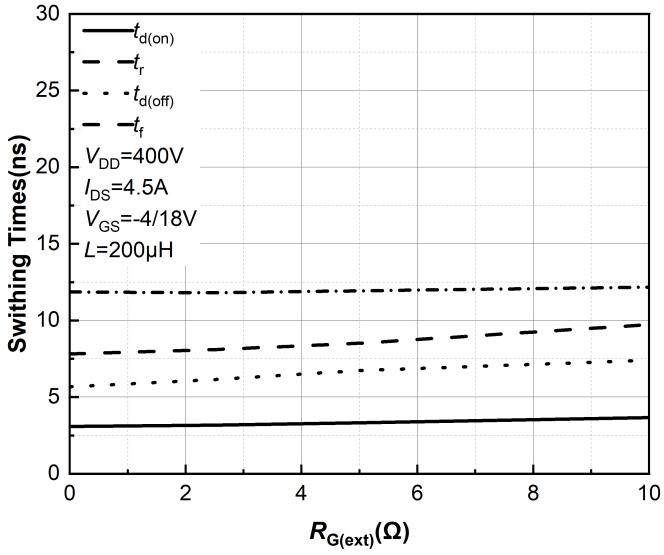


Figure 23. **Switching Times vs. R_G**

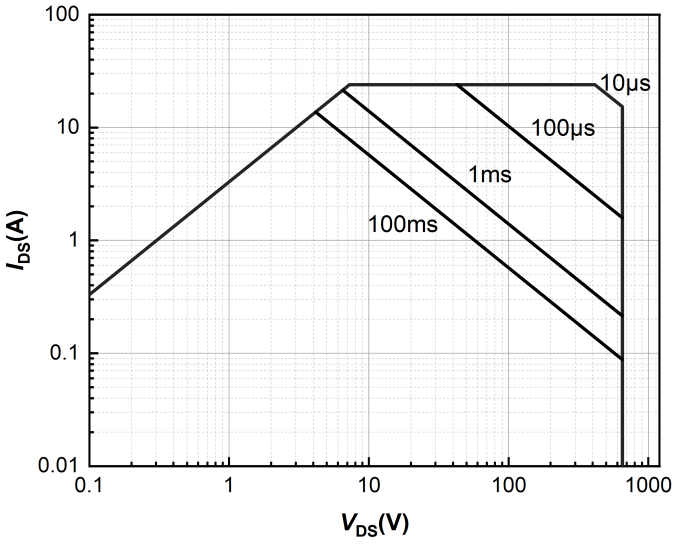
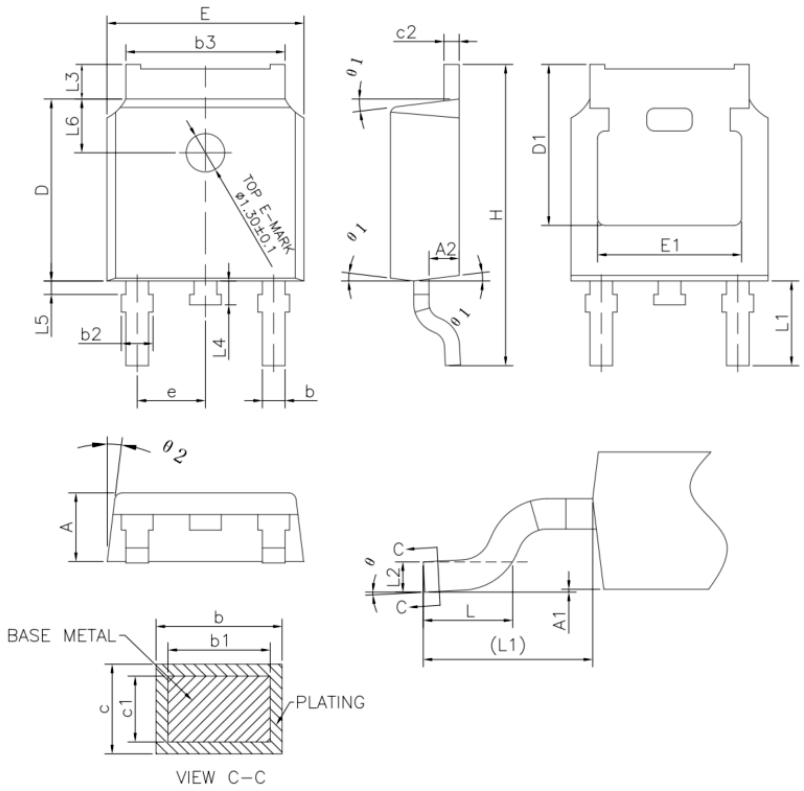


Figure 24. **Safe Operating Area**

Package Outlines



SYMBOL	Unit: mm		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0	-	0.10
A2	0.90	1.01	1.10
b	0.72	-	0.85
b1	0.71	0.76	0.81
b2	0.72	-	0.90
b3	5.13	5.33	5.46
c	0.47	-	0.60
c1	0.46	0.51	0.56
c2	0.47	-	0.60
D	6.00	6.10	6.20
D1	5.25	-	-
E	6.50	6.60	6.70
E1	4.70	-	-
e	2.186	2.286	2.386
H	9.80	10.10	10.40
L	1.40	1.50	1.70
L1	2.90 REF		
L2	0.508 BSC		
L3	0.90	-	1.25
L4	0.60	0.80	1.00
L5	0.15	-	0.75
L6	1.80 REF		
θ	0°	-	8°
θ1	5°	7°	9°
θ2	5°	7°	9°

Revision History

Document Version	Date of Release	Description of Changes
Preliminary	2025-04-15	Release of the datasheet.
Preliminary A	2025-05-19	Characteristics update.

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