IPD30N08S2L-21-VB Datasheet

N-Channel100-V(D-S)MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)[a]	Q_g (Typ.)
100	0.0185 at V_{GS} = 10 V	45	38 nC

FEATURES

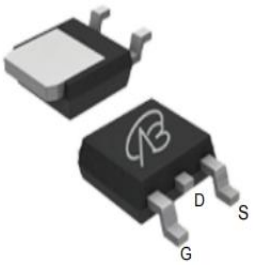
- Trench Power MOSFET
- 100 % R_g and UIS Tested

RoHS COMPLIANT

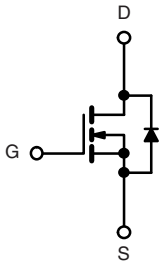
APPLICATIONS

- Primary Side Switch
- Isolated DC/DC Converter

TO-252



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)				
Parameter		**Symbol**	**Limit**	**Unit**
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	45[a]	A
	T_C = 100 °C		30	
	T_A = 25 °C		9.2[b]	
	T_A = 100 °C		6.8[b]	
Pulsed Drain Current		I_{DM}	140	
Continuous Source-Drain Diode Current	T_C = 25 °C	I_S	45[a]	
	T_A = 25 °C		2[b]	
Single Pulse Avalanche Current	L = 0.1 mH	I_{AS}	35	
Avalanche Energy		E_{AS}	101	mJ
Maximum Power Dissipation	T_C = 25 °C	P_D	136.4	W
	T_C = 100 °C		68.2	
	T_A = 25 °C		3[b]	
	T_A = 100 °C		1.5[b]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		**Symbol**	**Typical**	**Maximum**	**Unit**
Maximum Junction-to-Ambient[b]	Steady State	R_{thJA}	40	50	°C/W
Maximum Junction-to-Case		R_{thJC}	0.85	1.1	

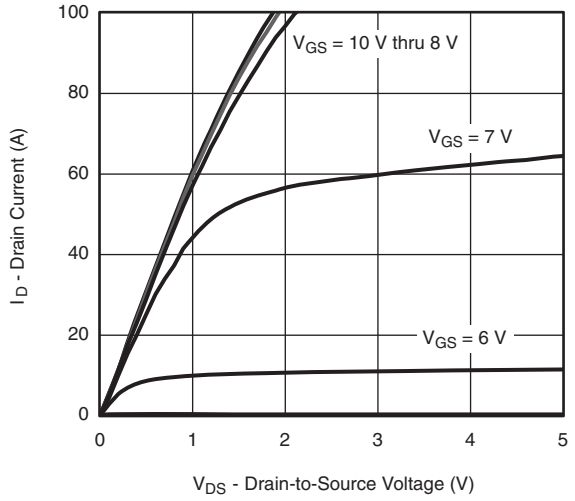
Notes:
a. Package limited.
b. Surface mounted on 1" x 1" FR4 board.

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	100			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA		110		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 12.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	2.5		5	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 100 V, V_{GS} = 0 V			1	µA
		V_{DS} = 100 V, V_{GS} = 0 V, T_J = 125 °C			50	
On-State Drain Current[a]	$I_{D(on)}$	$V_{DS} \geq$ 5 V, V_{GS} = 10 V	30			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 15 A		0.0185		Ω
Forward Transconductance[a]	g_{fs}	V_{DS} = 15 V, I_D = 15 A		33		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = 50 V, V_{GS} = 0 V, f = 1 MHz		2400		pF
Output Capacitance	C_{oss}			230		
Reverse Transfer Capacitance	C_{rss}			80		
Total Gate Charge	Q_g	V_{DS} = 50 V, V_{GS} = 10 V, I_D = 50 A		38	70	nC
Gate-Source Charge	Q_{gs}			14		
Gate-Drain Charge	Q_{gd}			12		
Gate Resistance	R_g	f = 1 MHz		1.6	2.5	Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 50 V, R_L = 1 Ω $I_D \cong$ 50 A, V_{GEN} = 10 V, R_g = 1 Ω		12	20	ns
Rise Time	t_r			10	20	
Turn-Off Delay Time	$t_{d(off)}$			18	35	
Fall Time	t_f			8	15	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode	I_S	T_C = 25 °C			35	A
Pulse Diode Forward Current[a]	I_{SM}				100	
Body Diode Voltage	V_{SD}	I_S = 15 A		0.85	1.5	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 50 A, dI/dt = 100 A/µs, T_J = 25 °C		80	120	ns
Body Diode Reverse Recovery Charge	Q_{rr}			160	240	nC
Reverse Recovery Fall Time	t_a			57		ns
Reverse Recovery Rise Time	t_b			23		

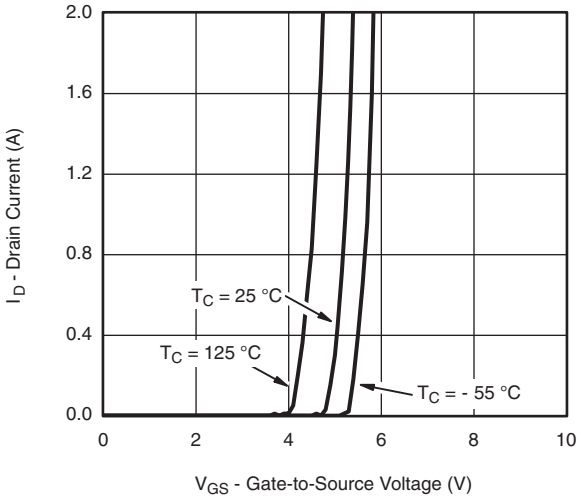
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

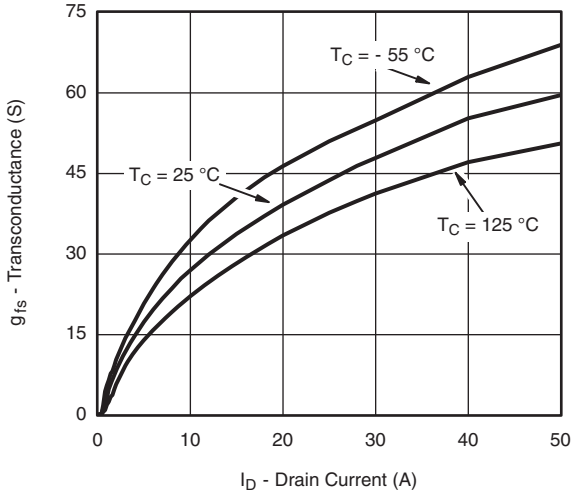
TYPICAL CHARACTERISTICS (25 °C, unless otherwise note)



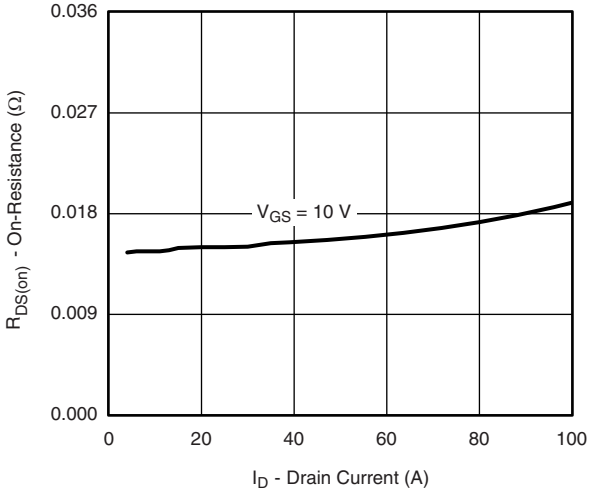
Output Characteristics



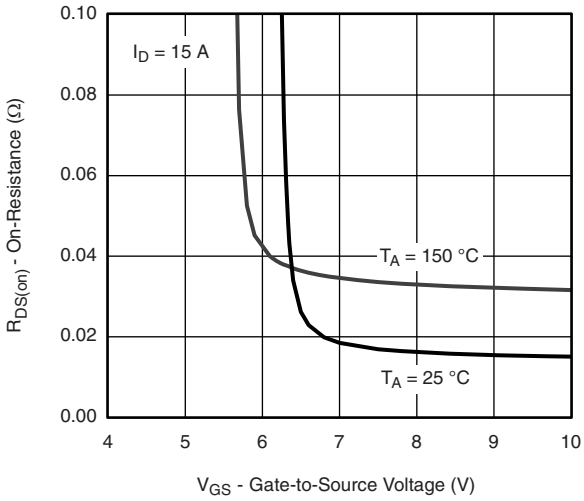
Transfer Characteristics



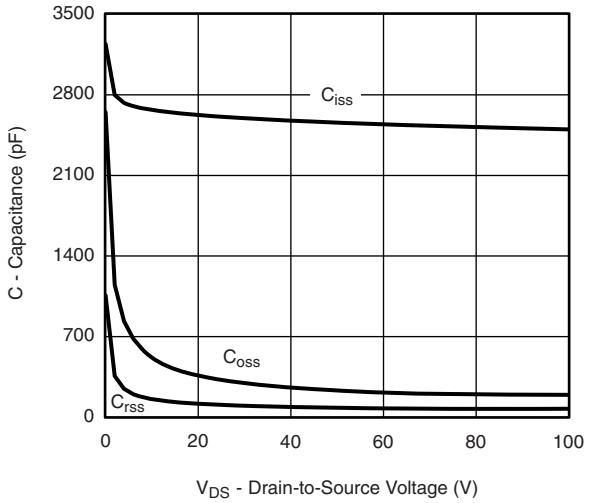
Transconductance



On-Resistance vs. Drain Current

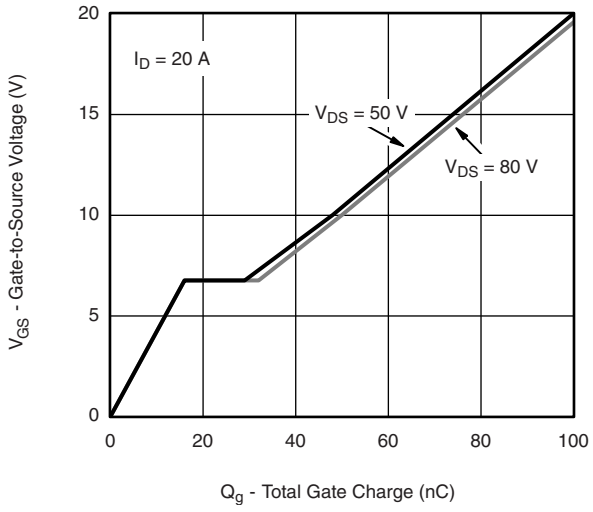


On-Resistance vs. Gate-to-Source Voltage

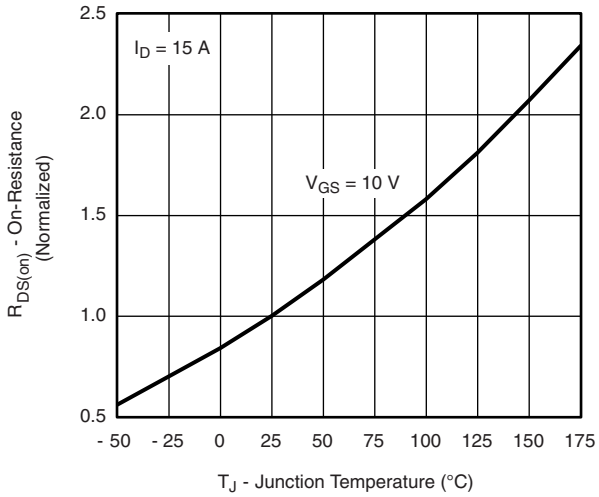


Capacitance

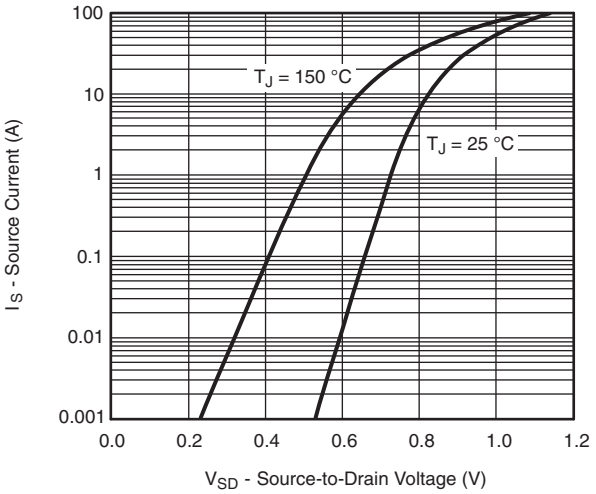
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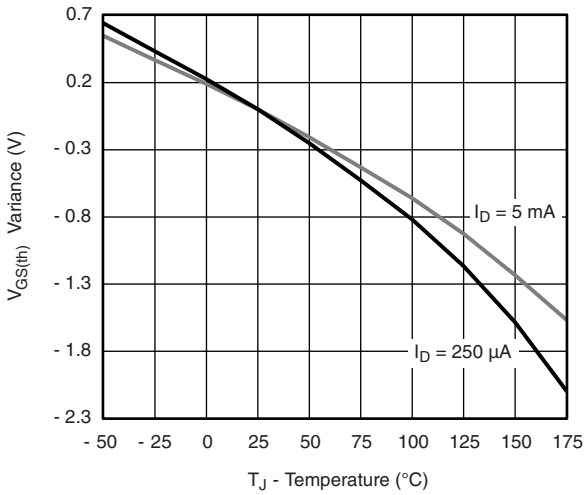
Gate Charge



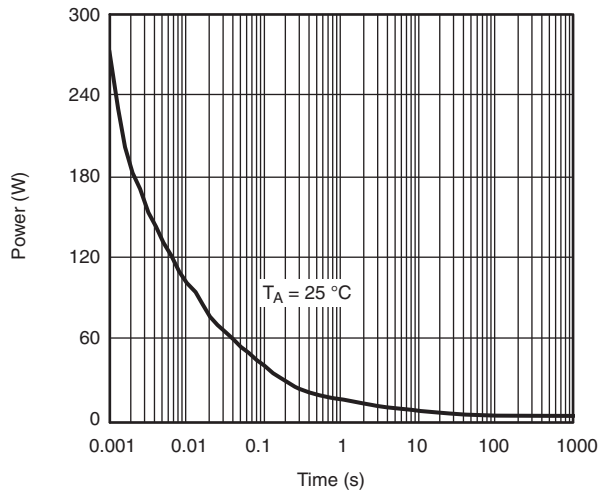
On-Resistance vs. Junction Temperature



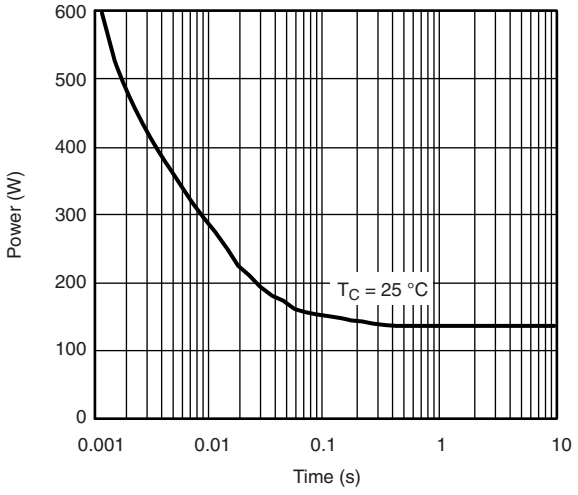
Source-Drain Diode Forward Voltage



Threshold Voltage

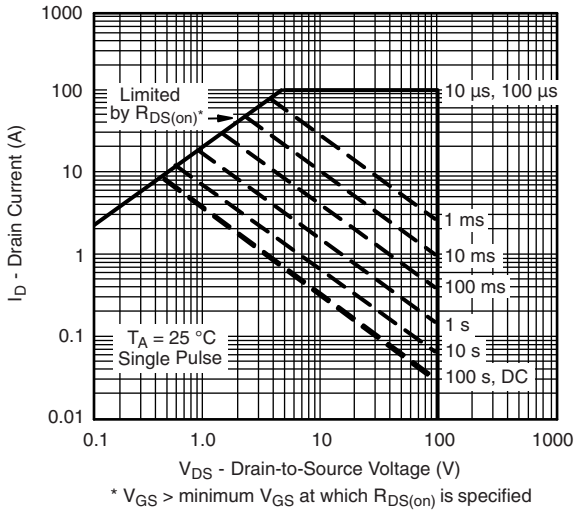


Single Pulse Power, Junction-to-Ambient

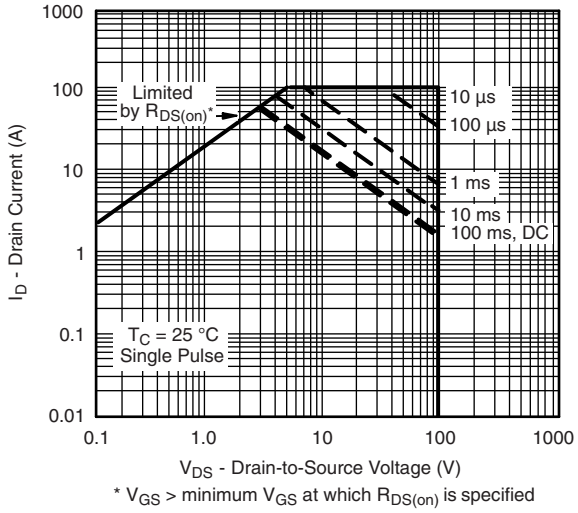


Single Pulse Power, Junction-to-Case

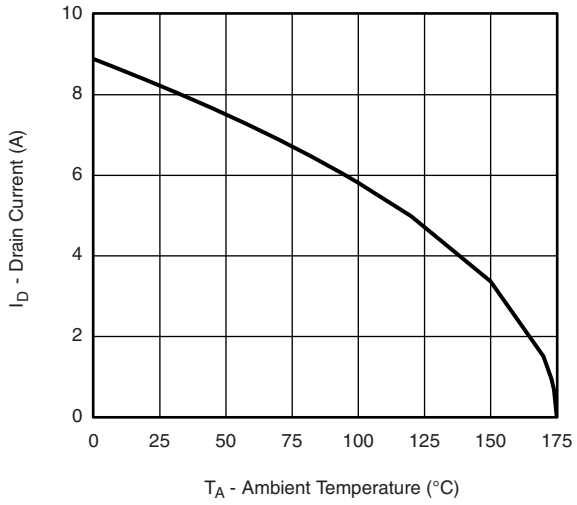
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



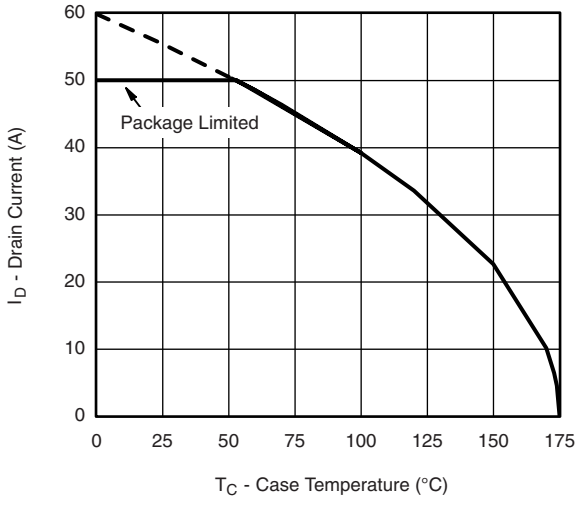
Safe Operating Area, Junction-to-Ambient



Safe Operating Area, Junction-to-Case



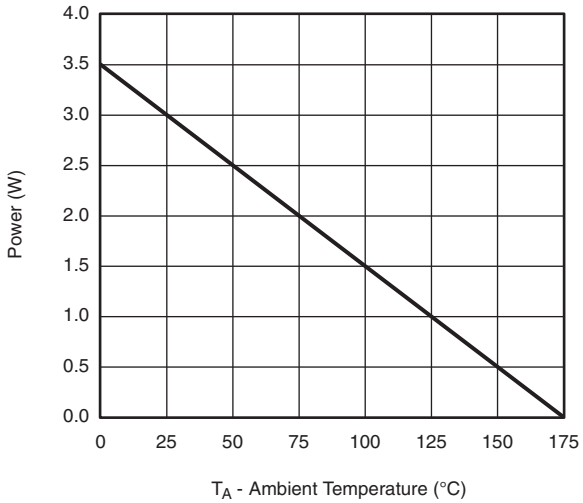
Current Derating, Junction-to-Ambient**



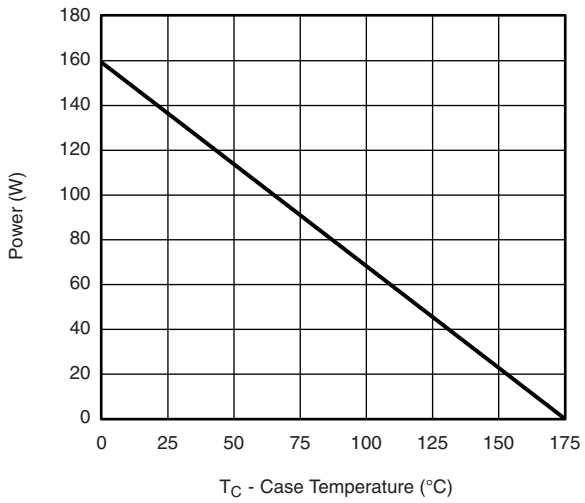
Current Derating, Junction-to-Case**

** The power dissipation P_D is based on $T_{J(max.)}$ = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



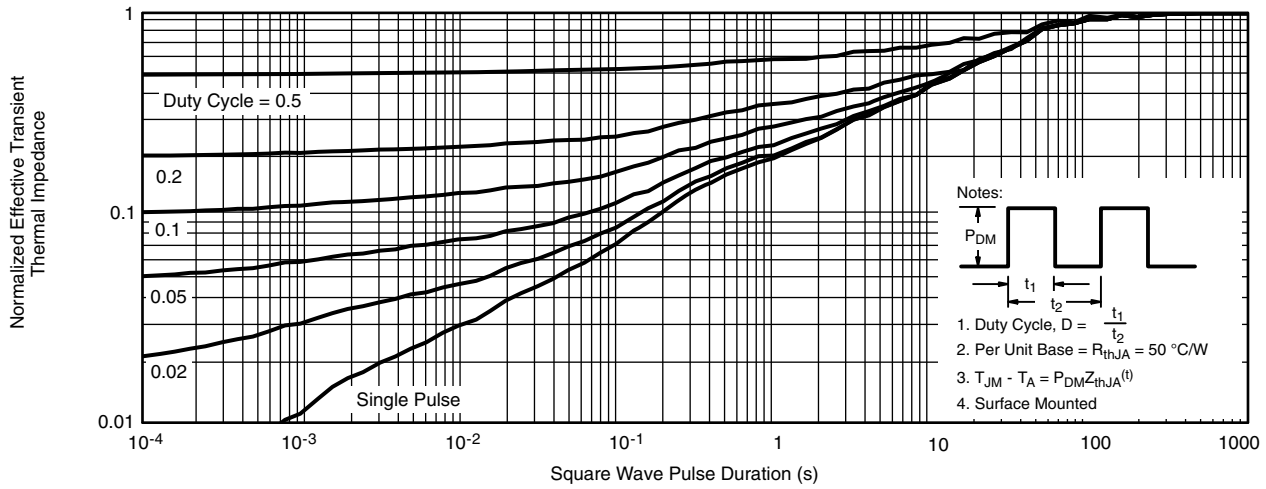
Power Derating, Junction-to-Ambient**



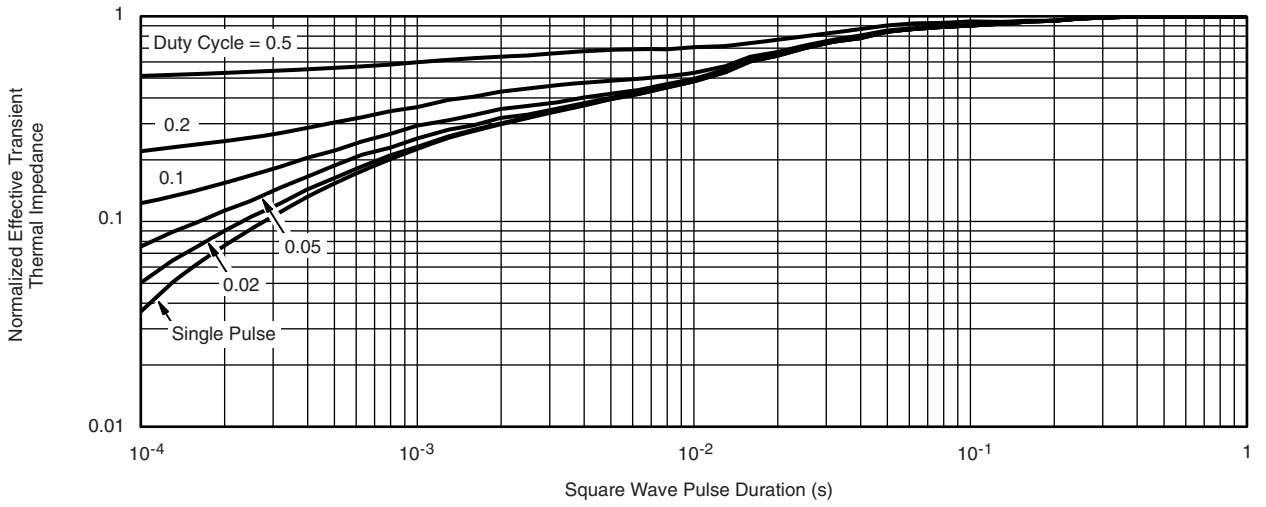
Power Derating, Junction-to-Case**

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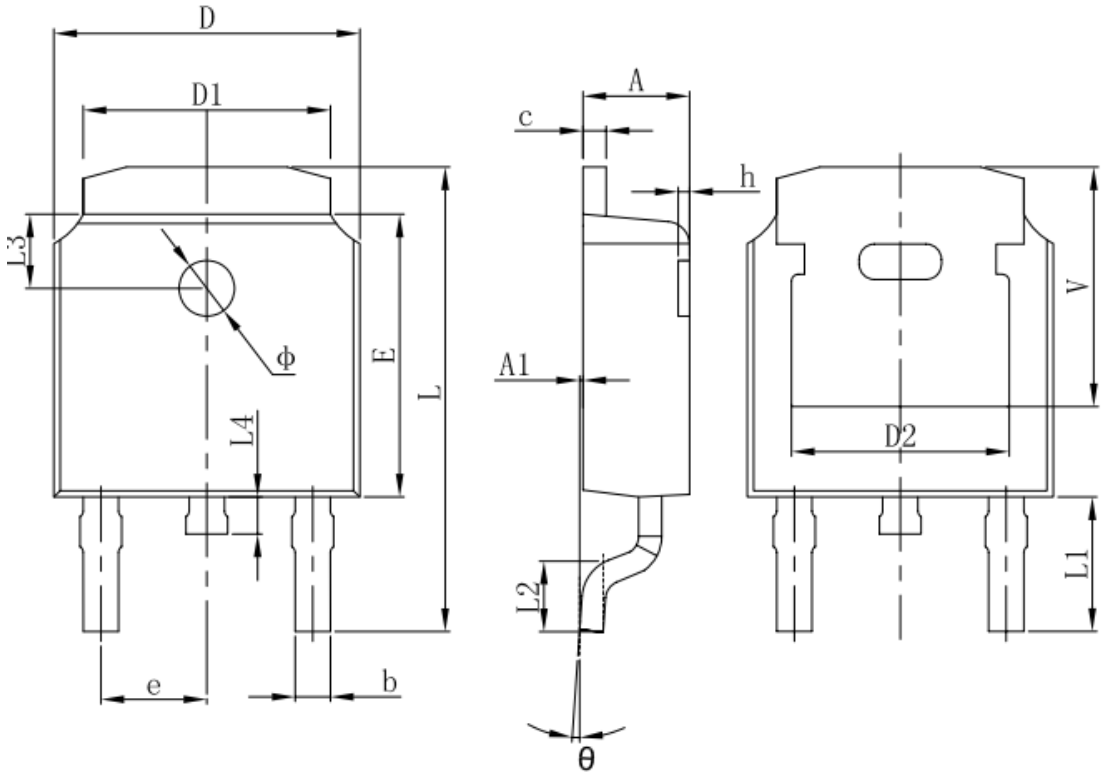


Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

TO252 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 REF.		0.190 REF.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900 REF.		0.114 REF.	
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063 REF.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250 REF.		0.207 REF.	

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