PRODUCT DATA SHEET

To learn more about JGSEMI, please visit our website at

Datasheet Resources Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.

General Description

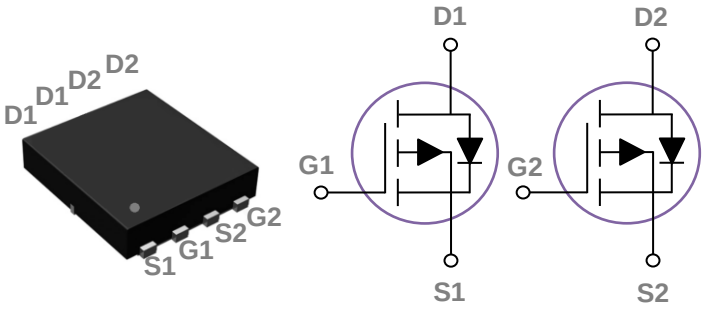
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-30V	15mΩ	-20A

Features

- *-30V, RDS(ON) =15mΩ@VGS = -10V*
- *Fast switching*
- *Green Device Available*
- *Suit for -4.5V Gate Drive Applications*

PDFN3*3 Dual Pin Configuration



Applications

- *MB / VGA / Vcore*
- *POL Applications*
- *Load Switch*
- *LED Application*

Absolute Maximum Ratings Tc=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Drain Current – Continuous (T_C=25℃)	-20	A
	Drain Current – Continuous (T_C=100℃)	-14	A
I_{DM}	Drain Current – Pulsed[1]	-80	A
P_D	Power Dissipation (T_C=25℃)	20	W
	Power Dissipation – Derate above 25℃	0.22	W/℃
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	℃/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	4.6	℃/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-30	---	---	V
$\triangle BV_{DSS}/\triangle T_J$	BV_{DSS} Temperature Coefficient	Reference to 25℃ , I_D=-1mA	---	-0.03	---	V/℃
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-27V , V_{GS}=0V , T_J=25℃	---	---	-1	uA
		V_{DS}=-24V , V_{GS}=0V , T_J=125℃	---	---	-10	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=-10V , I_D=-8A	---	15	20	mΩ
		V_{GS}=-4.5V , I_D=-6A	---	20	30	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-1.0	-1.6	-2.5	V
$\triangle V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	4	---	mV/℃
gfs	Forward Transconductance	V_{DS}=-10V , I_D=-8A	---	6.8	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge[2,3]	V_{DS}=-15V , V_{GS}=-4.5V , I_D=-5A	---	11	---	nC
Q_{gs}	Gate-Source Charge[2,3]		---	3.4	---	
Q_{gd}	Gate-Drain Charge[2,3]		---	4.2	---	
$T_{d(on)}$	Turn-On Delay Time[2,3]	V_{DD}=-15V , V_{GS}=-10V , R_G=6Ω I_D=-1A	---	5.8	---	ns
T_r	Rise Time[2,3]		---	18.8	---	
$T_{d(off)}$	Turn-Off Delay Time[2,3]		---	46.9	---	
T_f	Fall Time[2,3]		---	12.3	---	
C_{iss}	Input Capacitance	V_{DS}=-15V , V_{GS}=0V , F=1MHz	---	1250	---	pF
C_{oss}	Output Capacitance		---	160	---	
C_{rss}	Reverse Transfer Capacitance		---	90	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	-20	A
I_{SM}	Pulsed Source Current		---	---	-40	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=-1A , T_J=25℃	---	---	-1.2	V

Note :
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%.
3. Essentially independent of operating temperature.

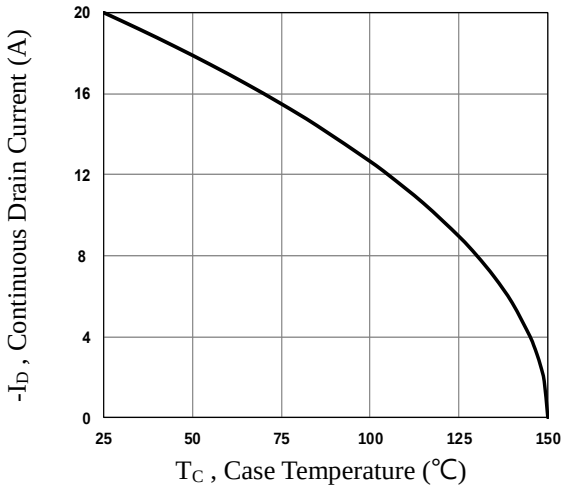


Fig.1 Continuous Drain Current vs. T_C

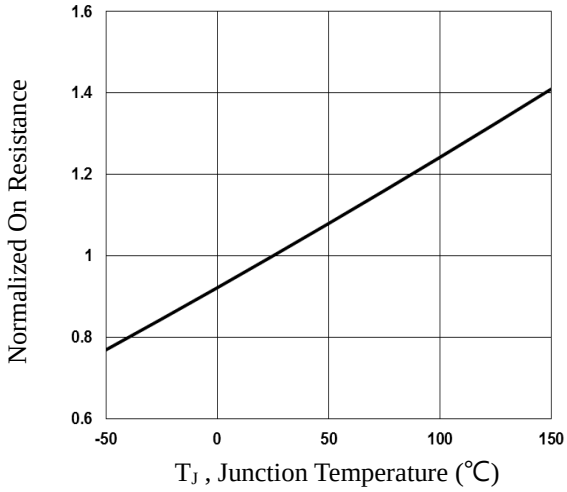


Fig.2 Normalized RDSON vs. T_J

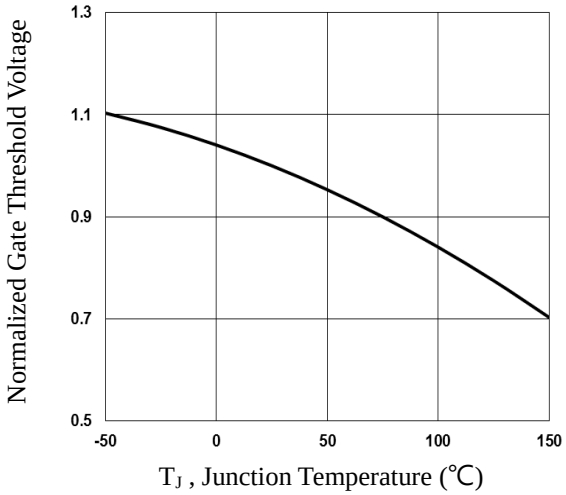


Fig.3 Normalized V_{th} vs. T_J

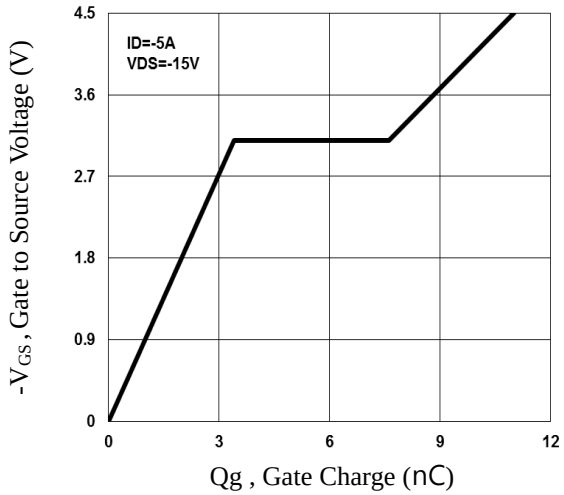


Fig.4 Gate Charge Waveform

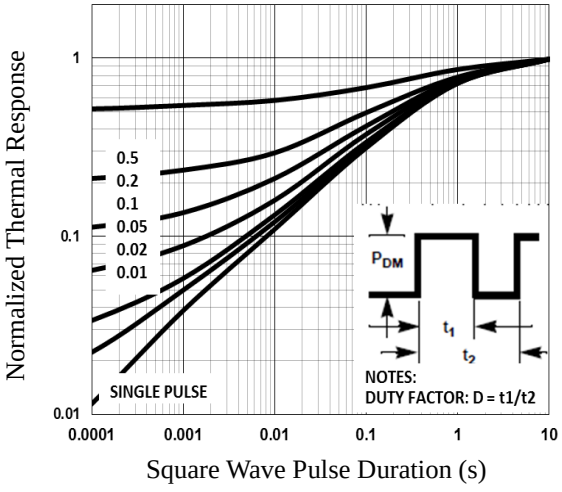


Fig.5 Normalized Transient Impedance

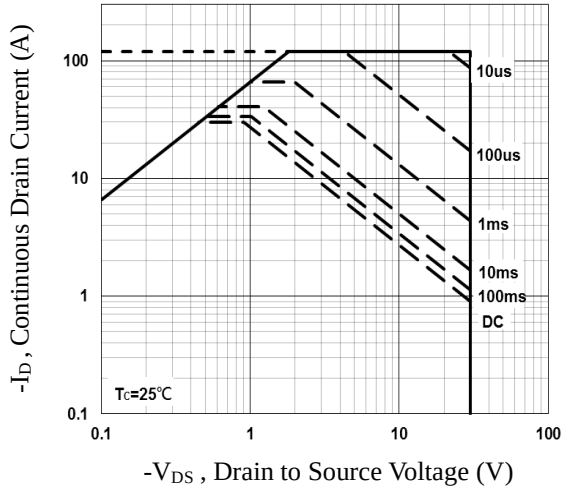


Fig.6 Maximum Safe Operation Area

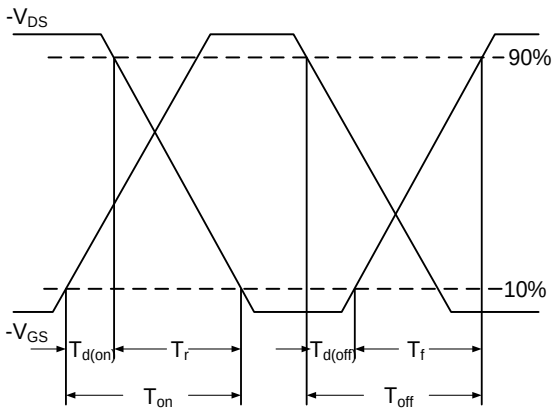


Fig.7 Switching Time Waveform

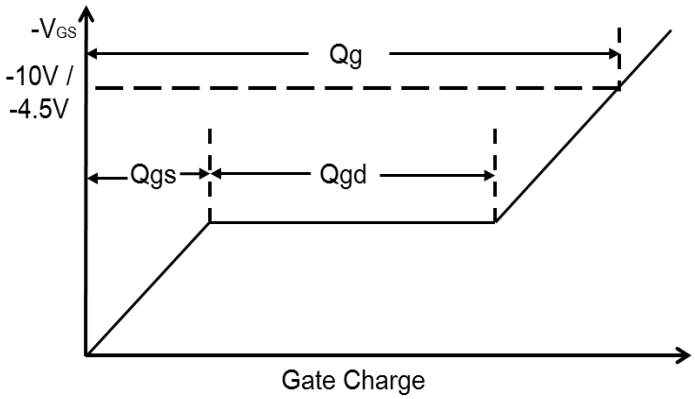
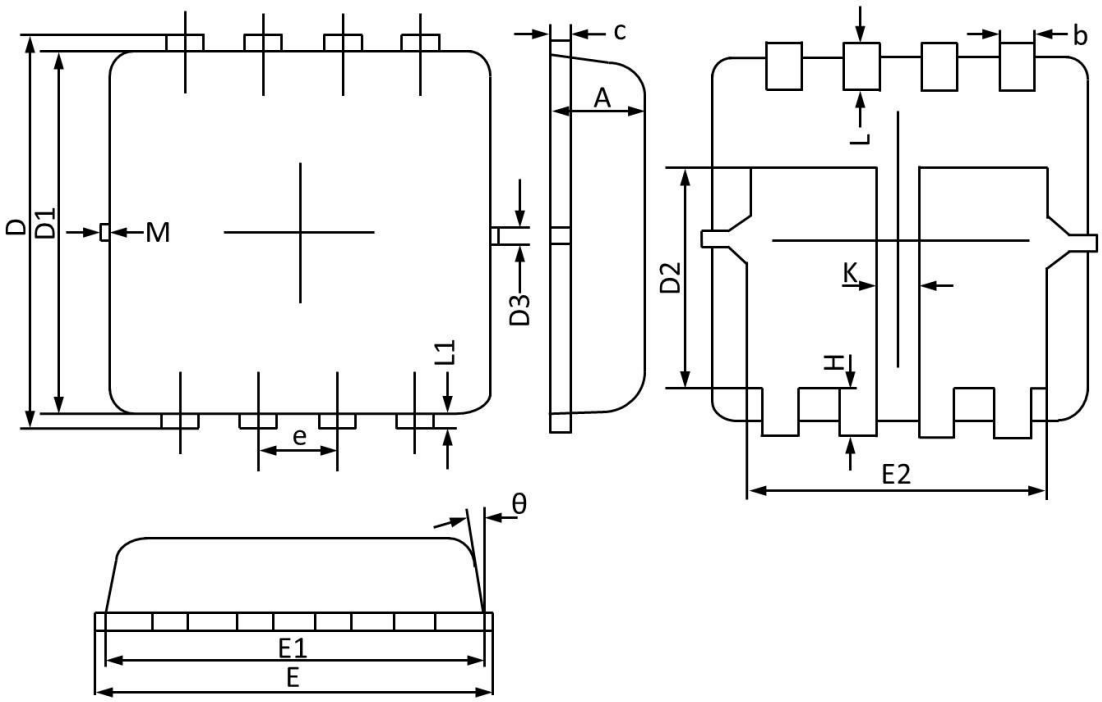


Fig.8 Gate Charge Waveform

PDFN3x3 Dual PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.670	0.880	0.026	0.035
b	0.250	0.350	0.010	0.014
c	0.100	0.250	0.004	0.010
D	3.150	3.550	0.124	0.140
D1	3.000	3.300	0.118	0.130
D2	1.500	2.000	0.059	0.079
D3	0.130	0.200	0.005	0.008
E	3.100	3.500	0.122	0.138
E1	3.000	3.200	0.118	0.126
E2	2.350	2.600	0.093	0.102
e	0.650 BSC		0.026 BSC	
H	0.300	0.500	0.012	0.020
L	0.300	0.500	0.012	0.020
L1	0.130 REF		0.005 REF	
K	0.300 REF		0.012 REF	
θ	0°	12°	0°	12°
M	0.150 REF		0.006 REF	

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