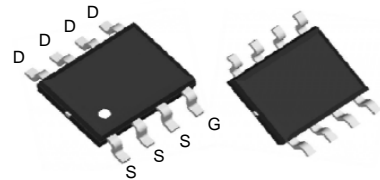


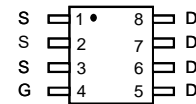
Features

- -30V, -5.1A
 $R_{DS(ON)} < 44m\Omega @ V_{GS} = -10V$
 $R_{DS(ON)} < 61m\Omega @ V_{GS} = -4.5V$
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free

SOP-8

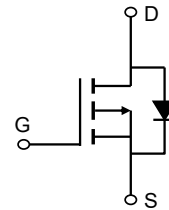


Top View



Applications

- Load Switch
- PWM Application
- Power Management



Absolute Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units
V_{DS}	Drain-to-Source Voltage	-30	V
V_{GS}	Gate-to-Source Voltage	± 20	V
I_D	Continuous Drain Current	$T_A = 25^\circ\text{C}$	-5.1
		$T_A = 100^\circ\text{C}$	-3.2
I_{DM}	Pulsed Drain Current ⁽¹⁾	-20	A
E_{AS}	Single Pulsed Avalanche Energy ⁽²⁾	12	mJ
P_D	Power Dissipation	$T_A = 25^\circ\text{C}$	1.4
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ⁽³⁾	89	$^\circ\text{C/W}$
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = -250\mu\text{A}, V_{GS} = 0\text{V}$	-30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -30\text{V}, V_{GS} = 0\text{V}$	-	-	-1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-1.0	-1.6	-2.5	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽⁴⁾	$V_{GS} = -10\text{V}, I_D = -5\text{A}$	-	35	44	m Ω
		$V_{GS} = -4.5\text{V}, I_D = -4\text{A}$	-	46	61	m Ω
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = -15\text{V}, f = 1\text{MHz}$	-	486	-	pF
C_{oss}	Output Capacitance		-	69	-	pF
C_{riss}	Reverse Transfer Capacitance		-	58	-	pF
Q_g	Total Gate Charge	$V_{GS} = 0 \text{ to } -10\text{V}$ $V_{DS} = -15\text{V}, I_D = -2\text{A}$	-	11	-	nC
Q_{gs}	Gate Source Charge		-	2	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	2	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = -10\text{V}, V_{DD} = -15\text{V}$ $I_D = -2\text{A}, R_{GEN} = 3\Omega$	-	3	-	ns
t_r	Turn-On Rise Time		-	2	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	26	-	ns
t_f	Turn-Off Fall Time		-	15	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-5.1	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-20	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = -5.1\text{A}$	-	-	-1.2	V
t_{rr}	Body Diode Reverse Recovery Time	$I_F = -2\text{A}, di/dt = 100\text{A/us}$	-	9	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	3	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. EAS condition: Starting $T_J = 25^\circ\text{C}$, $V_{DD} = -15\text{V}$, $V_G = -10\text{V}$, $R_G = 25\text{ohm}$, $L = 0.5\text{mH}$, $I_{AS} = -7\text{A}$
 3. $R_{\theta JA}$ is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB
 4. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Typical Performance Characteristics

Figure 1: Output Characteristics

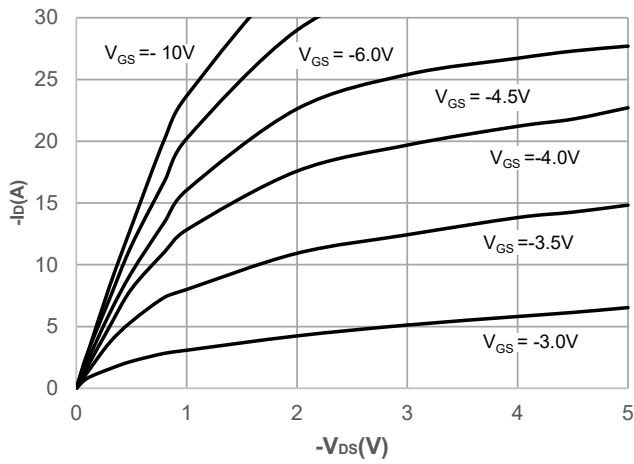


Figure 2: Typical Transfer Characteristics

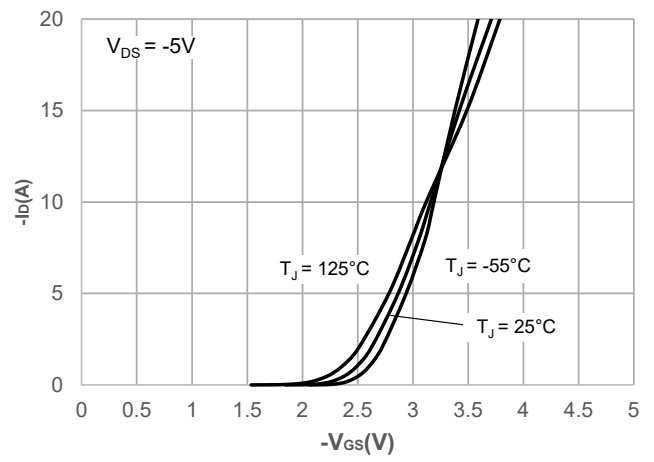


Figure 3: On-resistance vs. Drain Current

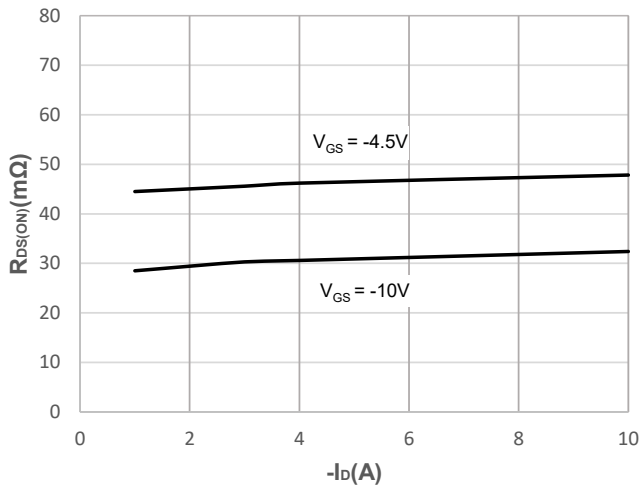


Figure 4: Body Diode Characteristics

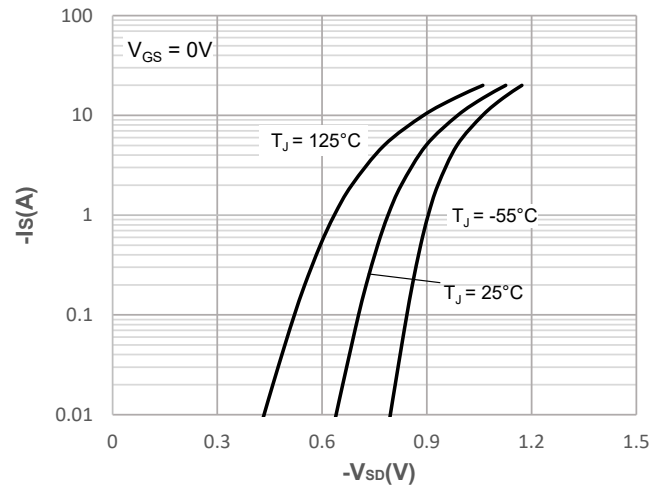


Figure 5: Gate Charge Characteristics

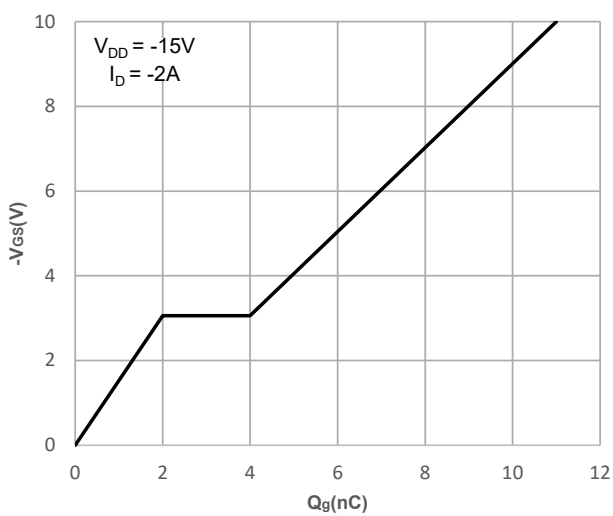
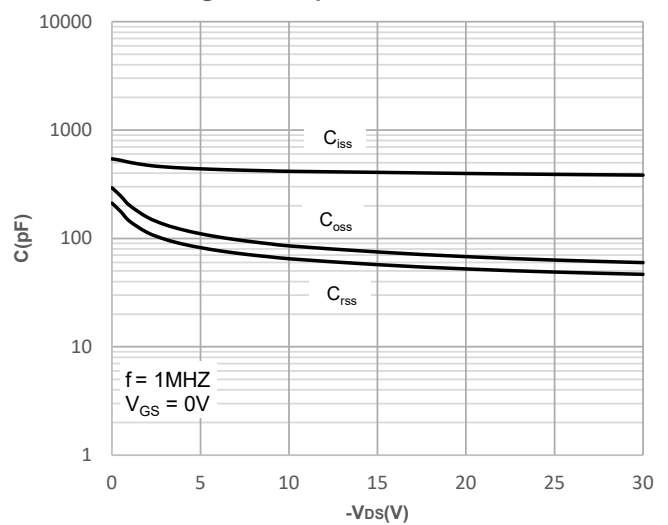


Figure 6: Capacitance Characteristics



Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

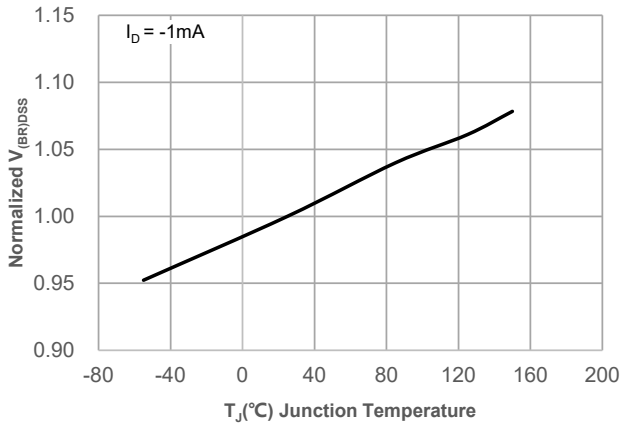


Figure 8: Normalized on Resistance vs. Junction Temperature

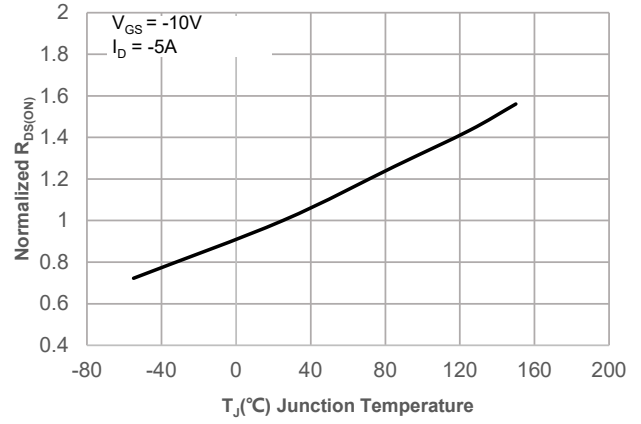


Figure 9: Maximum Safe Operating Area

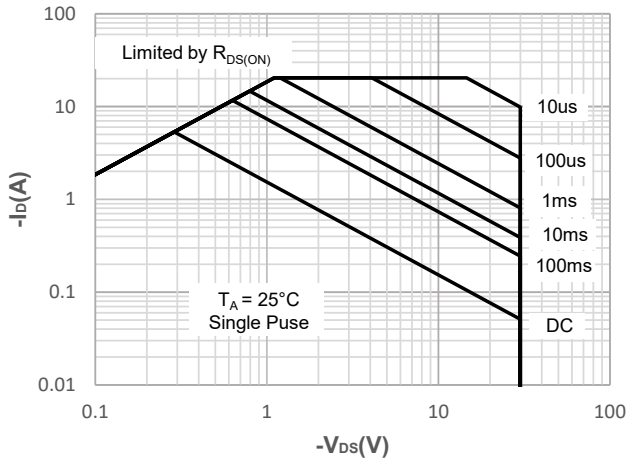


Figure 10: Maximum Continuous Driand Current vs. Ambient Temperature

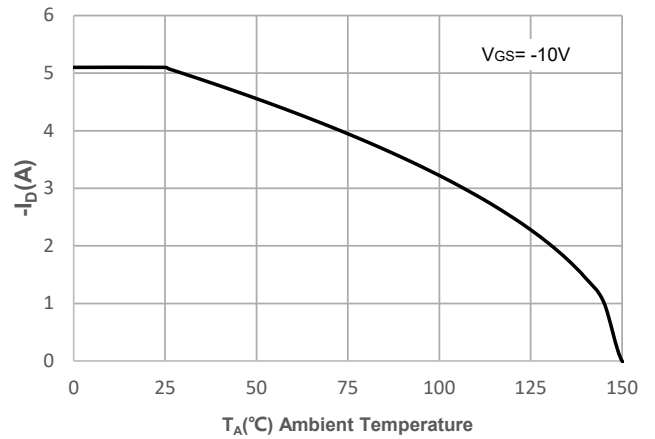


Figure 11: Normalized Maximum Transient Thermal Impedance

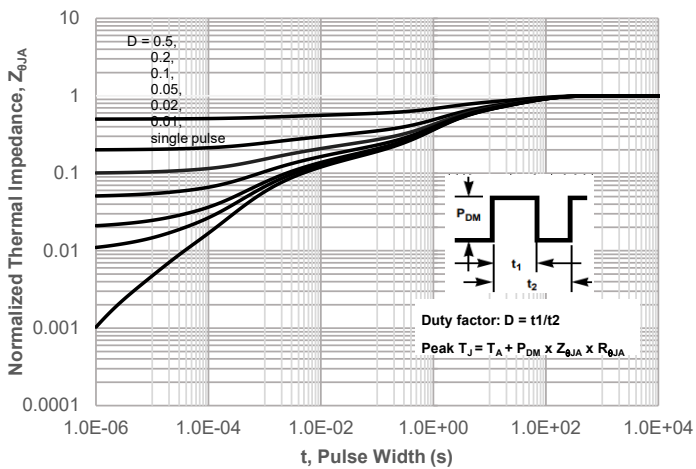
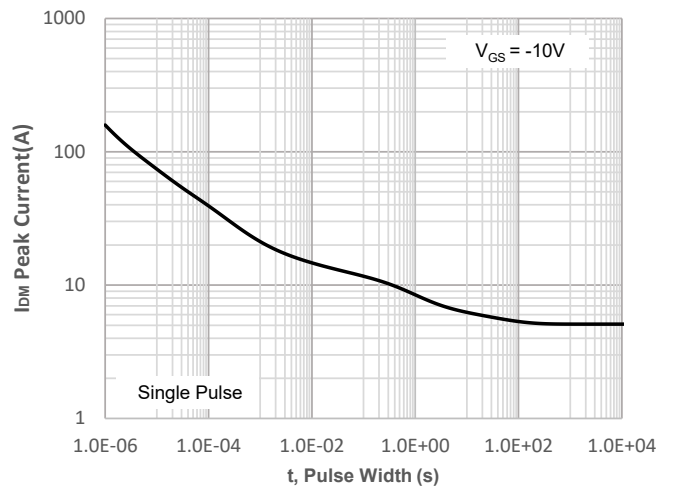


Figure 12: Peak Current Capacity



Test Circuit

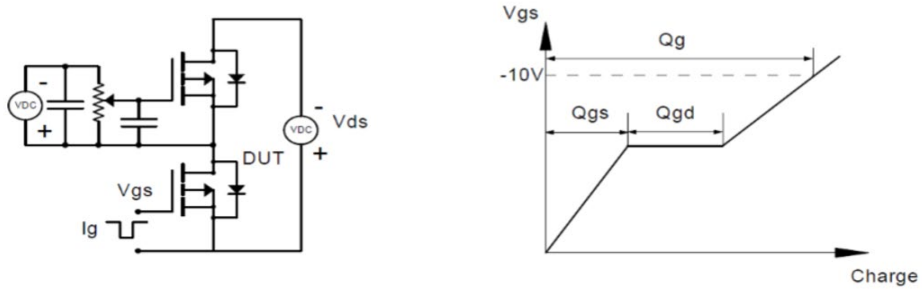


Figure 1: Gate Charge Test Circuit & Waveform

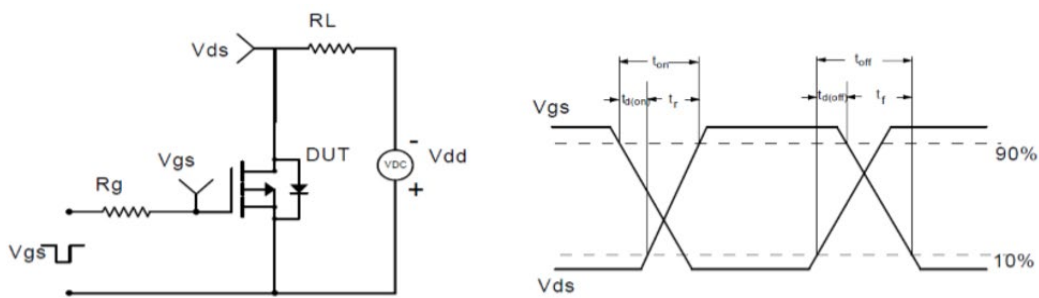


Figure 2: Resistive Switching Test Circuit & Waveform

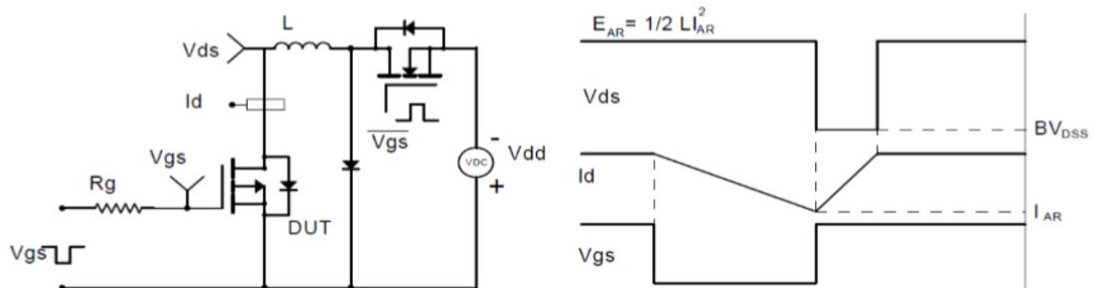


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

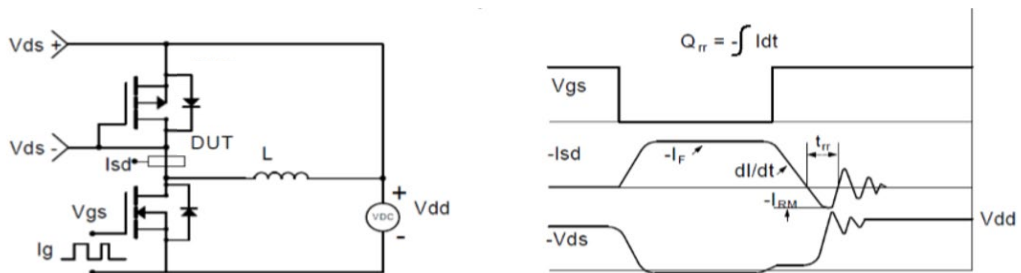
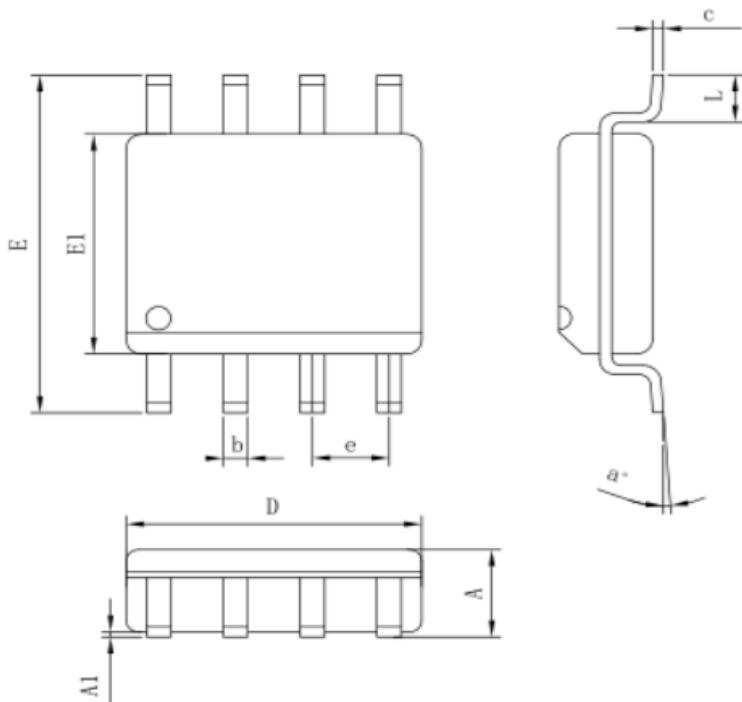


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(SOP-8)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	--	--	1.75
A1	0.10	--	0.23
b	0.35	--	0.48
c	0.19	--	0.25
D	4.70	4.90	5.00
E	5.80	6.00	6.20
E1	3.70	3.90	4.10
e	1.27BSC		
L	0.50	--	0.80
q*	0°	--	8°