

SPB18P06PG-VB Datasheet P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ)		
- 60	0.064 at V _{GS} = - 10 V	- 30	12		
	0.077 at V _{GS} = - 4.5 V	- 28	12		

FEATURES

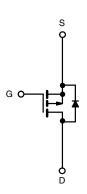
- Trench Power MOSFET 100
- % UIS Tested

APPLICATIONS

Load Switch







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted					
Parameter	Symbol	Limit	Unit		
Gate-Source Voltage	V _{GS}	± 20	V		
Continuous Drain Current (T _{.I} = 175 °C)	T _C = 25 °C	- I _D	- 30		
Continuous Diam Current (1) = 175 C)	T _C = 100 °C		- 20		
Pulsed Drain Current	I _{DM}	- 90	Α		
Continuing Source Current (Diode Conduction)	I _S	- 28			
Avalanche Current	I _{AS}	- 31	1		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	7.2	mJ	
Maximum Power Dissipation	T _C = 25 °C	Pn	60 ^a	W	
iviaximum rower bissipation	T _A = 25 °C	' D	6 ^b]	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
hunding to Ambient	t ≤ 10 sec	R _{thJA}	20	25		
Junction-to-Ambient ^D	Steady State		62	75	°C/W	
Junction-to-Case		R _{thJC}	5	6		

Notes:

- a. See SOA curve for voltage derating.
- b. Surface Mounted on 1" x 1" FR-4 boad.

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Parameter	Symbol	Test Conditions	Min	Typ ^a	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V, } I_D = -250 \mu\text{A}$				V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0	- 2.0	.0 - 3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = - 60 V, V _{GS} = 0 V			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 125 °C			- 50	μΑ	
		V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 175 °C			- 150		
On-State Drain Current ^b	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 10			Α	
		V _{GS} = - 10 V, I _D = - 5 A		0.064			
	_	V _{GS} = - 10 V, I _D = - 5 A, T _J = 125 °C		0.110		Ω	
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 5 A, T _J = 175 °C		0.250			
		V _{GS} = - 4.5 V, I _D = - 2 A		0.077			
Forward Transconductance ^b	9 _{fs}	V _{DS} = - 15 V, I _D = - 5 A		8		S	
Dynamic	•			•			
Input Capacitance	C _{iss}			1000		pF	
Output Capacitance	C _{oss}	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		210			
Reverse Transfer Capacitance	C _{rss}			110			
Total Gate Charge	Q_g			12.5	19	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -8.4 \text{ A}$		2.3			
Gate-Drain Charge	Q_{gd}]		3.2			
Gate Resistance	R_{g}	f = 1 MHz	8.0			Ω	
Turn-On Delay Time ^c	t _{d(on)}			5	10		
Rise Time ^c	t _r	$V_{DD} = -30 \text{ V}, R_L = 3.57 \Omega$		14	25		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 8.4 A, $V_{GEN} =$ - 10 V, $R_G = 2.5 \Omega$		15	25	ns	
Fall Time ^c	t _f]		7	12		
Source-Drain Diode Ratings and Cha	aracteristics	(T _C = 25 °C) ^b					
Pulsed Current	I _{SM}				- 30	Α	
Forward Voltage ^b	V_{SD}	I _F = - 2 A, V _{GS} = 0 V - 0.9		- 0.9	- 1.3	V	
Reverse Recovery Time tr		I _F = - 8 A, di/dt = 100 A/μs		50	80	ns	
Reverse Recovery Time	Q _{rr}			80	120	nC	

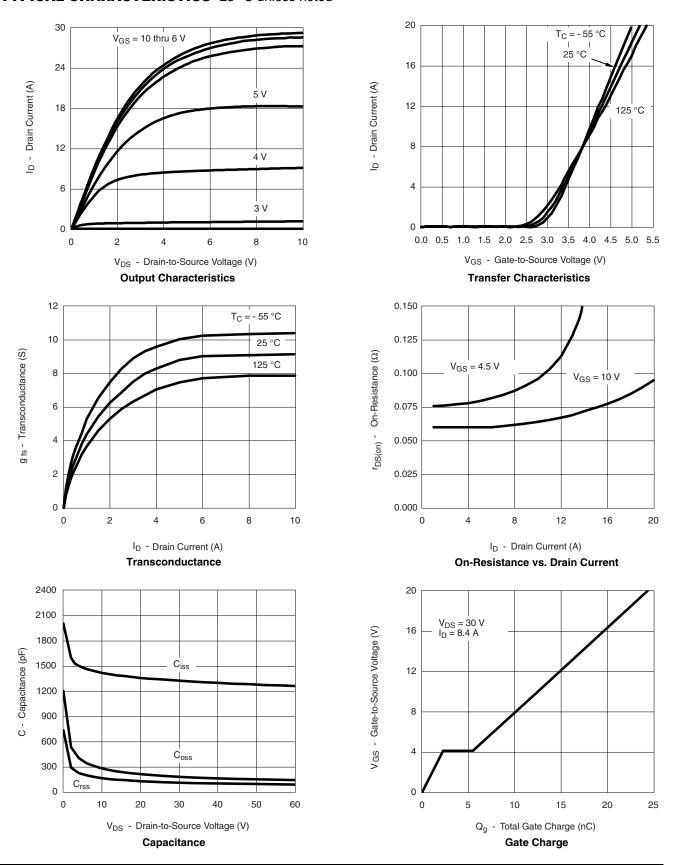
Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

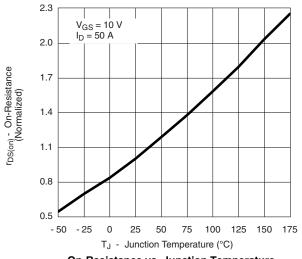


TYPICAL CHARACTERISTICS 25 °C unless noted

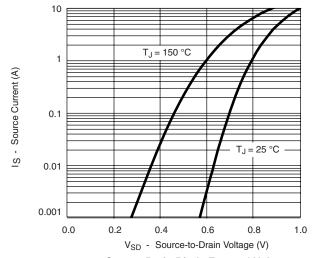




TYPICAL CHARACTERISTICS 25 °C unless noted



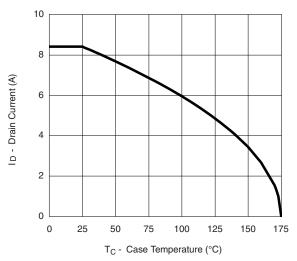




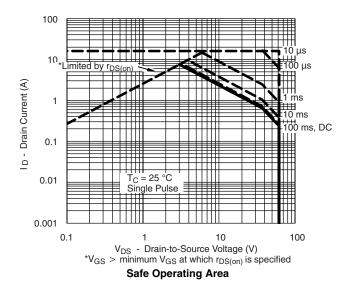
Source-Drain Diode Forward Voltage

THERMAL RATINGS

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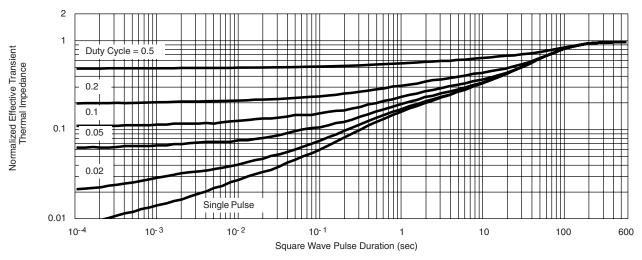
Drain Current vs. Case Temperature



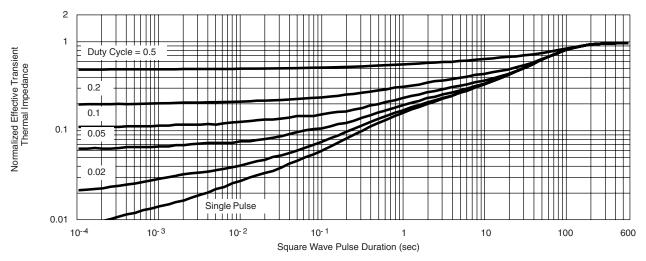
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THERMAL RATINGS



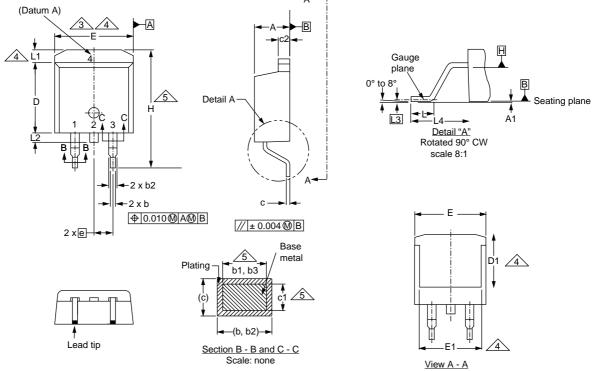
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



TO-263AB



	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
Α	4.06	4.83	0.160	0.190
A1	0.00	0.25	0.000	0.010
b	0.51	0.99	0.020	0.039
b1	0.51	0.89	0.020	0.035
b2	1.14	1.78	0.045	0.070
b3	1.14	1.73	0.045	0.068
С	0.38	0.74	0.015	0.029
c1	0.38	0.58	0.015	0.023
c2	1.14	1.65	0.045	0.065
D	8.38	9.65	0.330	0.380

	MILLIN	METERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
D1	6.86	-	0.270	-	
Е	9.65	10.67	0.380	0.420	
E1	6.22	-	0.245	-	
е	2.54 BSC		0.100 BSC		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	i	0.066	
L2	-	1.78	-	0.070	
L3	0.25 BSC 0.0°		0.010	BSC	
L4	4.78	5.28	0.188	0.208	
•	•				

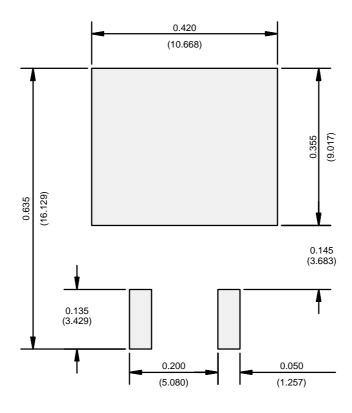
ECN: S-82110-Rev. A, 15-Sep-08 DWG: 5970

Notes

- 1. Dimensioning and tolerancing per ASME Y14.5M-1994.
- 2. Dimensions are shown in millimeters (inches).
- 3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body at datum A.
- 4. Thermal PAD contour optional within dimension E, L1, D1 and E1.
- 5. Dimension b1 and c1 apply to base metal only.
- 6. Datum A and B to be determined at datum plane H.
- 7. Outline conforms to JEDEC outline to TO-263AB.



RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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