N-Channel 80V (D-S) MOSFET

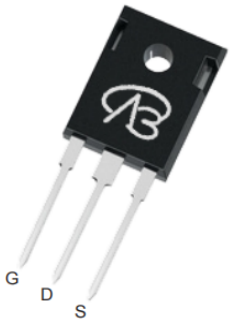
PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω) MAX.	I_D (A)	Q_g (TYP.)
80	0.0029 at V_{GS}=10V	215	115nC

FEATURES

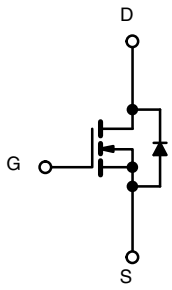
- SGT technology Power MOSFET
- Maximum 150 °C junction temperature
- 100 % R_g and UIS tested

RoHS COMPLIANT
HALOGEN FREE

TO-247



Top View



N-Channel MOSFET

APPLICATIONS

- Power supplies:
  - Uninterruptible power supplies
  - AC/DC switch-mode power supplies
  - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Solar micro inverter
- Class D audio amplifier

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	80	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	215	A
	T_C = 70 °C		129	
Pulsed Drain Current (t = 100 µs)		I_{DM}	645	
Avalanche Current	L = 0.5 mH	I_{AS}	70	
Single Avalanche Energy [a]		E_{AS}	1200	mJ
Maximum Power Dissipation [a]	T_C = 25 °C	P_D	178[b]	W
	T_C = 100 °C		71[b]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to +150	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) [c]	R_{thJA}	40	°C/W
Junction-to-Case (Drain)	R_{thJC}	0.7	

Notes

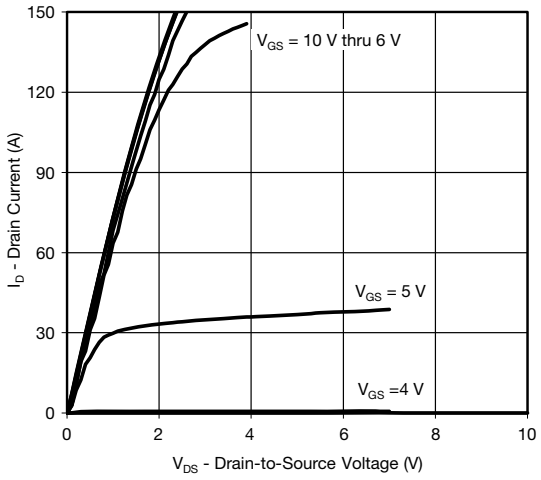
a. Duty cycle ≤ 1 %.
b. See SOA curve for voltage derating.
c. When mounted on 1" square PCB (FR4 material).

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	80	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	2.5	-	4.0	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V	-	-	± 250	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 64 V, V_{GS} = 0 V	-	-	1	µA
		V_{DS} = 64 V, V_{GS} = 0 V, T_J = 125 °C	-	-	150	
		V_{DS} = 64 V, V_{GS} = 0 V, T_J = 150°C	-	-	5	mA
On-State Drain Current [a]	$I_{D(on)}$	V_{DS} ≥ 10 V, V_{GS} = 10 V	90	-	-	A
Drain-Source On-State Resistance [a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 50 A	-	0.0029	-	Ω
		V_{GS} =7.5 V, I_D = 40 A	-	0.0032	-	
Forward Transconductance [a]	g_{fs}	V_{DS} = 15 V, I_D = 30 A	-	75	-	S
Dynamic [b]						
Input Capacitance	C_{iss}	V_{GS} = 0 V, V_{DS} = 100 V, f = 1 MHz	-	6000	-	pF
Output Capacitance	C_{oss}		-	175	-	
Reverse Transfer Capacitance	C_{rss}		-	21	-	
Total Gate Charge [c]	Q_g	V_{DS} = 80 V, V_{GS} = 10 V, I_D = 60 A	-	115	-	nC
Gate-Source Charge [c]	Q_{gs}		-	16.7	-	
Gate-Drain Charge [c]	Q_{gd}		-	16.9	-	
Gate Resistance	R_g	f = 1 MHz	1.5	3	5	Ω
Turn-On Delay Time [c]	$t_{d(on)}$	V_{DD} = 80 V, R_L = 1.66 Ω I_D ≅ 60 A, V_{GEN} = 10 V, R_g = 1 Ω	-	30	28	ns
Rise Time [c]	t_r		-	125	209	
Turn-Off Delay Time [c]	$t_{d(off)}$		-	35	70	
Fall Time [c]	t_f		-	85	155	
Drain-Source Body Diode Ratings and Characteristics [b] (T_C = 25 °C)						
Pulsed Current (t = 100 µs)	I_{SM}		-	-	645	A
Forward Voltage [a]	V_{SD}	I_F = 10 A, V_{GS} = 0 V	-	0.8	1.2	V
Reverse Recovery Time	t_{rr}	I_F = 30 A, di/dt = 100 A/µs	-	40	-	ns
Peak Reverse Recovery Charge	$I_{RM(REC)}$		-	11	20	A
Reverse Recovery Charge	Q_{rr}		-	0.9	1.8	µC

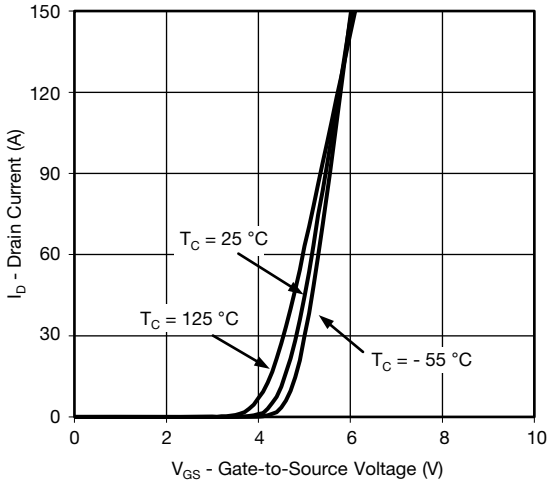
Notes

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

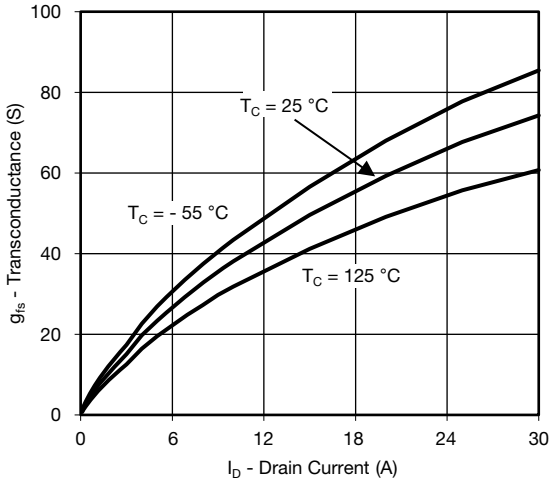
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



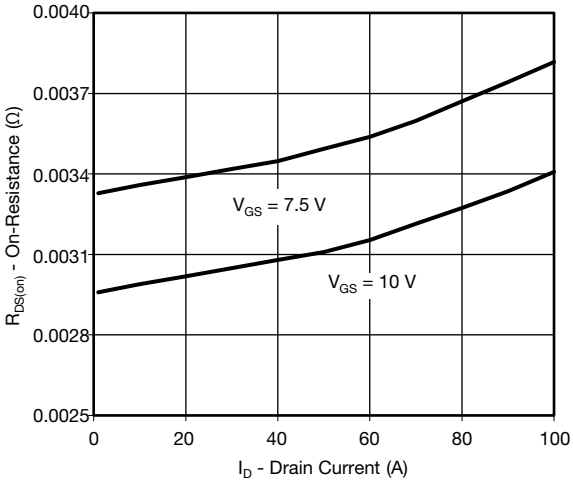
Output Characteristics



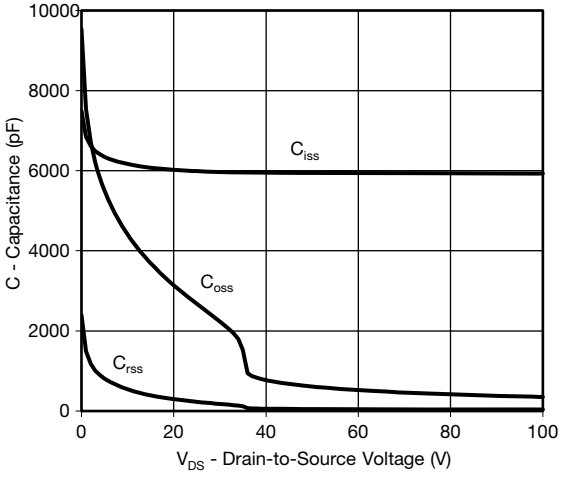
Transfer Characteristics



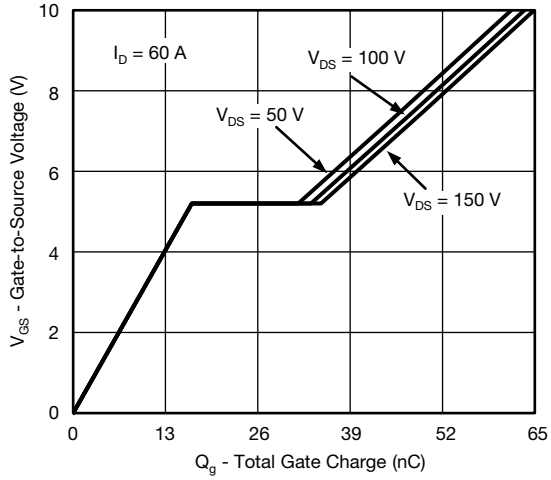
Transconductance



On-Resistance vs. Drain Current

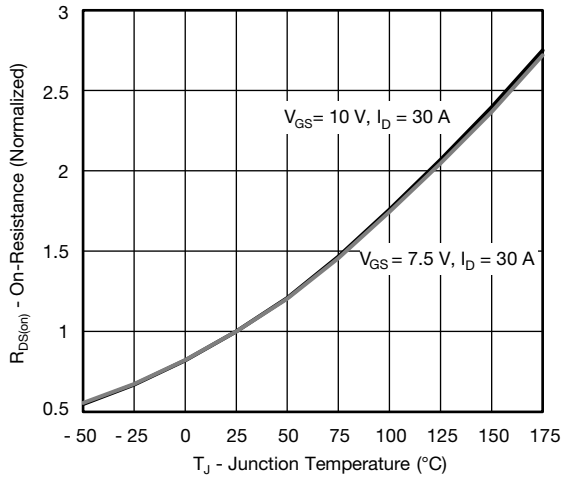


Capacitance

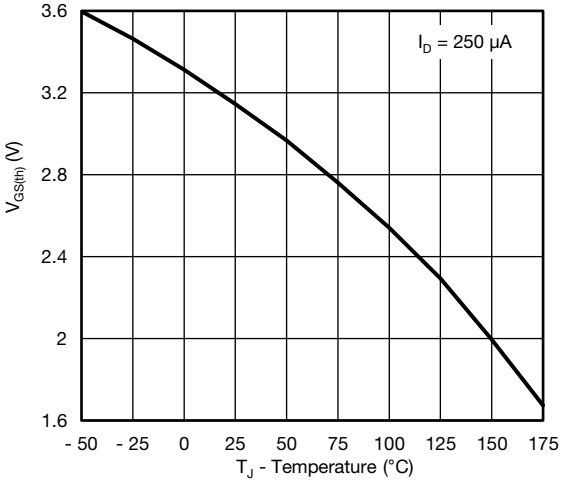


Gate Charge

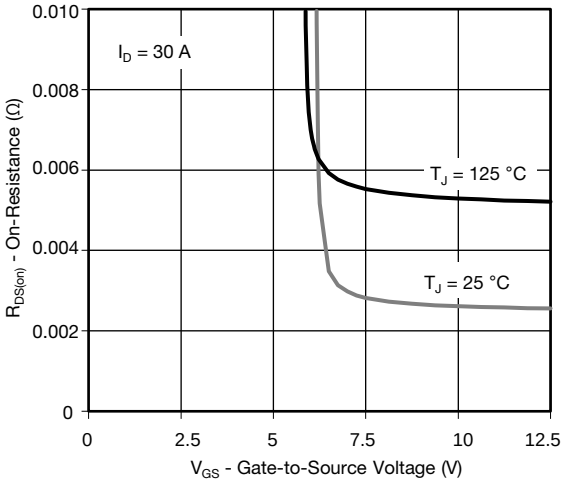
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



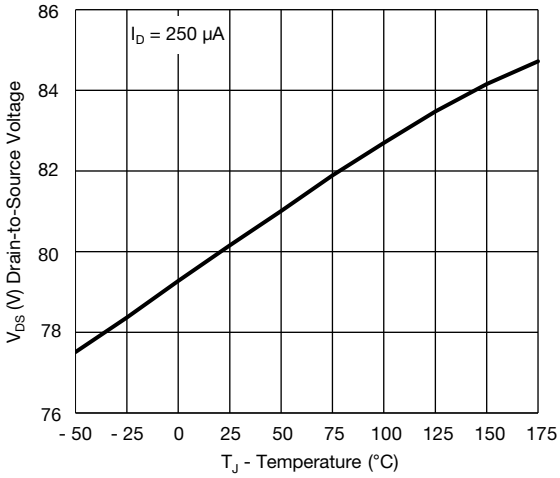
On-Resistance vs. Junction Temperature



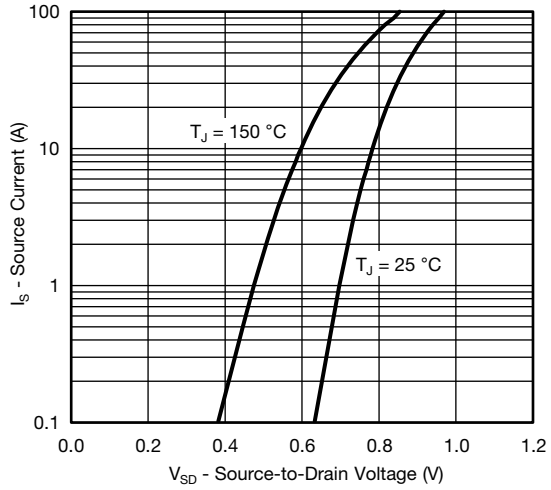
Threshold Voltage



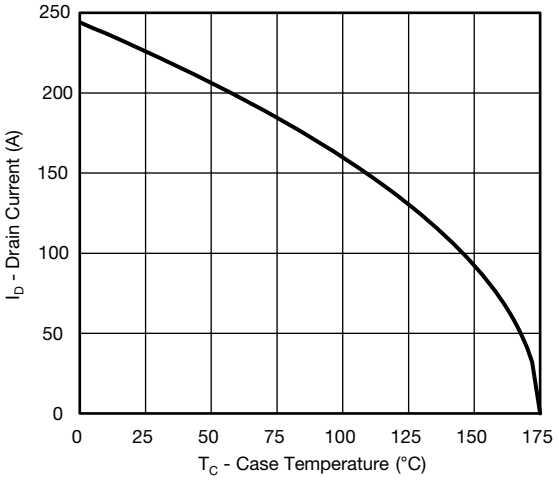
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature

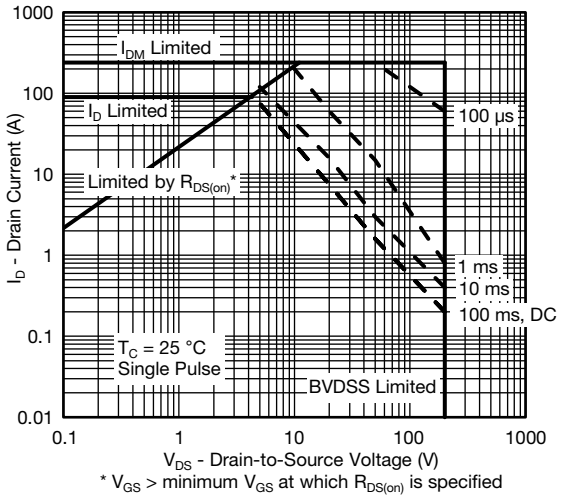


Source Drain Diode Forward Voltage

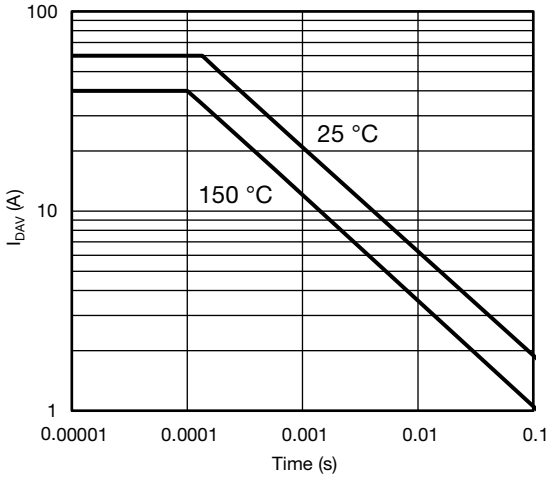


Current De-rating

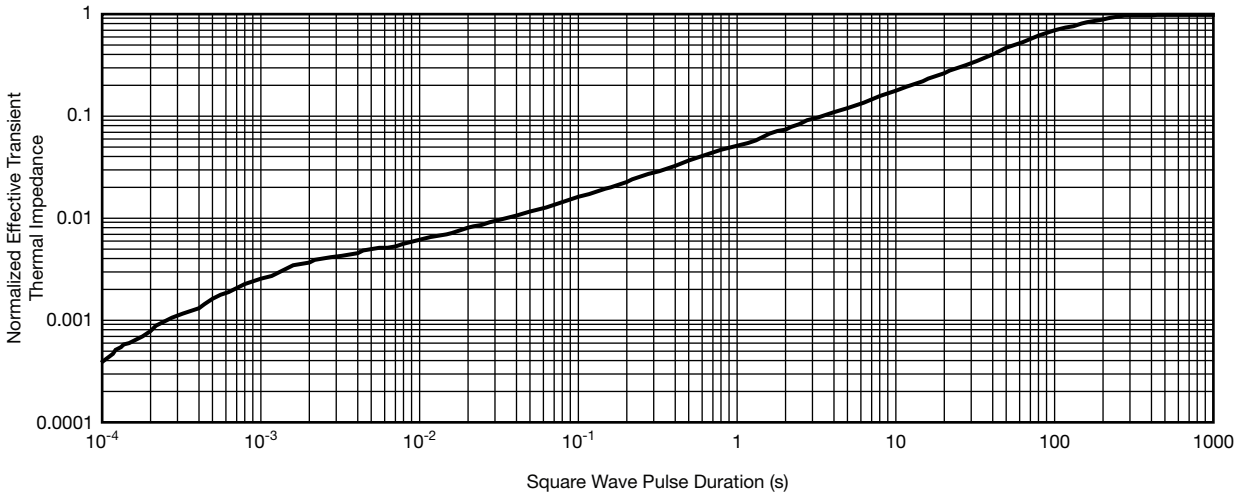
THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Safe Operating Area

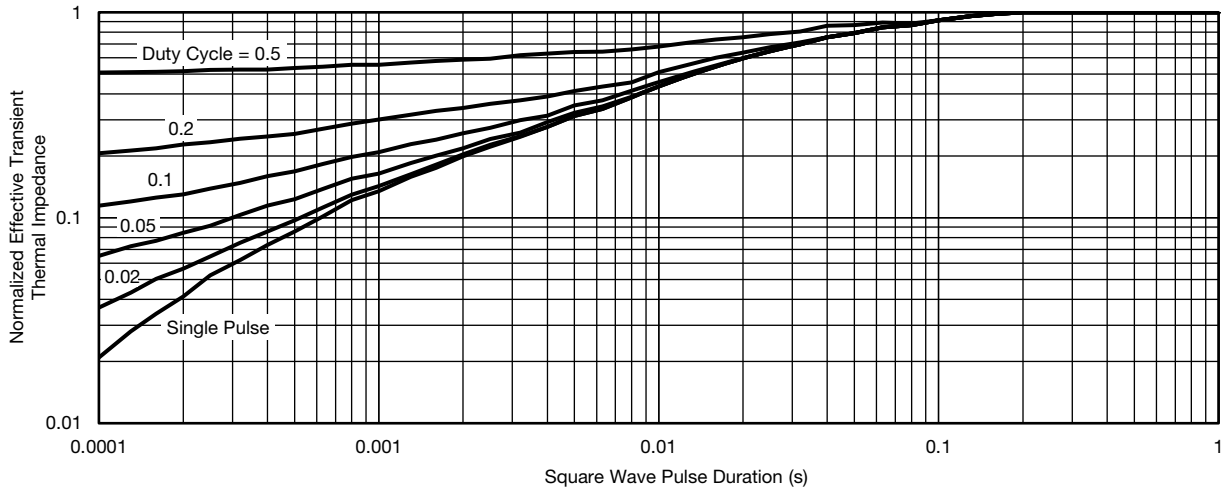


Single Pulse Avalanche Current Capability vs. Time



Normalized Thermal Transient Impedance, Junction-to-Ambient

THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



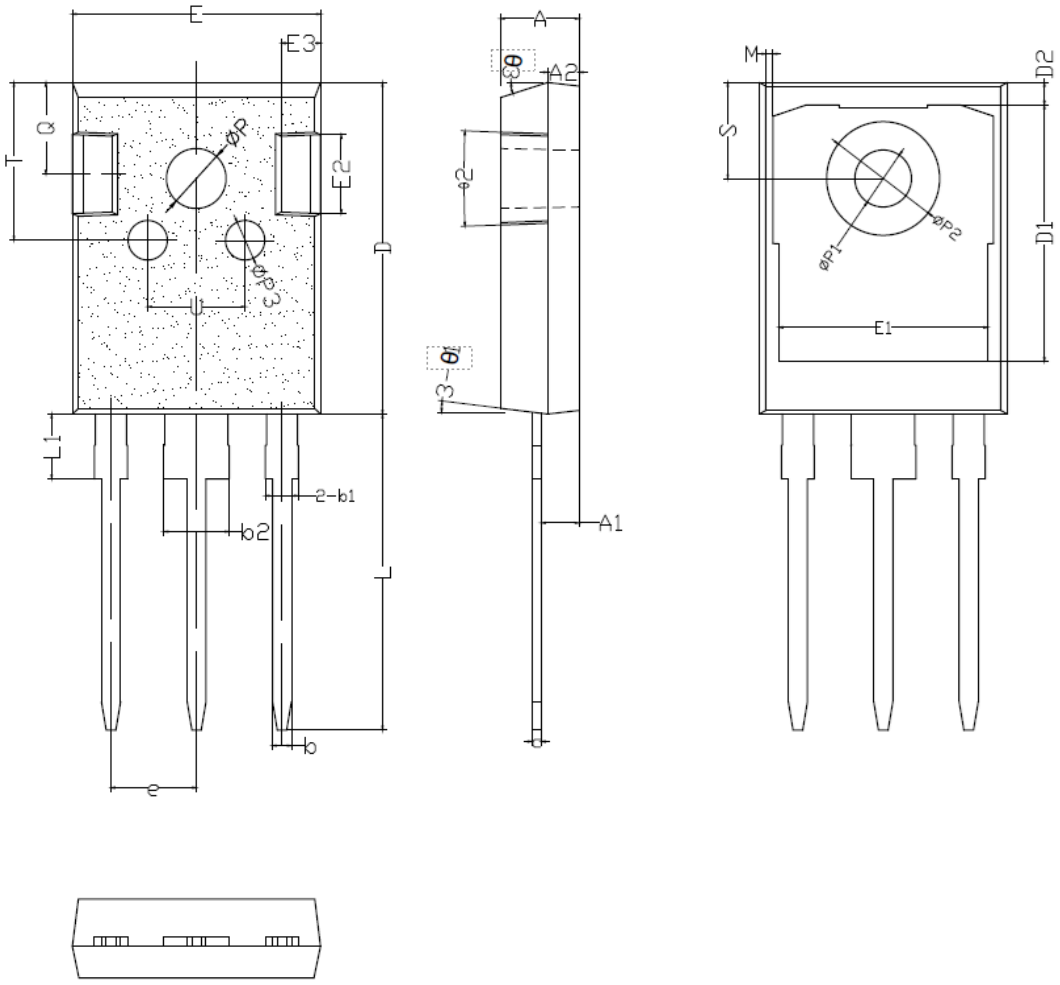
Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Case (25 °C)

  are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

TO-247 PACKAGE OUTLINE DIMENSIONS



SYMBOL	mm		
	MIN	NOM	MAX
*A	4.90	5.00	5.10
*A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
*b	1.15	1.20	1.25
*b1	1.95	2.10	2.25
*b2	2.95	3.10	3.25
*c	0.55	0.60	0.65
*D	20.90	21.00	21.10
D1	16.35	16.55	16.75
D2	1.05	1.20	1.35

*E	15.70	15.80	15.90
E1	13.10	13.25	13.40
E2	4.85	4.95	5.10
E3	2.40	2.50	2.60
*e	5.40	5.44	5.48
*L	19.80	19.98	20.15
*L1	-	-	4.30
*ΦP	3.40	3.50	3.60
*ΦP1	6.90	7.10	7.30
ΦP2	2.40	2.50	2.60
ΦP3	2.40	2.50	2.60
Q	5.60	5.80	6.00
*S	6.05	6.15	6.25
T	9.80	10.00	10.20
U	6.00	6.20	6.40
θ1	5°	7°	9°
θ2	1°	3°	5°
θ3	13°	15°	17°

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