PRODUCT DATA SHEET

To learn more about JGSEMI, please visit our website at

Datasheet Resources Samples

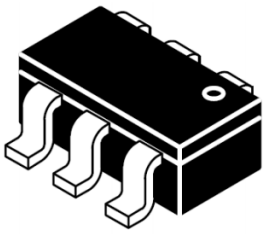
Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.

SRV05-4HTG-D

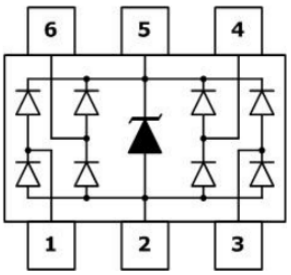
LOW CAPACITANCE TVS DIODE ARRAY

Features

- Ultra low leakage: nA level
- Operating voltage: 5V
- Low clamping voltage
- Complies with following standards:
  – IEC 61000-4-2 (ESD) immunity test
    Air discharge: ±15kV
    Contact discharge: ±8kV
  – IEC61000-4-4 (EFT) 40A (5/50ns)
  – IEC61000-4-5 (Lightning) 5A (8/20 μs)
- RoHS Compliant
- AEC-Q101 qualified.



SOT-26



Applications

- USB 2.0 power and data line
- Set-top box and digital TV
- Digital video interface (DVI)
- Notebook Computers
- SIM Ports
- 10/100/1000 Ethernet

Mechanical Characteristics

- Package: SOT-26
- Lead Finish: Lead Free
- UL Flammability Classification Rating 94V-0
- Quantity Per Reel:3,000pcs
- Reel Size:7inch

Absolute Maximum Ratings(Tamb=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppp	350	W
ESD per IEC 61000−4−2 (Air)	V_{ESD}	±15	Kv
ESD per IEC 61000−4−2 (Contact)		±8	
Operating Temperature Range	T_J	-55 to +125	℃
Storage Temperature Range	T_{STJ}	-55 to +150	℃

Electrical Characteristics(TA=25°C unless otherwise specified)

Part Number	V_{RWM} (V)	V_{BR} (V)	I_T (mA)	V_C @1A	Vc		I_R μA (Max)	C (Pf) (Typ.)
					(Max)	(@A)		
SRV05-4HTG-D	5	6	1	15	28	5	1	0.5

Characteristic Curves

Fig1. 8/20μs Pulse Waveform

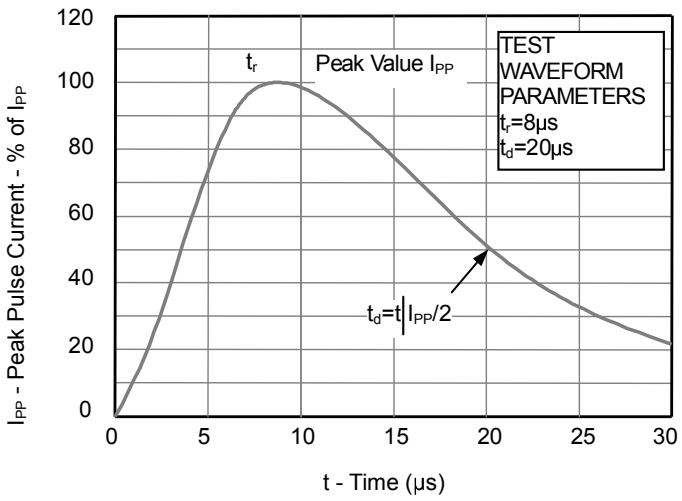


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)

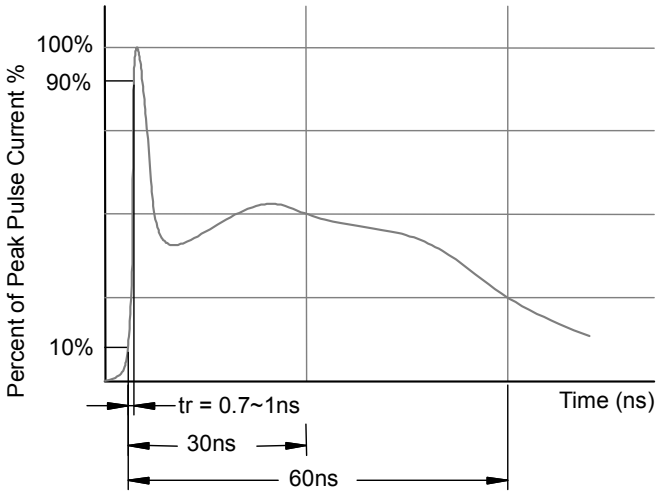
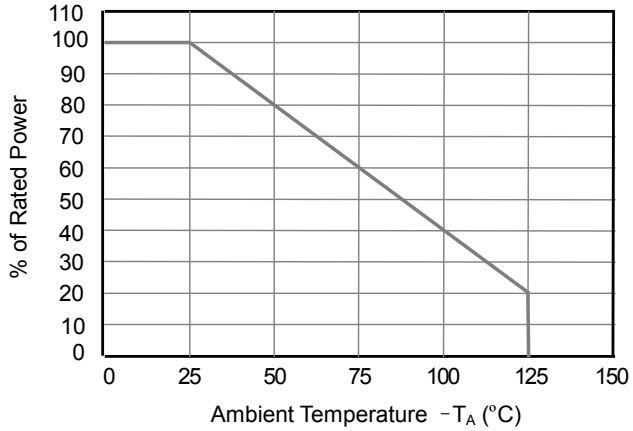


Fig3. Power Derating Curve



Applications Information

Figure 1. Data Line and Power Supply Protection Using V_{CC} as reference

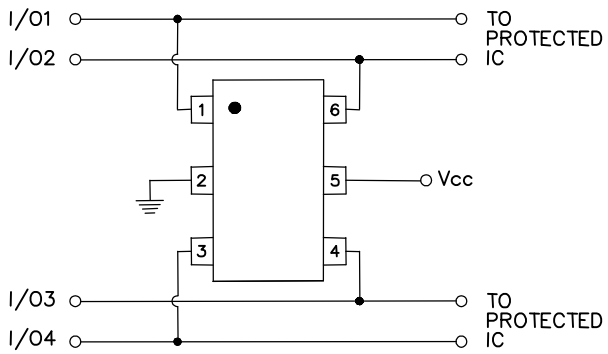


Figure 2. Data Line Protection with Bias and Power Supply Isolation Resistor

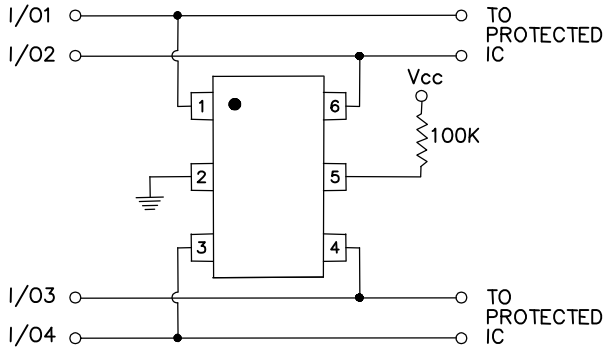
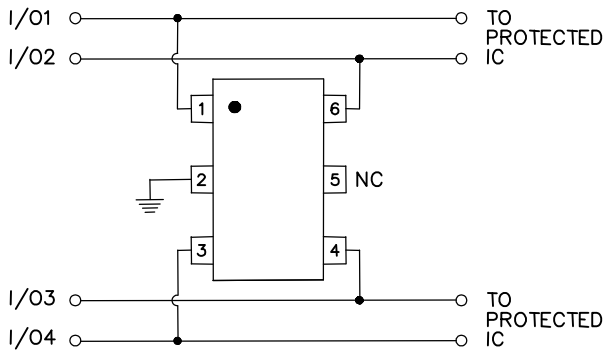


Figure 3. Data Line Protection Using Internal TVS Diode as Reference



Device Connection Options for Protection of Four High-Speed Data Lines

The SRV05-4HTG-D is designed to protect four data lines from transient over-voltages by clamping them to a fixed reference. When the voltage on the protected line exceeds the reference voltage (plus diode VF) the steering diodes are forward biased, conducting the transient current away from the sensitive circuitry. Data lines are connected at pins 1, 3, 4 and 6. The negative reference (REF1) is connected at pin 2. This pin should be connected directly to a ground plane on the board for best results. The path length is kept as short as possible to minimize parasitic inductance. The positive reference (REF2) is connected at pin 5. The options for connecting the positive reference are as follows:

1. To protect data lines and the power line, connect pin 5 directly to the positive supply rail (V_{CC}). In this configuration the data lines are referenced to the supply voltage. The internal TVS diode prevents over-voltage on the supply rail (See Figure1).

2. The SRV05-4HTG-D can be isolated from the power supply by adding a series resistor between pin 5 and VCC. A value of 100kΩ is recommended. The internal TVS and steering diodes remain biased, providing the advantage of lower capacitance (See Figure2).

3. In applications where no positive supply reference is available, or complete supply isolation is desired, the internal TVS may be used as the reference. In this case, pin 5 is not connected. The steering diodes will begin to conduct when the voltage on the protected line exceeds the working voltage of the TVS (plus one diode drop) (See Figure3).

Applications Information (Continue)

Figure 4. Video Interface Protection

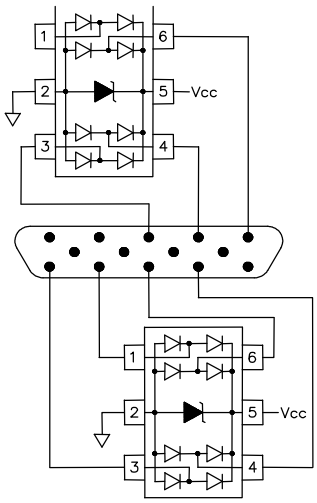


Figure 5 - Dual USB Port Protection

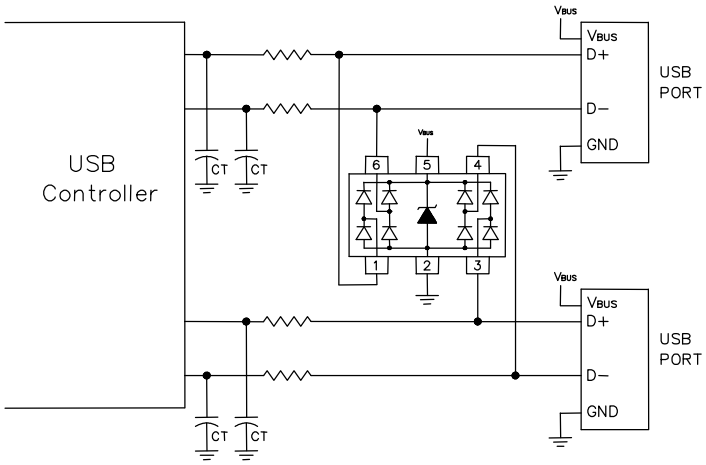
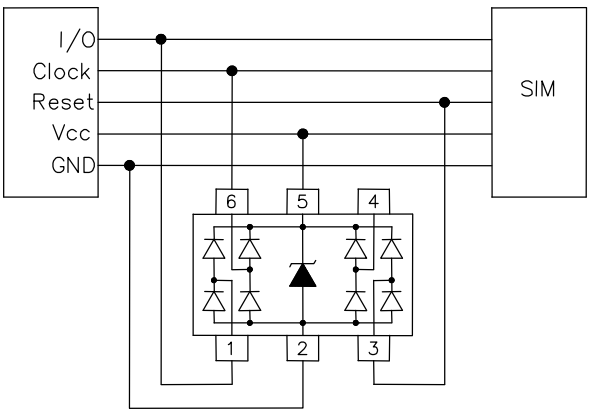


Figure 6 - SIM Port



Video Interface Protection

Video interfaces are susceptible to transient voltages resulting from electrostatic discharge (ESD) and "hot plugging" cables. If left unprotected, the video interface IC may be damaged or even destroyed. Protecting a high-speed video port presents some unique challenges. First, any added protection device must have extremely low capacitance and low leakage current so that the integrity of the video signal is not compromised. Second, the protection component must be able to absorb high voltage transients without damage or degradation. As a minimum, the device should be rated to handle ESD voltages per IEC61000-4-2, level 4 (±15kV air, ±8kV contact). The clamping voltage of the device (when conducting high current ESD pulses) must be sufficiently low enough to protect the sensitive CMOS IC. If the clamping voltage is too high, the "protected" device may latch-up or be destroyed. Finally, the device must take up a relatively small amount of board space, particularly in portable applications such as notebooks and handhelds. The SRV05-4HTG-D is designed to meet or exceed all of the above criteria. A typical video interface protection circuit is shown in Figure 4. All exposed lines are protected including R, G, B, H-Sync, V-Sync, and the ID lines for plug and play monitors.

Universal Serial Bus ESD Protection

The SRV05-4HTG-D may also be used to protect the USB ports on monitors, computers, peripherals or portable systems. Each device will protect up to two USB ports (Figure5). When the voltage on the data lines exceed the bus voltage (plus one diode drop), the internal rectifiers are forward biased conducting the transient current away from the protected controller chip. The TVS diode directs the surge to ground. The TVS diode also acts to suppress ESD strikes directly on the voltage bus. Thus, both power and data pins are protected with a single device.

Applications Information (Continue)

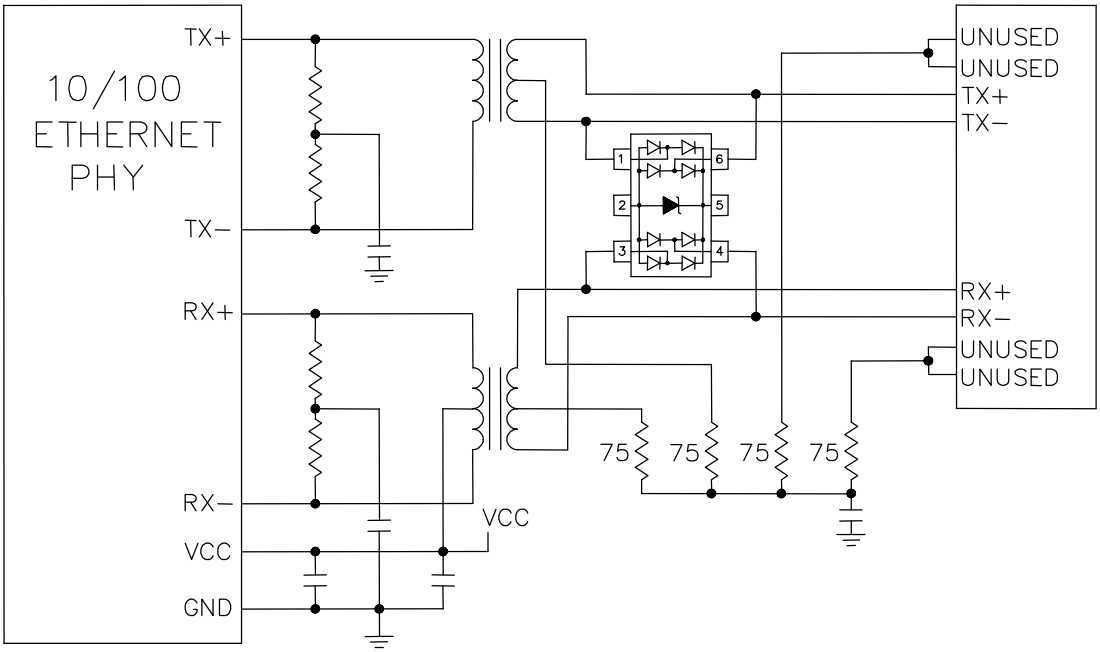


Figure 8 - 10/100 Ethernet Differential Protection

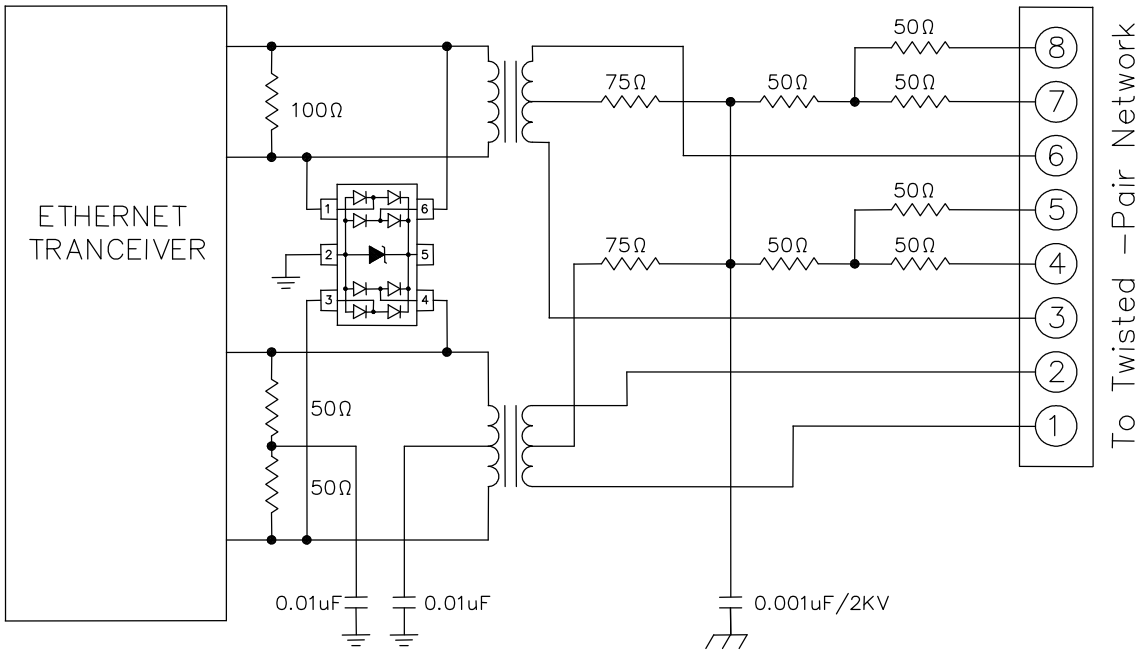
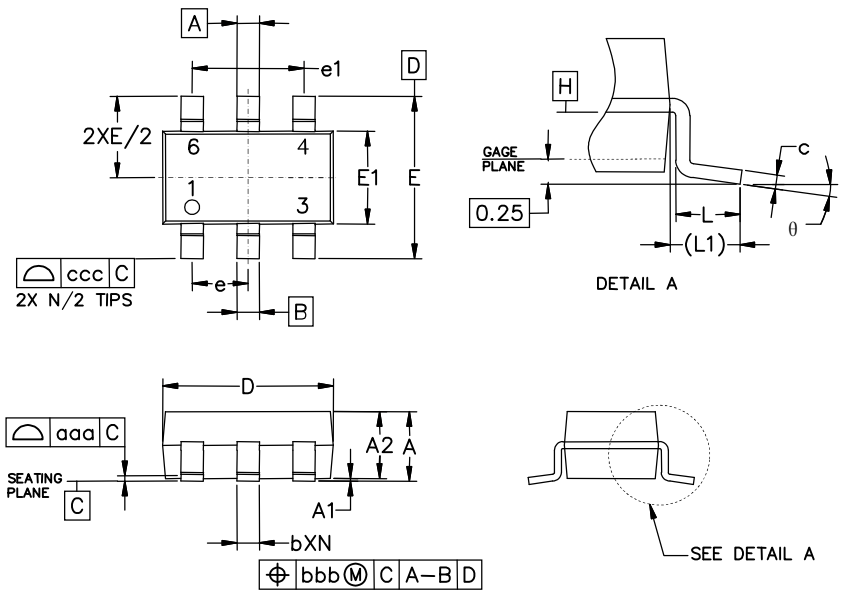


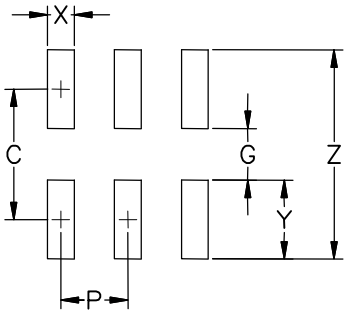
Figure 9 - 10/100 Ethernet Differential and Common Mode Protection

SOT-26 Package Outline & Dimensions



Symbol	Inches			Millimeters		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	0.035	-	0.057	0.90	-	1.45
A1	0.000	-	0.006	0.00	-	0.15
A2	0.035	0.045	0.051	0.90	1.15	1.30
b	0.010	-	0.020	0.25	-	0.50
c	0.003	-	0.009	0.08	-	0.22
D	0.110	0.114	0.122	2.80	2.90	3.10
E1	0.060	0.063	0.069	1.50	1.60	1.75
E	0.110 BSC			2.80 BSC		
e	0.037 BSC			0.95 BSC		
e1	0.075 BSC			1.90 BSC		
L	0.012	0.018	0.024	0.30	0.45	0.60
L1	(0.024)			(0.60)		
θ	0°	-	10°	0°	-	10°
aaa	0.004			0.10		
bbb	0.008			0.20		
ccc	0.008			0.20		

Soldering Footprint



Symbol	Inches	Millimeters
C	(0.098)	(2.50)
G	0.055	1.40
P	0.037	0.95
X	0.024	0.60
Y	0.043	1.10
Z	0.141	3.60

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