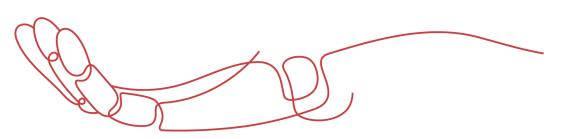




# **PRODUCT DATA SHEET**



To learn more about JGSEMI, please visit our website at







Datasheet

ources Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO\_questions@jgsemi.com.



#### **General Description**

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

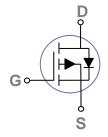
BVDSS	RDSON	ID
-20V	17mΩ	-6.5A

#### **Features**

- -20V, -6.5A,  $RDS(ON) = 17m\Omega@VGS = -4.5V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

## **SOT23-3L Pin Configuration**





### **Applications**

- Notebook
- Load Switch
- Networking

#### Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-20	V
V <sub>GS</sub>	Gate-Source Voltage	±12	V
I-	Drain Current – Continuous (T <sub>A</sub> =25°C)	-6.5	А
lD	Drain Current – Continuous (T <sub>A</sub> =70°C)	-4.1	А
Ірм	Drain Current – Pulsed <sup>1</sup>	-20	А
D	Power Dissipation (Tc=25°C)	1.56	W
$P_{D}$	Power Dissipation – Derate above 25°C	0.012	W/°C
Т <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 125	°C

#### **Thermal Characteristics**

Symbol	Symbol Parameter		Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		80	°C/W



## Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-20			٧
△BV <sub>DSS</sub> /△T <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =-1mA		-0.01		V/°C
IDSS	Drain Source Leekens Current	V <sub>DS</sub> =-20V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			-1	uA
	Drain-Source Leakage Current	V <sub>DS</sub> =-16V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C		-10	uA	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±12V , V <sub>DS</sub> =0V			±100	nA

### **On Characteristics**

		V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-5A		17	24	mΩ
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-2.5V , I <sub>D</sub> =-4A		21	26	11152
$V_{GS(th)}$	Gate Threshold Voltage	\/ \/ I- 250uA	-0.3	-0.68	-1.0	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA		3		mV/°C
gfs	Forward Transconductance	V <sub>DS</sub> =-10V , I <sub>S</sub> =-5A		15		S

## **Dynamic and switching Characteristics**

	_ <del>_</del>			
$Q_g$	Total Gate Charge <sup>2,3</sup>		 19.5	
$Q_{gs}$	Gate-Source Charge <sup>2,3</sup>	V <sub>DS</sub> =-10V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-5A	 2	 nC
$Q_gd$	Gate-Drain Charge <sup>2, 3</sup>		 3.6	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2,3</sup>		 10.4	
Tr	Rise Time <sup>2,3</sup>	$V_{DD}$ =-10V , $V_{GS}$ =-4.5V , $R_{G}$ =25 $\Omega$	 37.5	 ~0
$T_{d(off)}$	Turn-Off Delay Time <sup>2,3</sup>	I <sub>D</sub> =-1A	 89.1	 nS
Tf	Fall Time <sup>2, 3</sup>		 24.6	
C <sub>iss</sub>	Input Capacitance		 150	
Coss	Output Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , F=1MHz	 200	 pF
Crss	Reverse Transfer Capacitance		 100	

## **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions		Тур.	Max.	Unit
Is	Continuous Source Current	Va-Va-OV Force Current			-6.5	Α
Isм	Pulsed Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-13	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25°C			-1.2	V

#### Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq 300$ us , duty cycle  $\leq 2$ %.
- 3. Essentially independent of operating temperature.



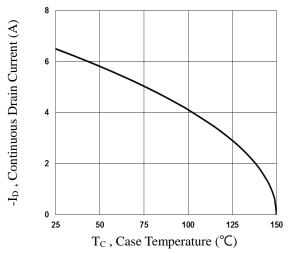


Fig.1 Continuous Drain Current vs. Tc

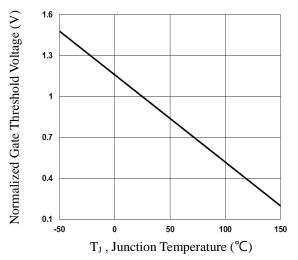


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

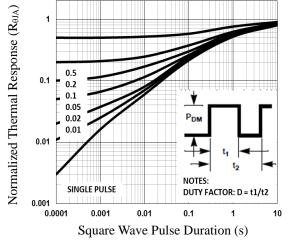


Fig.5 Normalized Transient Impedance

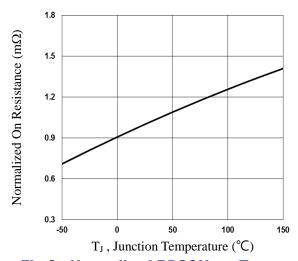


Fig.2 Normalized RDSON vs. T<sub>J</sub>

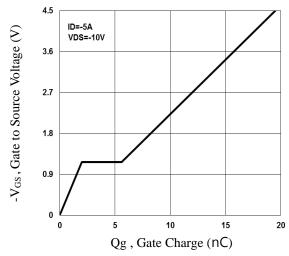


Fig.4 Gate Charge Waveform

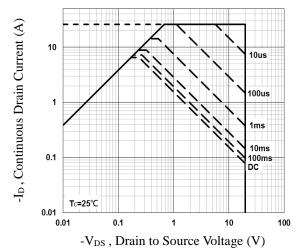
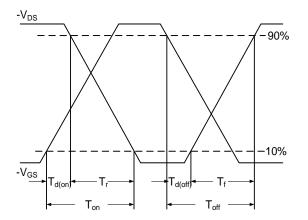


Fig.6 Maximum Safe Operation Area





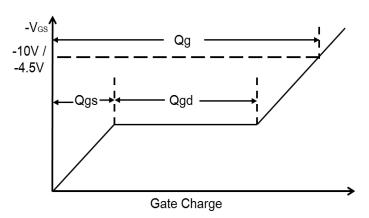
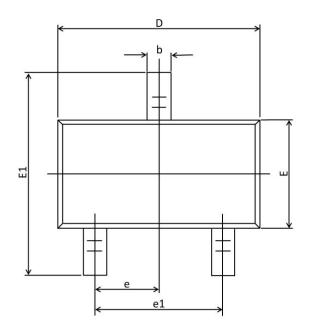
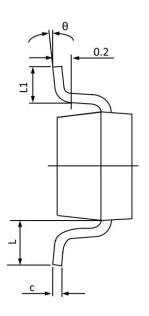


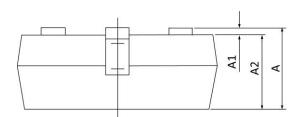
Fig.8 Gate Charge Waveform



# **SOT23-3L PACKAGE INFORMATION**







Crumbal	Dimensions I	n Millimeters	Dimension	s In Inches	
Symbol	Min	Max	Min	Max	
A	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.400	0.012	0.016	
c	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
<b>E</b> 1	2.650	2.950	0.104	0.116	
e	0.950 TYP.		0.037	TYP.	
e1	1.800	2.000	0.071	0.079	
L	0.700	REF.	0.028 REF.		
L1	0.300	0.600	0.012	0.024	
$\theta$	<b>0</b> °	8°	<b>0</b> °	8°	



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