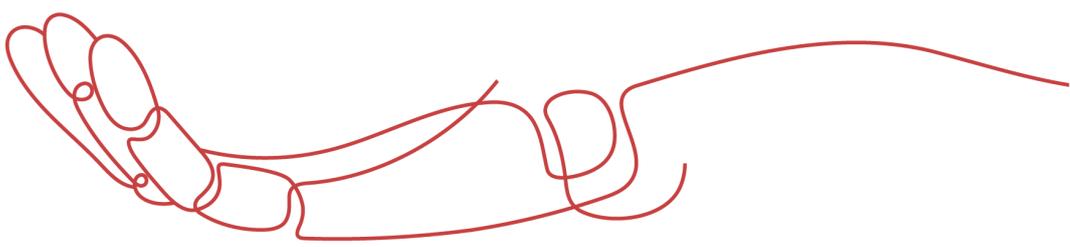


PRODUCT DATA SHEET



To learn more about JGSEMI, please visit our website at



Datasheet



Resources



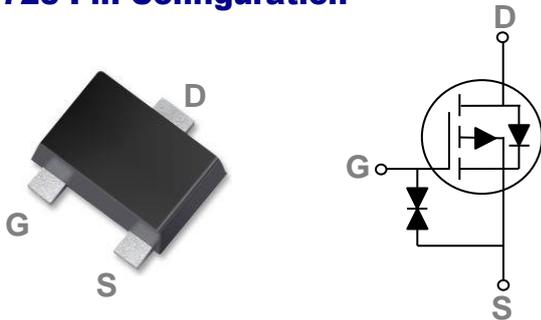
Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.

General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT723 Pin Configuration



BVDSS	RDSON	ID
-20V	650mΩ	-400mA

Features

- -20V,-400mA, $R_{DS(ON)} = 650m\Omega @ V_{GS} = -4.5V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for -1.5V Gate Drive Applications

Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	± 8	V
I_D	Drain Current – Continuous ($T_A=25^\circ\text{C}$)	-400	mA
	Drain Current – Continuous ($T_A=70^\circ\text{C}$)	-320	mA
I_{DM}	Drain Current – Pulsed ¹	-1.6	A
P_D	Power Dissipation ($T_A=25^\circ\text{C}$)	450	mW
	Power Dissipation – Derate above 25°C	3.6	mW/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 125	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	280	$^\circ\text{C/W}$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-20	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	-0.01	---	V/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-20V, V _{GS} =0V, T _J =25°C	---	---	-1	uA
		V _{DS} =-16V, V _{GS} =0V, T _J =125°C	---	---	-10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±8V, V _{DS} =0V	---	---	±20	uA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-4.5V, I _D =-0.3A	---	500	650	mΩ
		V _{GS} =-2.5V, I _D =-0.2A	---	700	900	
		V _{GS} =-1.8V, I _D =-0.1A	---	1100	1400	
		V _{GS} =-1.5V, I _D =-0.1A	---	1700	2300	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-0.3	-0.7	-1.0	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	3	---	mV/°C

Dynamic and switching Characteristics

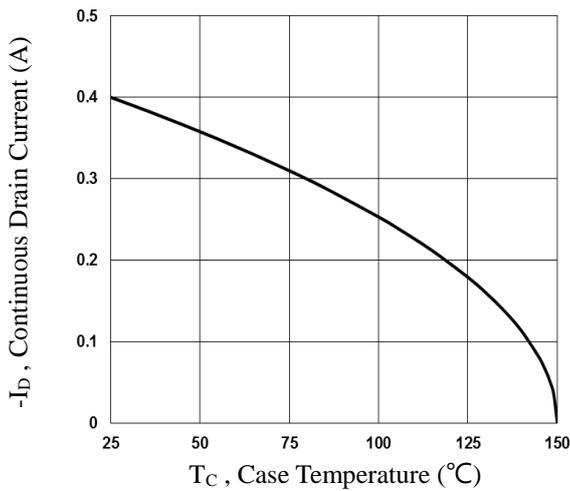
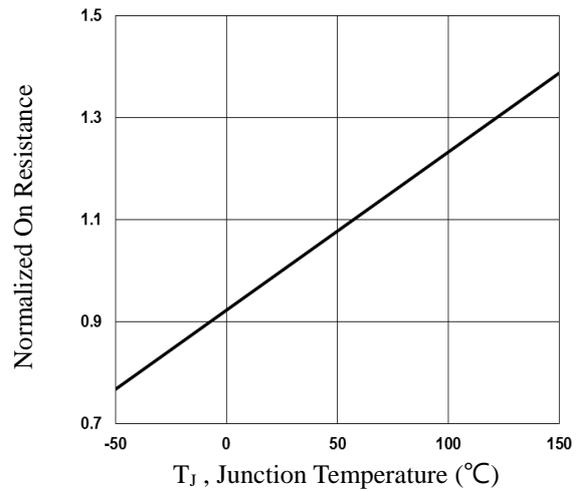
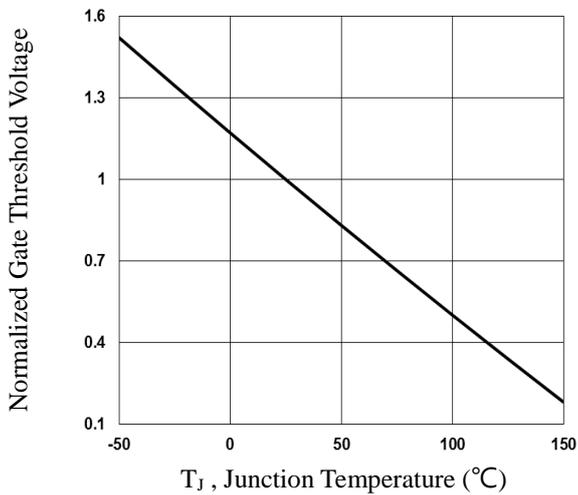
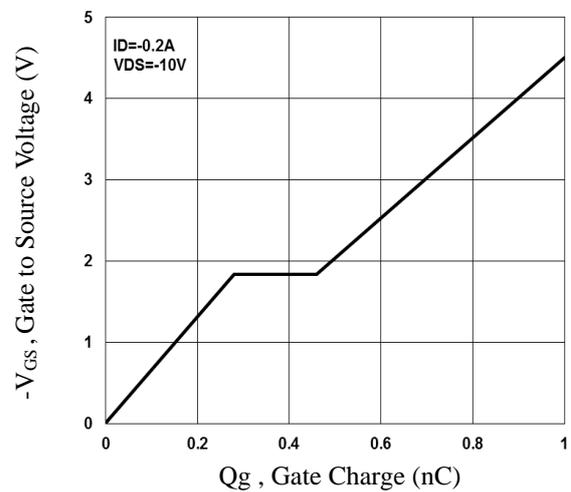
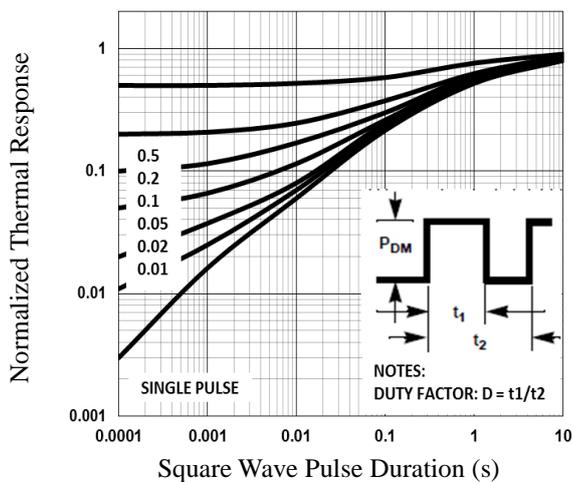
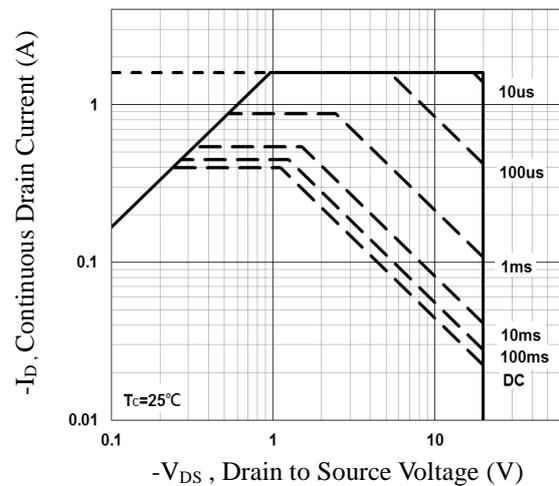
Q _g	Total Gate Charge ^{2,3}	V _{DS} =-10V, V _{GS} =-4.5V, I _D =-0.2A	---	1	2	nC
Q _{gs}	Gate-Source Charge ^{2,3}		---	0.28	0.5	
Q _{gd}	Gate-Drain Charge ^{2,3}		---	0.18	0.4	
T _{d(on)}	Turn-On Delay Time ^{2,3}	V _{DD} =-10V, V _{GS} =-4.5V, R _G =10Ω I _D =-0.2A	---	8	16	ns
T _r	Rise Time ^{2,3}		---	5.2	10	
T _{d(off)}	Turn-Off Delay Time ^{2,3}		---	30	60	
T _f	Fall Time ^{2,3}		---	18	36	
C _{iss}	Input Capacitance	V _{DS} =-10V, V _{GS} =0V, F=1MHz	---	40	78	pF
C _{oss}	Output Capacitance		---	15	30	
C _{rss}	Reverse Transfer Capacitance		---	6.5	13	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	-0.4	A
I _{SM}	Pulsed Source Current		---	---	-0.8	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =-0.2A, T _J =25°C	---	---	-1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_c

Fig.2 Normalized $R_{DS(on)}$ vs. T_j

Fig.3 Normalized V_{th} vs. T_j

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Response

Fig.6 Maximum Safe Operation Area

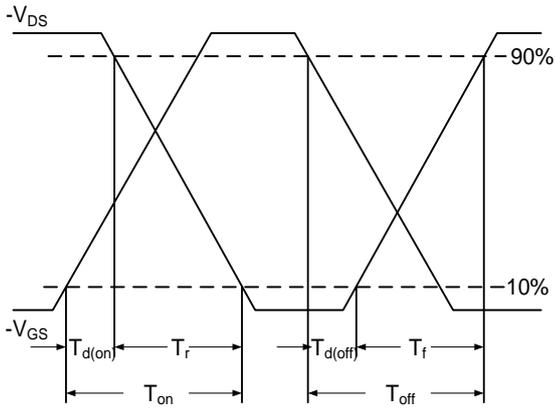


Fig.7 Switching Time Waveform

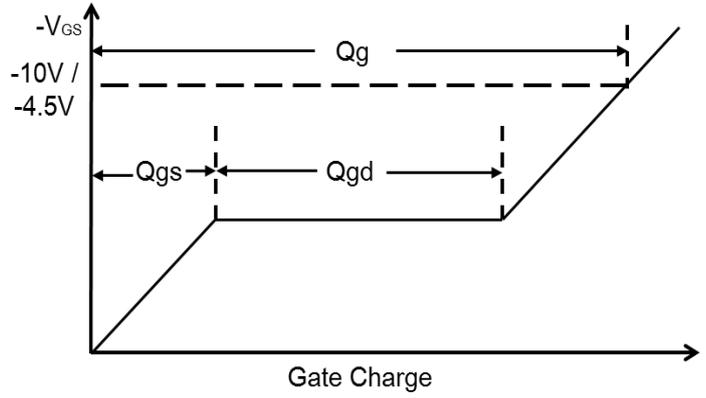
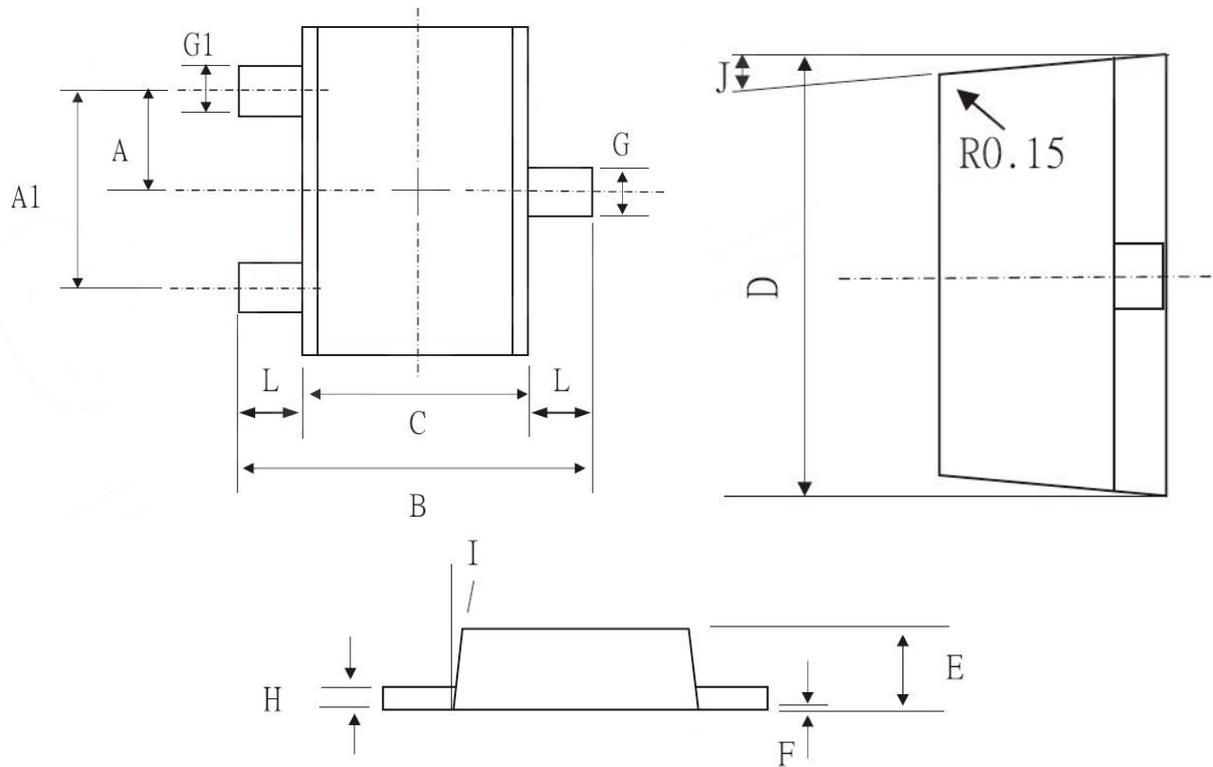


Fig.8 Gate Charge Waveform

SOT723 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.4 BSC		0.016 BSC	
A1	0.8 BSC		0.031 BSC	
B	1.250	1.150	0.049	0.045
C	0.850	0.750	0.033	0.030
D	1.250	1.150	0.049	0.045
E	0.390	0.370	0.015	0.015
F	0.050	0.000	0.002	0.000
G	0.270	0.220	0.011	0.009
G1	0.250	0.170	0.010	0.007
H	0.150	0.080	0.006	0.003
I	13°	9°	13°	9°
L	0.250	0.150	0.010	0.006
J	11°	7°	11°	7°

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