

Description

The DC0721P0 is a bi-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The DC0721P0 complies with the IEC 61000-4-2 (ESD) with $\pm 30\text{kV}$ air and $\pm 30\text{kV}$ contact discharge. It is assembled into an ultra-small lead-free DFN0603-2 package. The small size and high ESD surge protection make DC0721P0 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Mechanical Characteristics

- ◆ Package: DFN0603-2 (0.6 x 0.3 x 0.3mm)
- ◆ Case Material: "Green" Molding Compound.
- ◆ Moisture Sensitivity: Level 3 per J-STD-020
- ◆ Terminal Connections: See Diagram Below
- ◆ Marking Information: See Below

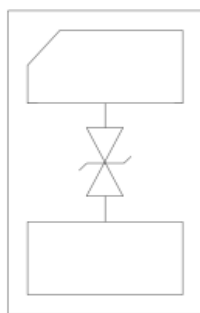
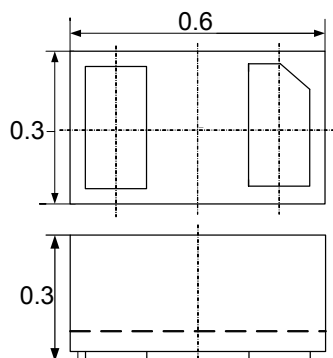
Features

- ◆ Ultra small package: 0.6 x 0.3 x 0.3mm
- ◆ Protects one data or power line
- ◆ Ultra low leakage: nA level
- ◆ Operating voltage: 7V
- ◆ Low clamping voltage
- ◆ Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30\text{kV}$
 - Contact discharge: $\pm 30\text{kV}$
 - IEC61000-4-5 (Lightning) 5A (8/20us)
- ◆ RoHS Compliant

Applications

- ◆ Cellular Handsets and Accessories
- ◆ Personal Digital Assistants
- ◆ Notebooks and Handhelds
- ◆ Portable Instrumentation
- ◆ Digital Cameras
- ◆ Peripherals
- ◆ Audio Players
- ◆ Keypads, Side Keys, LCD Displays

Dimensions and Pin Configuration



Marking Information



7A = Device Marking Code

Package Dimensions (mm) Circuit and Pin Schematic

Ordering Information

Part Number	Marking	Packaging	Reel Size
DC0721P0	7A	10000/Tape & Reel	7 inch

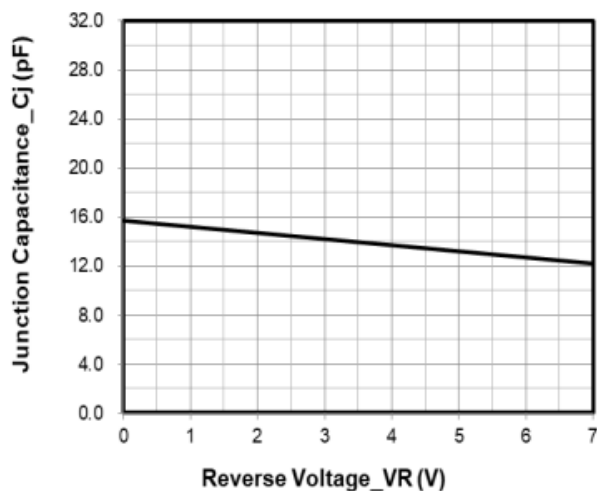
Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power(8/20 μs)	Ppk	80	W
Peak Pulse Current(8/20 μs)	Ipp	5	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	VESD	± 30 ± 30	kV
Operating Temperature Range	TJ	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^{\circ}\text{C}$

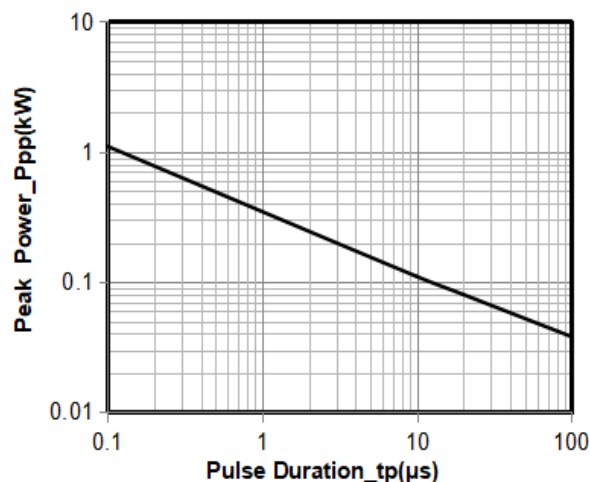
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			7	V	
Breakdown Voltage	VBR	7.5			V	IT = 1mA
Reverse Leakage Current	IR			0.2	μA	VRWM = 7V
Clamping Voltage	VC			9	V	IPP = 1A
Clamping Voltage	VC			10	V	IPP = 5A
Junction Capacitance	CJ		15		pF	VR = 0V, f = 1MHz

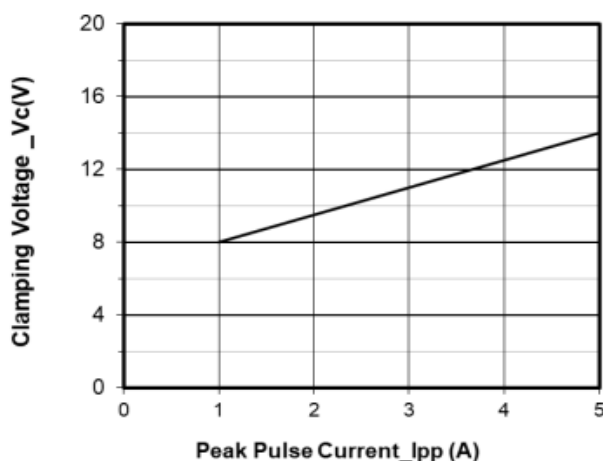
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



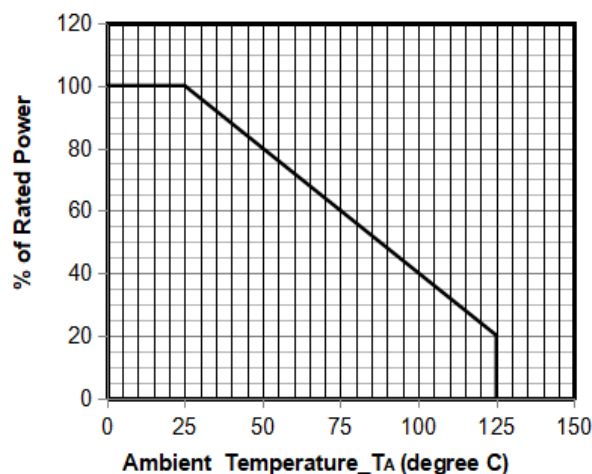
Junction Capacitance vs. Reverse Voltage



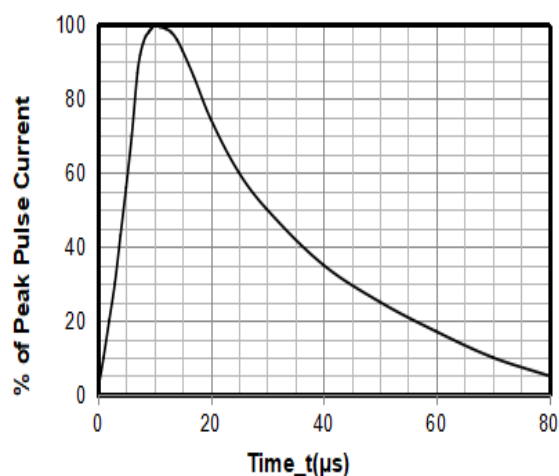
Peak Pulse Power vs. Pulse Time



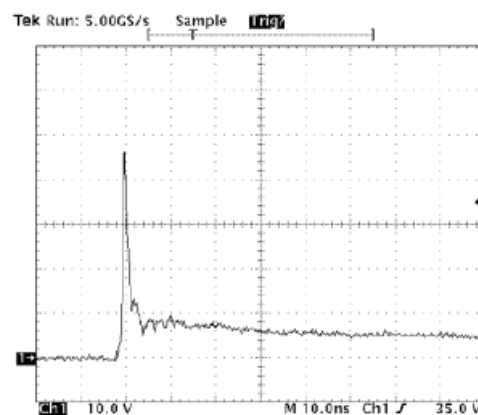
Clamping Voltage vs. Peak Pulse Current $t_p = 8 \text{ } 20\mu\text{s}$



Power Derating Curve



8 X 20us Pulse Waveform

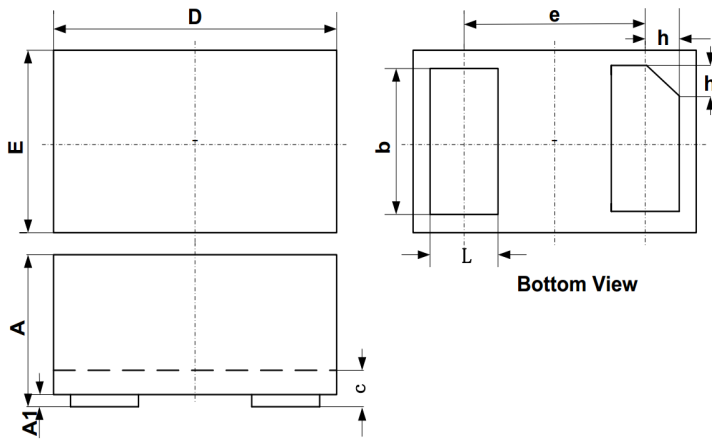


Note: Data is taken with a 10x attenuator

ESD Clamping Voltage

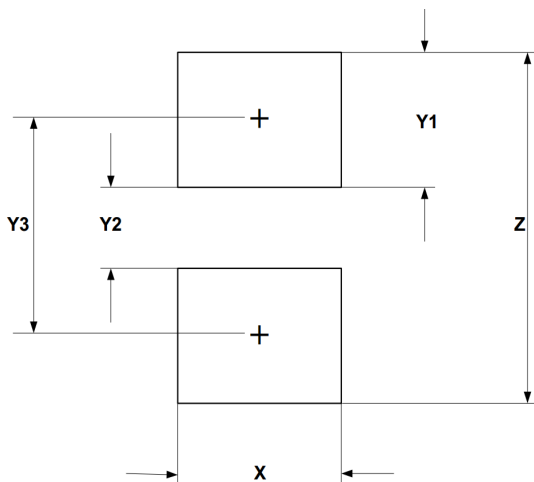
8 kV Contact per IEC61000-4-2

DFN0603-2 Package Outline Drawing



SYM	DIMENSIONS		
	MILLIMETERS		
	MIN	NOM	MAX
A	0.230		0.330
A1	0.000	0.020	0.050
b	0.215	0.245	0.275
c	0.120	0.150	0.180
D	0.550	0.600	0.650
e	0.355 BSC		
E	0.250	0.300	0.350
L	0.160	0.190	0.220
h	0.079 BSC		

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X	0.30	0.012
Y1	0.25	0.010
Y2	0.15	0.006
Y3	0.40	0.016
Z	0.65	0.026