

Surface Mount General Purpose Silicon

FEATURES

- For surface mounted applications
- Low profile package
- Glass Passivated Chip Junction
- Easy to pick and place
- Lead free in comply with EU RoHS 2011/65/EU directives

MECHANICAL DATA

· Case: SMC



Top View Marking Code: S5M Simplified outline SMC and symbol

PINNING

PIN	DESCRIPTION				
1	Cathode				
2	Anode				

Maximum Ratings and Electrical characteristics

Parameter	Symbols	S5MC	Units
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	1000	٧
Maximum RMS voltage	V_{RMS}	700	٧
Maximum DC Blocking Voltage	V _{DC}	1000	V
Maximum Average Forward Rectified Current	I _{F(AV)}	5	А
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	I _{FSM}	120	А
Maximum Instantaneous Forward Voltage at 5 A	V _F	1.0	V
Maximum DC Reverse Current $T_a = 25 ^{\circ}\text{C}$ at Rated DC Blocking Voltage $T_a = 125 ^{\circ}\text{C}$	I _R	5 100	μA
Typical Junction Capacitance (1)	C _j	50	pF
Typical Thermal Resistance (2)	$R_{ heta JA} \ R_{ heta JC}$	35 13	°C/W
Operating and Storage Temperature Range	T_{j},T_{stg}	-55 ~ +150	°C

^(1) Measured at 1 MHz and applied reverse voltage of 4 V D.C

⁽ 2) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.



Fig.1 Forward Current Derating Curve

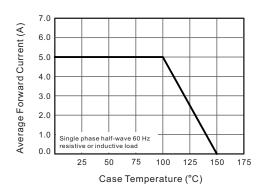


Fig.2 Typical Reverse Characteristics

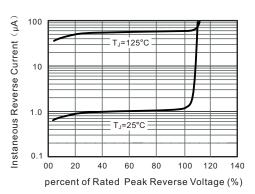


Fig.3 Typical Forward Characteristic

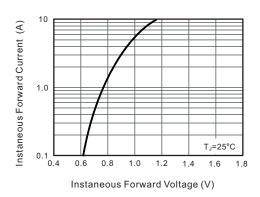


Fig.4 Typical Junction Capacitance

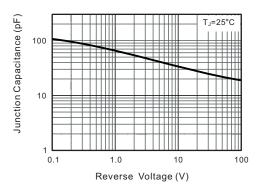
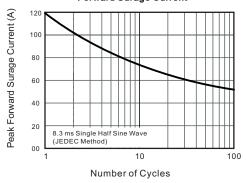


Fig.6 Maximum Non-Repetitive Peak Forward Surage Current

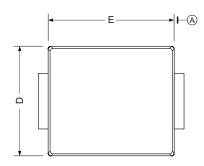


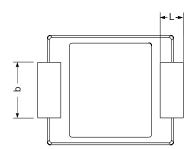


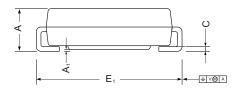
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SMC







SMC mechanical data

UNIT		Α	Е	D	E₁	A ₁	С	L	b
mm	max	2.62	7.0	6.2	8.0	0.21	0.31	1.6	3.25
	min	2.00	6.5	5.6	7.6	0.05	0.15	0.9	2.75
mil	max	103	276	244	315	8.3	12	63	128
	min	79	256	220	299	2.0	5.9	35	108

The recommended mounting pad size

