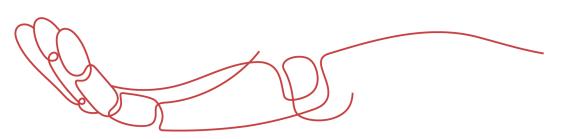


# **PRODUCT DATA SHEET**



To learn more about JGSEMI, please visit our website at







Datasheet

es Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to Fairchild\_questions@jgsemi.com.





#### **Features**

- 100Watts peak pulse power (tp =  $8/20\mu s$ )
- Tiny DFN1006 package
- Bidirectional configurations
- Solid-state silicon-avalanche technology
- Low clamping voltage
- Low leakage current
- Low capacitance (Cj=0.25pF typ. IO to
- Protection one data/power line to:
- IEC 61000-4-2 ±20kV contact ±20kV air
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 4A (8/20μs)

#### **Applications**

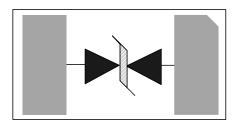
- Cell Phone Handsets and Accessories
- Microprocessor based equipment
- Personal Digital Assistants (PDA's)
- Notebooks, Desktops, and Servers
- Portable Instrumentation

## **Mechanical Data**

- Molding compound flammability rating: UL 94V-0
- Packaging: Tape and Reel
- **RoHS/WEEE Compliant**

DFN1006 package

**Schematic & PIN Configuration** 



**DFN1006** 



**Absolute Maximum Rating** 

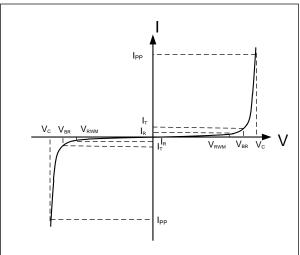
Rating	Symbol	Value	Units
Peak Pulse Power ( $t_p = 8/20 \mu s$ )	P <sub>PP</sub>	100	Watts
Peak Pulse Current ( t <sub>p</sub> =8/20μs ) (note1)	$I_{pp}$	4.0	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	$V_{\mathrm{ESD}}$	20 20	kV
Lead Soldering Temperature	$T_{L}$	260(10seconds)	°C
Junction Temperature	$T_{J}$	-55 to + 125	°C
Storage Temperature	$T_{ m stg}$	-55 to + 125	$^{\circ}$

#### **Electrical Characteristics**

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	$V_{RWM}$				5.0	V
Reverse Breakdown Voltage	$V_{BR}$	I <sub>T</sub> =1mA	6.0			V
Reverse Leakage Current	$I_R$	V <sub>RWM</sub> =5V,T=25℃			100	nA
Peak Pulse Current	$I_{PP}$	tp =8/20μs			4.0	A
Clamping Voltage	$V_{\rm C}$	$I_{PP}=4A, t_p=8/20 \mu s$			25	V
Junction Capacitance	C <sub>j</sub>	IO to IO $V_R = 0V, f = 1MHz$		0.25	0.4	pF

## **Electrical Parameters (TA = 25 °C unless otherwise noted)**

Symbol	Parameter	
IPP	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ Ipp	
V <sub>RWM</sub>	Working Peak Reverse Voltage	
Ir	Maximum Reverse Leakage Current @ V <sub>RWM</sub>	
$V_{BR}$	Breakdown Voltage @ IT	
Iт	Test Current	



Note:.  $8/20\mu s$  pulse waveform.



#### **Typical Characteristics**

Figure 1: Peak Pulse Power vs. Pulse Time

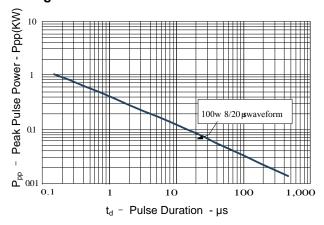


Figure 2: Power Derating Curve

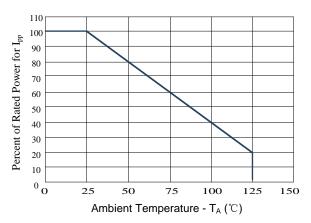


Figure3: Pulse Waveform

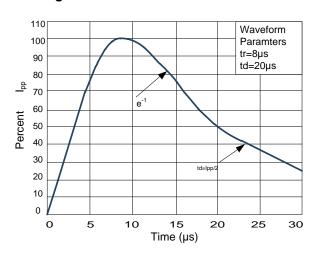


Figure 4: Clamping Voltage vs.lpp

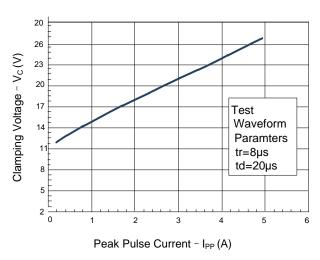


Figure 5: Positive Clamping voltage (TLP)

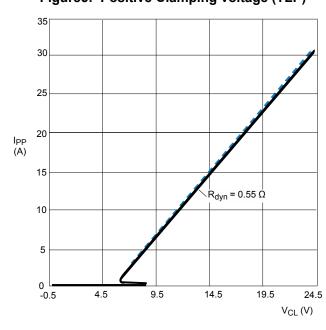
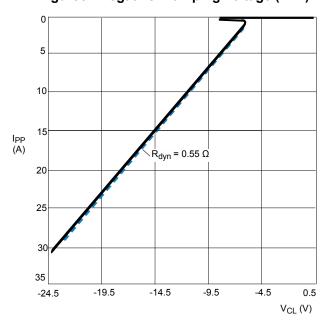
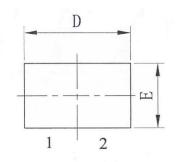


Figure 5: Negative Clamping voltage (TLP)

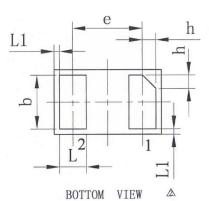


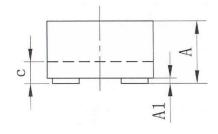


## **Outline Drawing – DFN1006**









SYMBOL	M	ILLIMETI	ER
SYMBOL	MIN	NOM	MAX
A	0.45	0.50	0.55
A1	0	0.02	0.05
ь	0.45	0.50	0.55
c	0.12	0.15	0.18
D	0.95	1.00	1.05
e		0. 65BSC	BSC
Е	0.55	0.60	0.65
L	0.20	0.25	0.30
L1	0.05REF		
h	0.07	0.12	0.17
裁体尺寸 (Mil)	20*20		

### Marking



# **Ordering information**

Order code	Package	Base qty	Delivery mode
TESD05CB	DFN1006	10k	Tape and reel



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