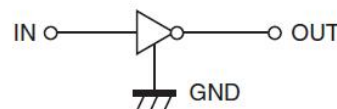
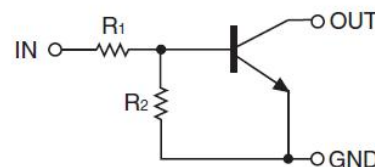


Digital Transistors (Built-in Resistors)

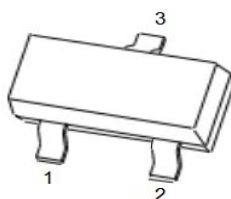
DIGITAL TRANSISTOR (NPN)

FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input.They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy



PIN CONNENCTIONS and MARKING



SOT-23

- 1.IN
- 2.GND
- 3.OUT

ORDERING INFORMATION

Part Number	MARKING ⁽¹⁾	Package	Packing Method	Pack Quantity
DTC124ECA	25	SOT-23	Reel	3000pcs/Reel

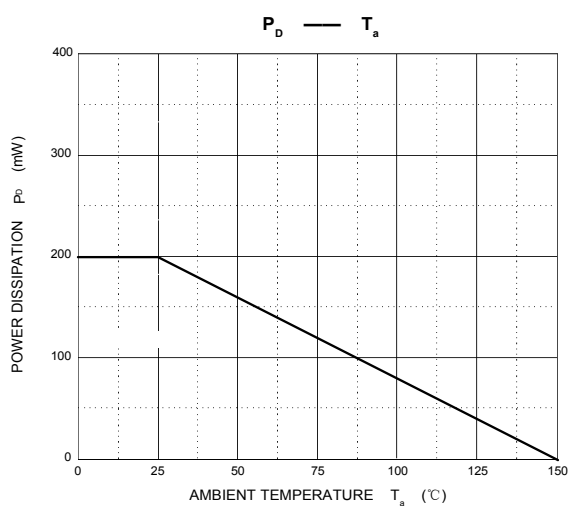
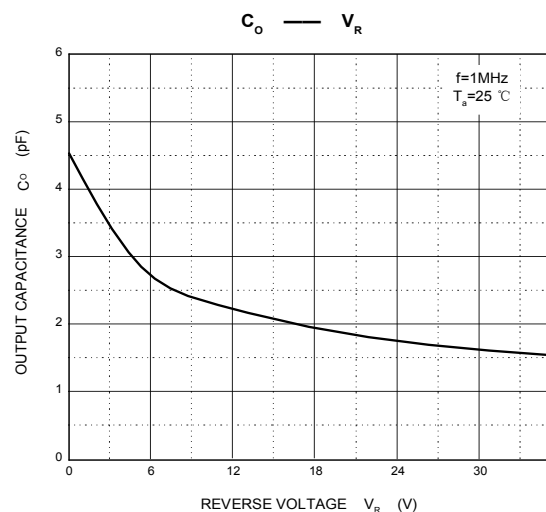
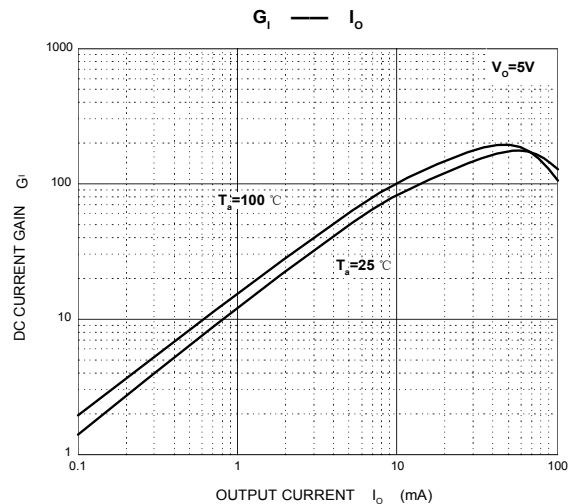
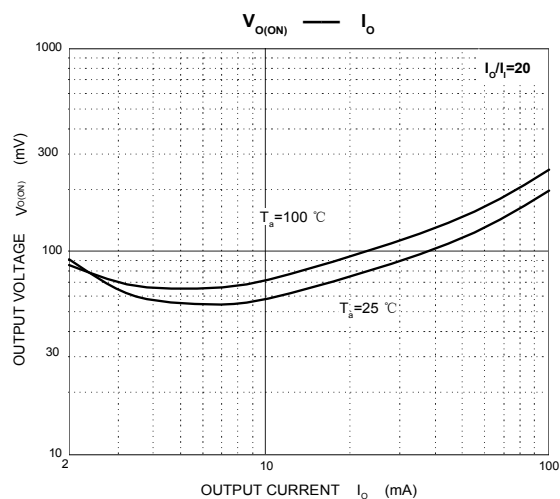
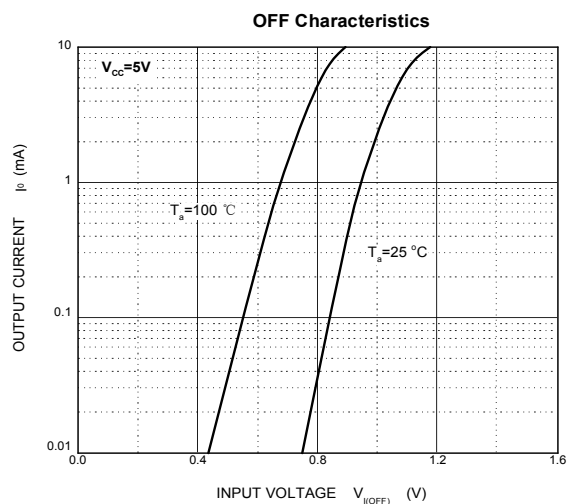
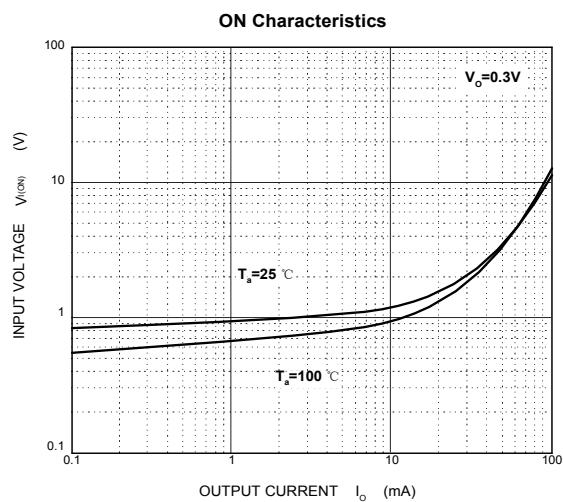
MAXIMUM RATINGS(Ta=25°C unless otherwise noted)

Symbol	Parameter	Limits	Unit
V _{CC}	Supply Voltage	50	V
V _{IN}	Input Voltage	-10~+40	V
I _O	Output Current	30	mA
I _{CM}	Peak Collector Current	100	mA
P _D	Power Dissipation	200	mW
T _J , T _{stg}	Operation Junction and Storage Temperature Range	-55~+150	°C

ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	V _{I(off)}	V _{CC} =5V, I _O =100μA	0.5			V
	V _{I(on)}	V _O =0.2V, I _O =5mA			3	V
Output voltage	V _{O(on)}	I _O /I _I =10mA/0.5mA		0.1	0.3	V
Input current	I _I	V _I =5V			0.36	mA
Output current	I _{O(off)}	V _{CC} =50V, V _I =0			0.5	μA
DC current gain	G _I	V _O =5V, I _O =5mA	56			
Input resistance	R ₁		15.4	22	28.6	kΩ
Resistance ratio	R ₂ /R ₁		0.8	1	1.2	
Transition frequency	f _T	V _O =10V, I _O =5mA, f=100MHz		250		MHz

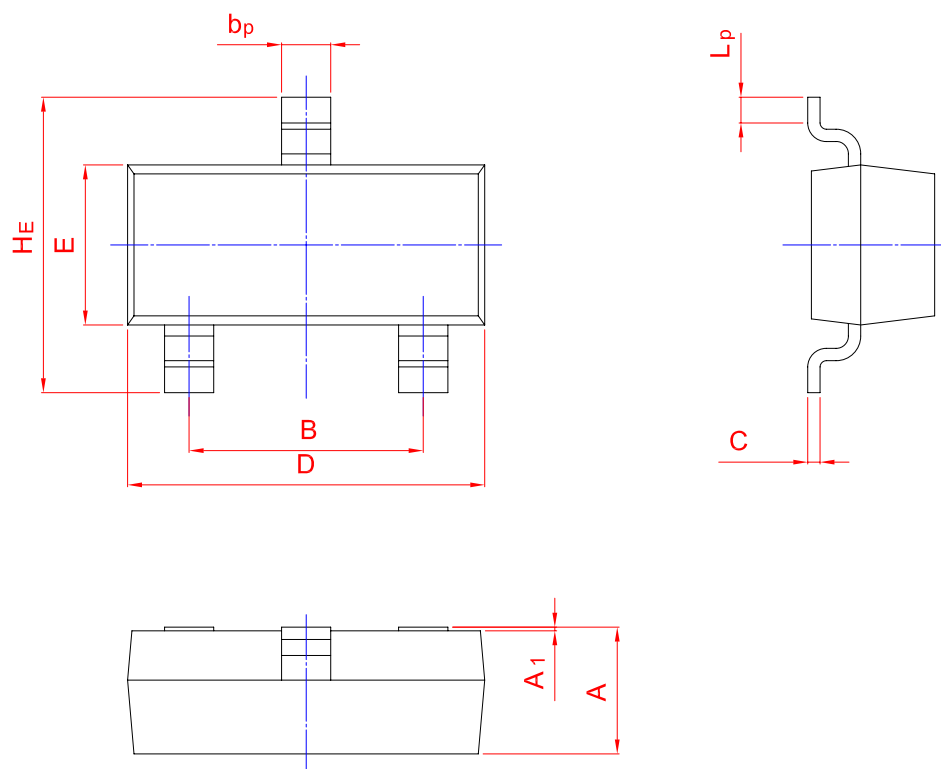
Typical Characteristics



PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23



UNIT	A	B	b_p	C	D	E	H_E	A_1	L_p
mm	1.40 0.95	2.04 1.78	0.50 0.35	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	0.50 0.20