

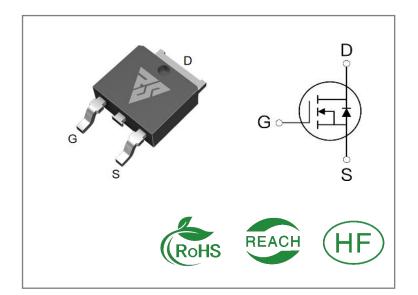
ID	R _{DS} (ON)(Typ)	VDSS
2A	6Ω	1000V

Applications:

- Switch Mode Power Supply(SMPS)
- Adapter & Charger
- AC-DC Switching Power Supply

Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability



Ordering Information

Part Number	Package	Package Marking		Qty.	
RS2N100D	T0-252	RS2N100D	Tape&reel	2500 PCS	

Absolute Maximun Ratings Tc= 25℃ unless otherwise specified

Symbol	Parameter	RS2N100D	Units
VDSS	Drain-to-Source Voltage	1000	V
ID	Continuous Drain Current TC=25℃	2	Δ
IDM	Pulsed Drain Current (Note*1)	8	Α
PD	Power Dissipation	75	W
VGS	Gate- to- Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L = 10mH, VDD = 50V, RG = 25 Ω	45	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	$^{\circ}\!\mathrm{C}$
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

^{*} Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.



Thermal Resistance

Symbol	Parameter	RS2N100D	Units	Test Conditions
RθJC	Junction-to-Case	1.67	°C/W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^{\circ}$ C
RθJA	Junction-to- Ambient	60		1 cubic foot chamber,free air.

OFF Characteristics TJ= 25 ^oC unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	1000 V		V	VGS=0V,ID=250μ A	
IDSS	Drain- to- Source Leakage Current			1	μΑ	VDS=1000V,VGS =0V
ICCC	Gate- to- Source Forward Leakage Gate- to- Source Reverse Leakage			100	- A	VGS=30V ,VDS=0 V
IGSS			1	-100	nA	VGS=-30V ,VDS= 0V

ON Characteristics TJ=25 °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance(Note*2)		6	7.2	Ω	VGS=10V,ID=1A
VGS(TH	Gate Threshold Voltage	3		4	V	VGS=VDS,ID=25 0μA

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time		36			
trise	Rise Time		12			VDS=500V
td(OFF)	Turn- OFF Delay Time		100		nS	ID=2A RG=25Ω
tfall	Fall Time		43			



Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance 419			VGS=0V		
Coss	Output Capacitance		45		рF	VDS=25V
Crss	Reverse Transfer Capacitance		9			f=1.0MHz
Qg	Total Gate Charge		16			VDS=800V
Qgs			2		nC	ID=2A
Qgd			8			VGS=15V

Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			2	Α	Integral pn- diode
ISM	Maximum Pulsed Current			8	Α	in MOSFET
VSD	Diode Forward Voltage			1.4	V	IS=1.0A,VGS=0V
trr	Reverse Recovery Time		432		nS	VGS=0V
Qrr	Reverse Recovery Charge		424		nC	IS=2A,di/dt=100A /μs

Notes:

^{* 1.} Repetitive rating, pulse width limited by maximum junction temperature.

^{* 2.} Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%



Typical Feature Curve

Figure 1. Output Characteristics (T_J = 25°C)

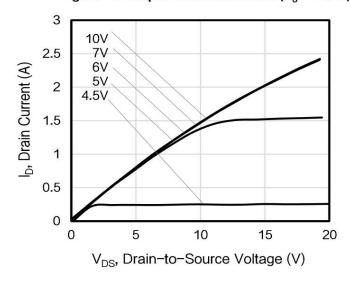


Figure 3. Drain Current vs. Temperature

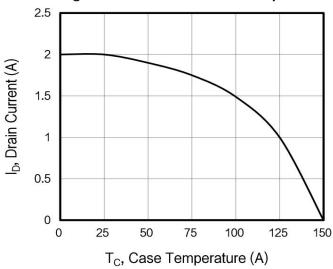


Figure 5. Transfer Characteristics

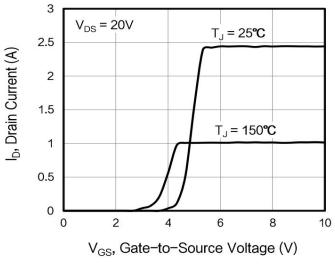


Figure 2. Body Diode Forward Voltage

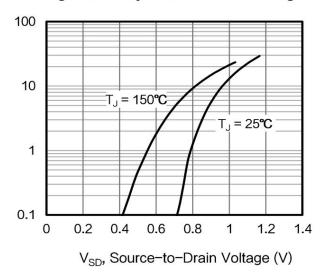


Figure 4. BV_{DSS} Variation vs. Temperature

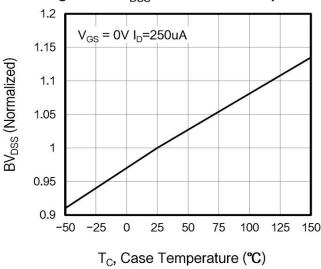
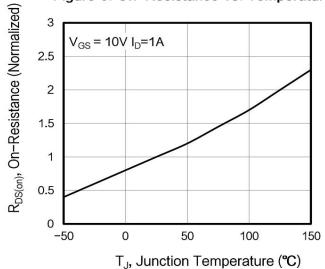


Figure 6. On-Resistance vs. Temperature



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Is, Source Current (A)



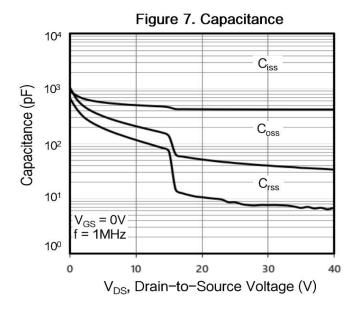


Figure 8. Gate Charge

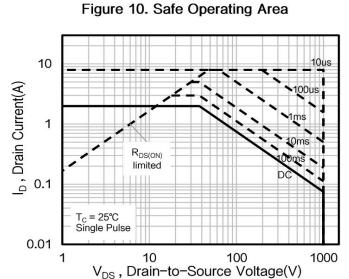
(N) 98 8

V_{DD} = 800V

O 5 10 15 20

Q_q, Total Gate Charge (nC)

Figure 9. Transient Thermal Impedance 10¹ Z_{thJC}, Thermal Impedance (K/W) 10º D = 0.5 D = 0.2 D = 0.1 D = 0.05 D = 0.02 D = 0.01 10-1 10-2 Single Pulse 10-3 10-5 10-7 10-6 10-4 10-3 10-2 10-1 T_p, Pulse Width (s)



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Test Circuits and Waveforms

Figure A: Gate Charge Test Circuit and Waveform

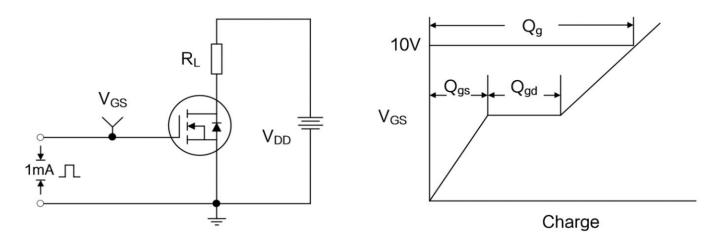


Figure B: Resistive Switching Test Circuit and Waveform

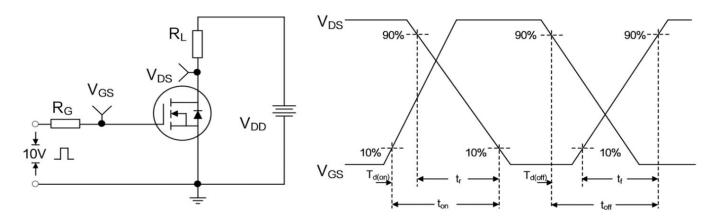
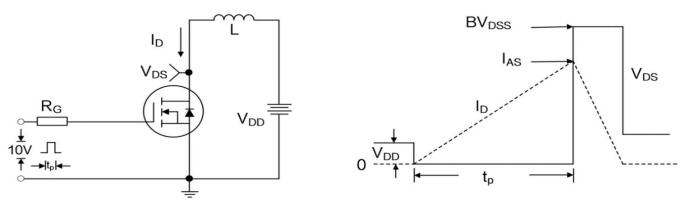


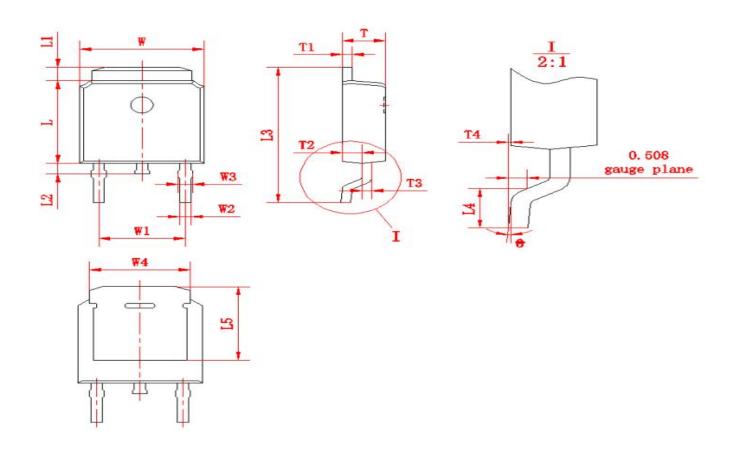
Figure C: Unclamped Inductive Switching Test Circuit and Waveform



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Package outline drawing(TO-252 Unit: mm)



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符号	Min	Max	符号	Min	Max	符号	Min	Max
W	6.50	6.70	L1	0.80	1.20	T1	0.48	0.58
W1	(4.572)		L2	0.60	1.00	T2	0.95	1.15
W2	0.6	0.8	L3	9.70	10.30	Т3	0.48	0.58
W3	0.68	0.88	L4	1.30	1.70	T4	0.00	0.12
W4	(5	.3)	L5	(5.20)		0	0	8
L	6.00	6.20	Т	2.20	2.40			



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