

VDS	RDS(on)	ID@25℃
1200V	40mΩ	68A

Applications:

- Solar Inverters
- Switch Mode Power Supplies
- High Voltage DC/DC Converters
- EV Charging
- Motor Drives

Features:

- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Easy to Parallel and Simple to Drive
- Avalanche Ruggedness

Benefits:

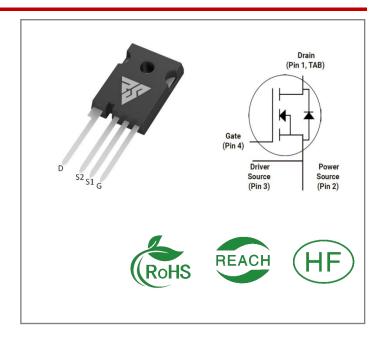
- Higher System Efficiency
- Reduced Cooling Requirements
- Increased Power Density
- Increased System Switching Frequency

Ordering Information

Part Number	Package	Marking	Packing	Qty.	
RSM120040Z	TO-247-4	RSM120040Z	Tube	30 PCS	

Maximum Ratings (TJ= 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
VDSmax	Drain - Source Voltage	1200	V	VGS=0V,ID =100μA	
VGSmax	Gate - Source Voltage	-8/+22	V	Absolute maximum values	
VGSop	Gate - Source Voltage	-4/+18	٧	Recommended operational values	
ID	Continuous Drain Current	68 49	Α	VGS=18V, TC =25°C VGS=18V, TC =100°C	
ID(pulse)	Pulsed Drain Current	100	Α	Pulse width tp limited by TJmax	
PD	Power Dissipation	340	W	TC =25°C, TJ =175°C	
TL	Solder Temperature	260	$^{\circ}$		
TJ, Tstg	Operating Junction and StorageTemperature	-40 to + 175	$^{\circ}\!$		





Electrical Characteristics (TJ= 25°C unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions	Note
V(BR)D SS	Drain-Source Breakdown Voltage	120 0			٧	VGS=0V,ID =100μA	
	Gate Threshold	1.9	2.6	4.0	٧	VGS= VDS, IDS=9.5mA,TC =25°C	
VGS(th)	Voltage		1.8		٧	VGS= VDS, IDS=9.5mA,TC =175℃	
IDSS	Zero Gate Voltage Drain Current		1	100	μА	VDS= 1200V, VGS=0V	
IGSS	Gate-Source Leakage Current		10	250	nA	VGS=22V, VDS= 0V	
DDC()	Drain-Source on-state		40	53	mΩ	VGS=18V, ID =33.3A, TC =25°C	
RDS(on)	Resistance		65			VGS=18V, ID =33.3A, TC =175℃	
Ciss	Input Capacitance		207 0			VCS-0V VDS-1000 V	
Coss	Output Capacitance		112		рF	VGS=0V, VDS=1000 V, f=1MHz, VAC=25 mV	
Crss	Reverse Transfer Capacitance		11			. I	
EON	Turn-On Switching Energy		141 0		. μJ	VDS =800V, VGS =-4/18V,ID = 33.3A,	
EOFF	Turn-Off Energy		750		μ	RG(ext) = 2.5Ω, L= 200 μ H	
td(on)	Turn-On Delay Time		17				
tr	Rise Time		58			VDS =800V, VGS =-4/18 V	
td(off)	Turn-Off Delay Time		26		ns	ID = 33.3A, RG(ext) =2.5 Ω , RL =20 Ω	
tf	Fall Time		15				
RG(int)	Internal Gate Resistance		4.9		Ω	f=1 MHz, VAC=25mV	
Qgs	Gate to Source Charge		34		nC		
Qgd	Gate to Drain Charge		20		nC	VDS=800V, VGS=-4/18V ID =33.3A	
Qg	Total Gate Charge		121				



Reverse Diode Characteristics (TJ= 25° C unless otherwise specified)

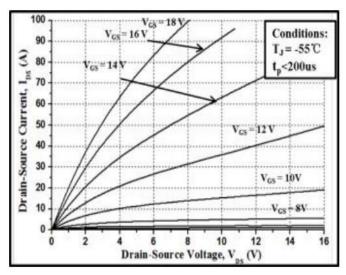
Symbol	Parameter	Тур.	Max	Unit	Test Conditions	Note
VCD	Diada Famusud Valtaga	4.5		٧	VGS=-4V, ISD = 20 A, TJ = 25℃	
VSD	/SD Diode Forward Voltage 4.2		٧	VGS=-4V, ISD= 20 A, TJ= 175℃		
IS	Continuous Diode Forward Current		51	Α	VGS=-4V,TC= 25°C	
trr	Reverse Recovery time	38		ns		
Qrr	Reverse Recovery Charge	109		nC	ISD= 33.3 A, VR = 800V	
Irrm	Peak Reverse Recovery Current	5		А	****	

Thermal Characteristics (TJ= 25°C unless otherwise specified)

Symbol	Parameter	Тур.	Unit	Test Conditions	Not e
RθJC	Thermal Resistance from Junction to Case	0.44	°C/W		
RθJA	Thermal Resistance From Junction to Ambient	40	C/ VV		

3 /8

Typical Feature Curve



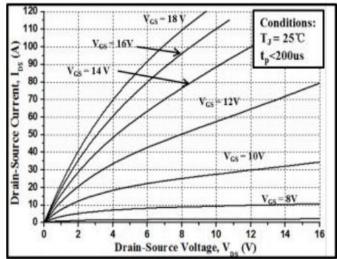


Figure 1. Output Characteristics T_J = -55C

Figure 2. Output Characteristics T_J = 25**C**Copyright Reasunos



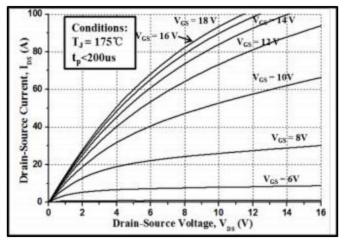


Figure 3. Output Characteristics T_J = 1750

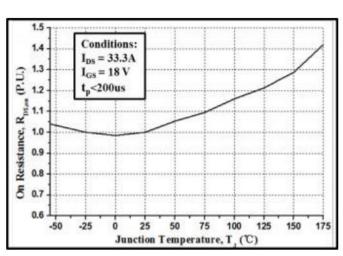


Figure 4. Normalized On-Resistance vs. Temperature

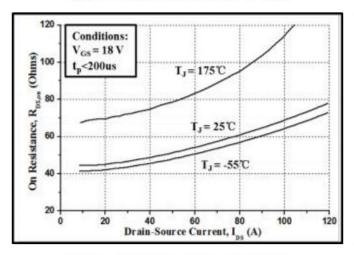


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

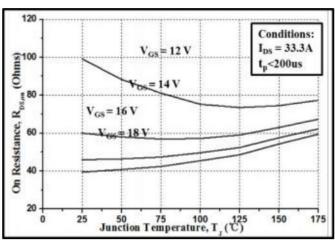


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

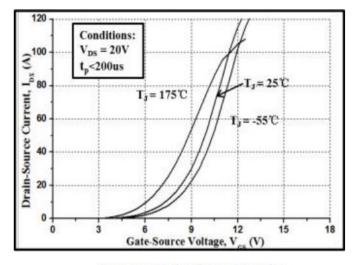


Figure 7. Transfer Characteristic for Various Junction Temperatures

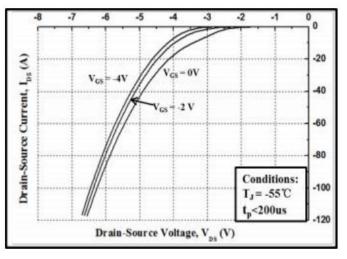
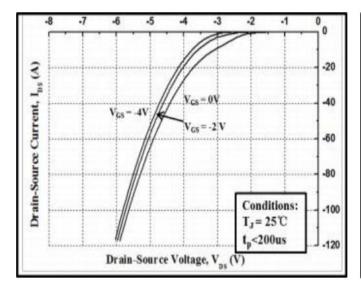


Figure 8. Body Diode Characteristic at -550





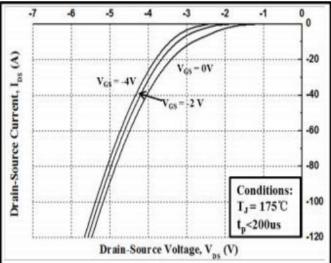
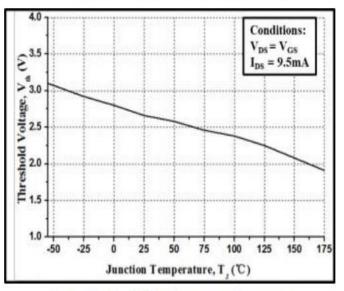


Figure 9. Body Diode Characteristic at 25C

Figure 10. Body Diode Characteristic at 1750



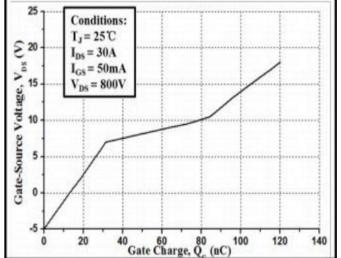
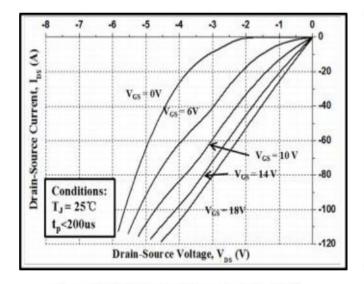


Figure 11. Threshold Voltage vs. Temperature

Figure 12. Gate Charge Characteristics







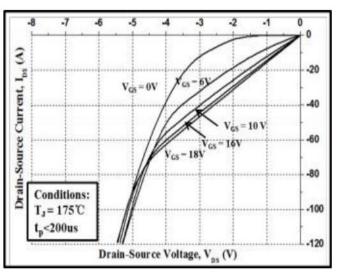


Figure 14. 3rd Quadrant Characteristic at 1750

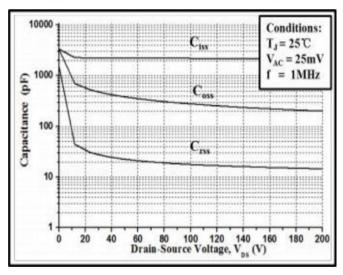


Figure 15. Capacitances vs. Drain-Source Voltage (0 - 200V)

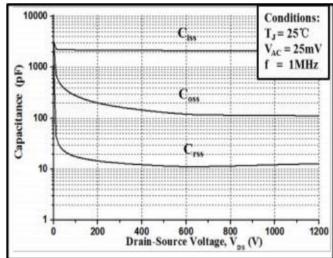
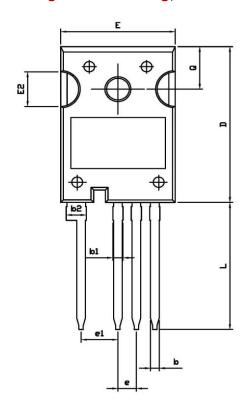
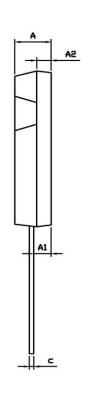


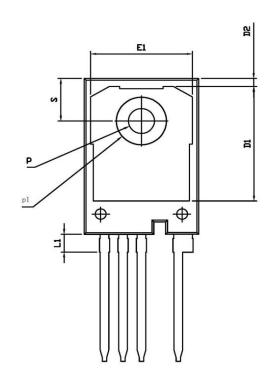
Figure 16. Capacitances vs. Drain-Source Voltage (0 - 1200V)



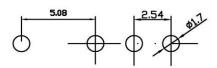
Package outline drawing(TO-247-4 Unit: mm)







RECOMMENDED LAND PATTERN



UNIT: mm

	MIN	NOM	MAX
A	4.80	5.00	5. 20
A1	2. 25	2.40	2.45
A2	1.85	2.00	2. 15
b	1.05	1.20	1. 35
b1	1.00	1.30	1.60
b2	2.35	2.65	2.95
С	0.50	0.60	0.70
D	22.34	22.54	22.74
D1	16.00	16.50	17.00
D2	0.97	1.17	1. 37
е	2. 34	2.54	2.74
e1	4.88	5.08	5. 28
Е	15.60	15.80	16.00
E1	13.50	14.00	14. 50
E2	4.80	5.00	5. 20
L	18.08	18.38	18.68
L1	2.38	2.58	2.78
р	3. 50	3.60	3. 70
p1	6.60	6.80	7.00
Q	6.00	6. 15	6. 30
S	6.00	6. 15	6. 30



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