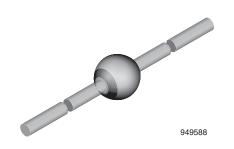


BYV28-50, BYV28-100, BYV28-150, BYV28-200

Vishay Semiconductors

Ultra-Fast Avalanche Sinterglass Diode



DESIGN SUPPORT TOOLS

click logo to get started



MECHANICAL DATA

Case: SOD-64

Terminals: plated axial leads, solderable per MIL-STD-750,

method 2026

Polarity: color band denotes cathode end

Mounting position: any Weight: approx. 858 mg

FEATURES

- · Controlled avalanche characteristic
- Low forward voltage
- Ultra fast recovery time
- · Glass passivated junction
- Hermetically sealed package
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

• Very fast rectification e.g. for switch mode power supply

ORDERING INFORMATION (Example)					
DEVICE NAME ORDERING CODE TAPED UNITS MINIMUM ORDER QUANT					
BYV28-200	BYV28-200-TR	2500 per 10" tape and reel	12 500		
BYV28-200	BYV28-200-TAP	2500 per ammopack	12 500		

PARTS TABLE					
PART	TYPE DIFFERENTIATION	PACKAGE			
BYV28-50	V _R = 50 V; I _{F(AV)} = 3.5 A	SOD-64			
BYV28-100	$V_R = 100 \text{ V}; I_{F(AV)} = 3.5 \text{ A}$	SOD-64			
BYV28-150	$V_R = 150 \text{ V}; I_{F(AV)} = 3.5 \text{ A}$	SOD-64			
BYV28-200	V _R = 200 V; I _{F(AV)} = 3.5 A	SOD-64			

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
	See electrical characteristics	BYV28-50	$V_R = V_{RRM}$	50	V	
Reverse voltage = repetitive peak reverse		BYV28-100	$V_R = V_{RRM}$	100	V	
voltage		BYV28-150	$V_R = V_{RRM}$	150	V	
		BYV28-200	$V_R = V_{RRM}$	200	V	
	See electrical characteristics	BYV28-50	V_{RSM}	55	V	
Dook roveree veltage, non repetitive		BYV28-100	V_{RSM}	110	V	
Peak reverse voltage, non repetitive		BYV28-150	V_{RSM}	165	V	
		BYV28-200	V_{RSM}	220	V	
Peak forward surge current	t _p = 10 ms, half sine wave		I _{FSM}	90	Α	
Repetitive peak forward current			I _{FRM}	25	Α	
Average forward current			I _{F(AV)}	3.5	Α	
Pulse energy in avalanche mode, non repetitive (inductive load switch off)	I _{(BR)R} = 1 A, Tj = 175 °C		E _R	20	mJ	
Junction and storage temperature range			$T_j = T_{stg}$	-55 to +175	°C	



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MAXIMUM THERMAL RESISTANCE (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	TEST CONDITION SYMBOL VALUE		UNIT		
Junction ambient	Lead length I = 10 mm, T _L = constant	R_{thJA}	25	K/W		
	On PC board with spacing 25 mm	R_{thJA}	70	K/W		

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Famuerd voltage	I _F = 5 A		V_{F}	-	-	1.1	V
Forward voltage	I _F = 5 A, T _j = 175 °C		V_{F}	-	-	0.89	V
	$V_R = V_{RRM}$		I _R	-	-	1	μA
Reverse current	V _{RSM}		I _R	-	-	100	μA
	$V_R = V_{RRM}$, $T_j = 165$ °C		I _R	-	-	150	μA
Reverse recovery time	I _F = 0.5 A, I _R = 1 A, i _R = 0.25 A		t _{rr}	-	-	30	ns

TYPICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified)

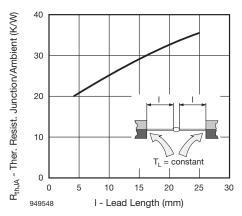


Fig. 1 - Max. Thermal Resistance vs. Lead Length

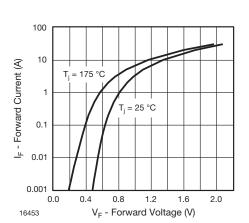


Fig. 2 - Forward Current vs. Forward Voltage

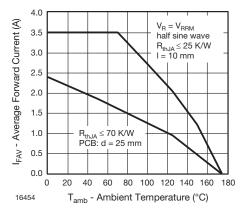


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

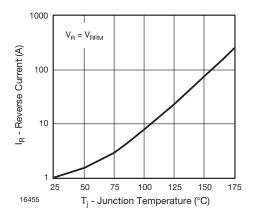


Fig. 4 - Reverse Current vs. Junction Temperature

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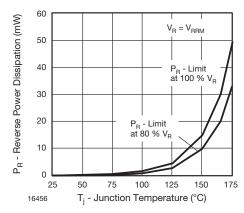


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

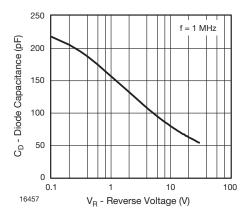
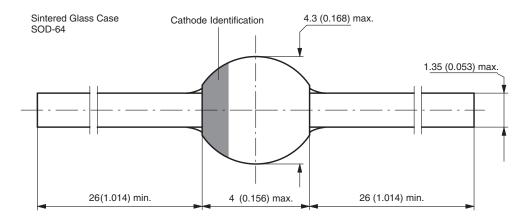


Fig. 6 - Diode Capacitance vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters (inches): SOD-64



Document-No.: 6.563-5006.4-4 Rev. 3 - Date: 09.February.2005

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