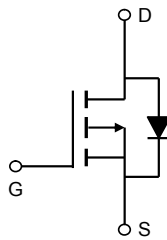


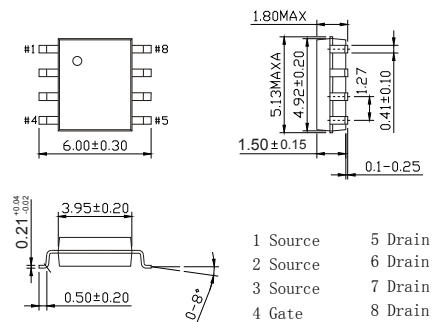
■ Features

- $V_{DS} = -30V$
- $I_D = -15A$ ($V_{GS} = -10V$)
- $R_{DS(ON)} < 7.5m\Omega$ ($V_{GS} = -10V$)
- $R_{DS(ON)} < 12m\Omega$ ($V_{GS} = -4.5V$)



SOP-8

Unit: mm



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DS}	-30	V
Gate-Source Voltage		V _{GS}	± 20	
Continuous Drain Current	T _A =25°C	I _D	-15	A
	T _A =70°C		-12.8	
Pulsed Drain Current		I _{DM}	-80	
Avalanche Current		I _{AS} , I _{AR}	30	
Avalanche energy	L=0.1mH	E _{AS} , E _{AR}	135	mJ
Power Dissipation	T _A =25°C	P _D	3.1	W
	T _A =70°C		2	
Thermal Resistance..Junction- to-Ambient	t ≤ 10s	R _{thJA}	40	°C/W
	Steady-State		75	
Thermal Resistance..Junction- to-Lead		R _{thJL}	24	
Junction Temperature		T _J	150	°C
Junction Storage Temperature Range		T _{stg}	-55 to 150	

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DS}	$I_D = -250 \mu A$, $V_{GS} = 0V$	-30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30V$, $V_{GS} = 0V$			-5	μA
		$V_{DS} = -30V$, $V_{GS} = 0V$, $T_J = 55^\circ C$			-25	
Gate-Body leakage current	I_{GSS}	$V_{DS} = 0V$, $V_{GS} = \pm 20V$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	-1.4		-2.7	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = -10V$, $I_D = -15A$			7.5	m Ω
		$V_{GS} = -10V$, $I_D = -15A$, $T_J = 125^\circ C$			11.5	
		$V_{GS} = -4.5V$, $I_D = -10A$			12	
On state drain current	$I_{D(on)}$	$V_{GS} = -10V$, $V_{DS} = -5V$	-80			A
Forward Transconductance	g_{FS}	$V_{DS} = -5V$, $I_D = -15A$	35	50		S
Input Capacitance	C_{iss}	$V_{GS} = 0V$, $V_{DS} = -15V$, $f = 1MHz$		5270	6400	pF
Output Capacitance	C_{oss}			945		
Reverse Transfer Capacitance	C_{rss}			745		
Gate resistance	R_g	$V_{GS} = 0V$, $V_{DS} = 0V$, $f = 1MHz$		2	3	Ω
Total Gate Charge (10V)	Q_g	$V_{GS} = -10V$, $V_{DS} = -15V$, $I_D = -15A$		100	120	nC
Total Gate Charge (4.5V)				51.5		
Gate Source Charge	Q_{gs}			14.5		
Gate Drain Charge	Q_{gd}			23		
Turn-On DelayTime	$t_{d(on)}$	$V_{GS} = -10V$, $V_{DS} = -15V$, $R_L = 1\Omega$, $R_{GEN} = 3\Omega$		14		ns
Turn-On Rise Time	t_r			16.5		
Turn-Off DelayTime	$t_{d(off)}$			76.5		
Turn-Off Fall Time	t_f			37.5		
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -15A$, $dI/dt = 100A/\mu s$		36.7	45	nC
Body Diode Reverse Recovery Charge	Q_{rr}			28		
Maximum Body-Diode Continuous Current	I_S				-5	A
Diode Forward Voltage	V_{SD}	$I_S = -1A$, $V_{GS} = 0V$			-1	V

Note : The static characteristics in Figures 1 to 6 are obtained using <300 μs pulses, duty cycle 0.5% max.

Typical Characteristics

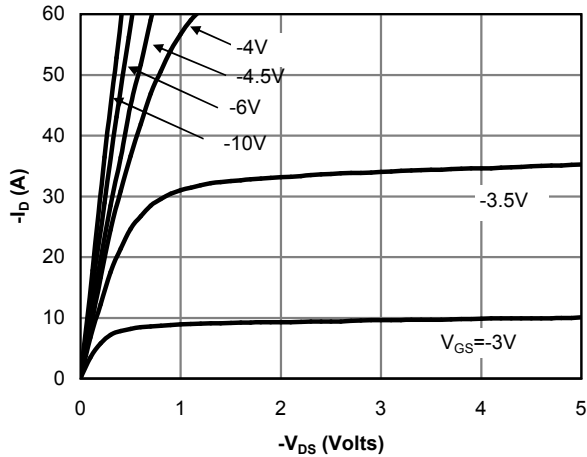


Fig 1: On-Region Characteristics (Note E)

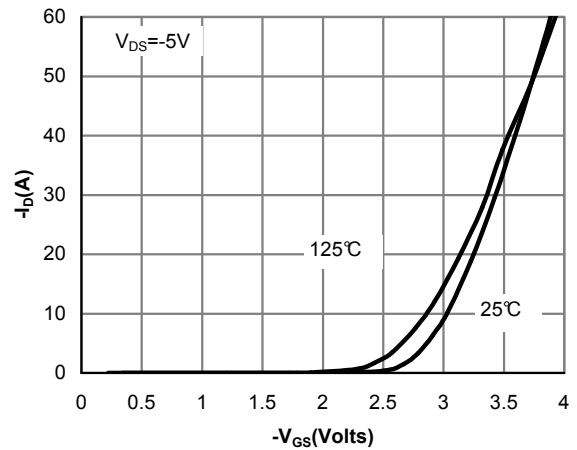


Figure 2: Transfer Characteristics (Note E)

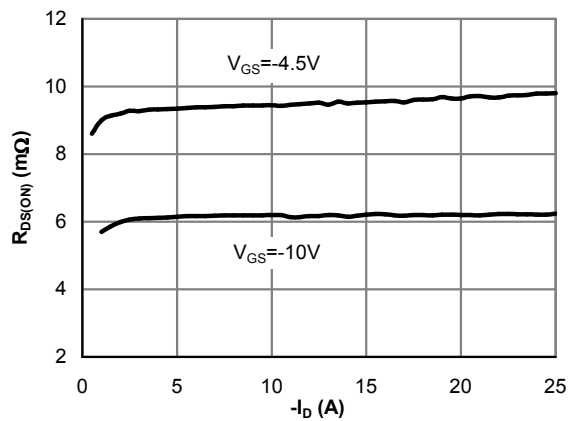


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

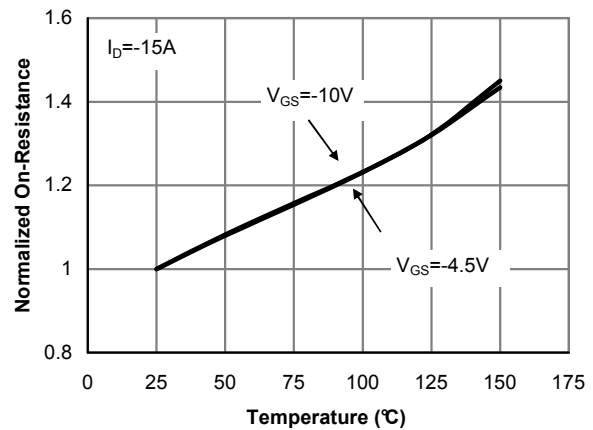


Figure 4: On-Resistance vs. Junction Temperature (Note E)

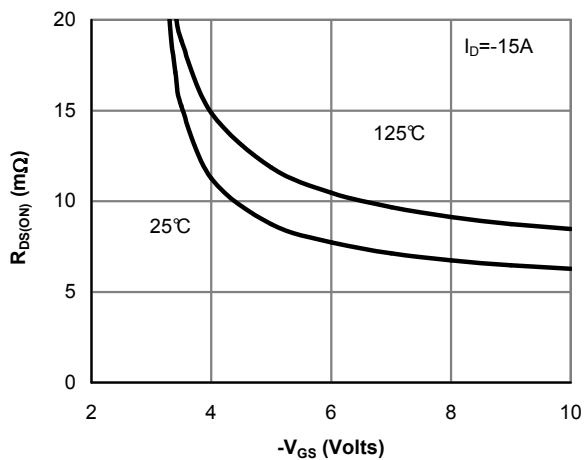


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

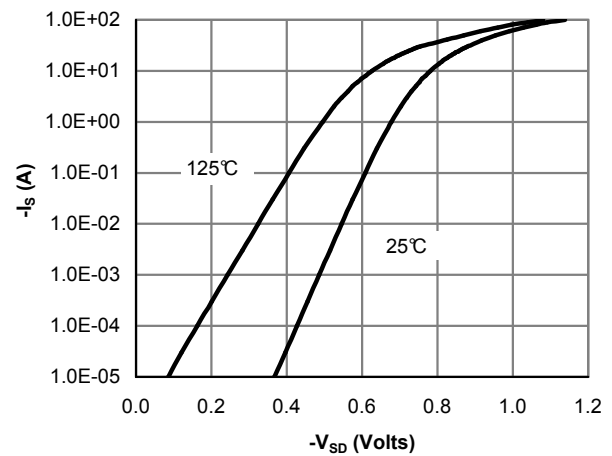


Figure 6: Body-Diode Characteristics (Note E)

Typical Characteristics

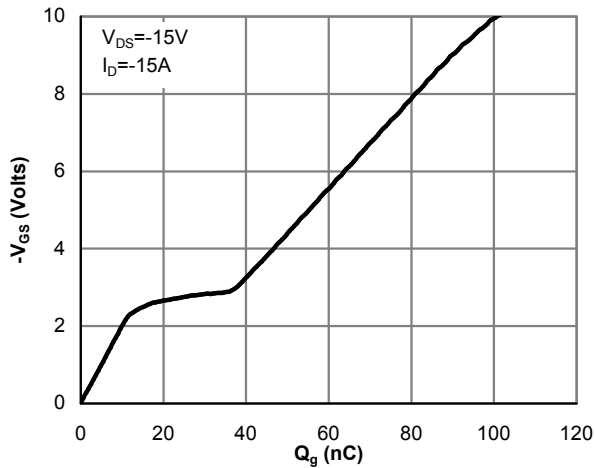


Figure 7: Gate-Charge Characteristics

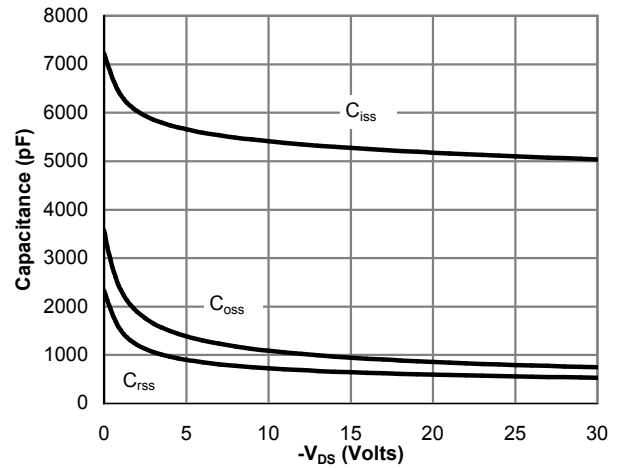


Figure 8: Capacitance Characteristics

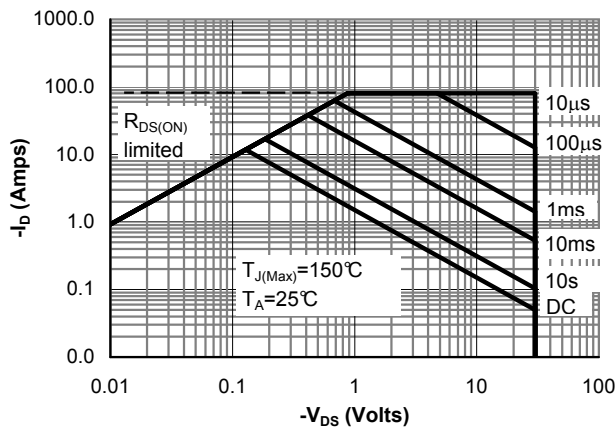


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

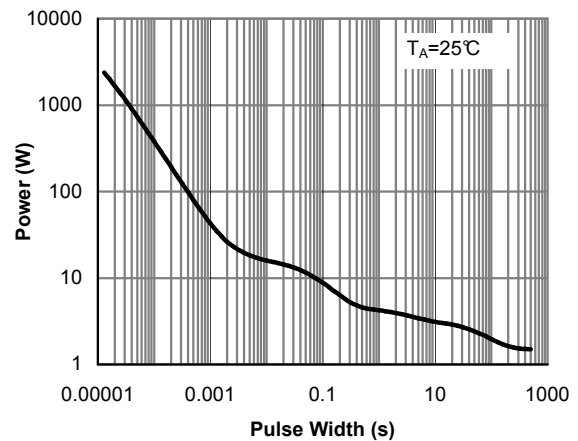


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

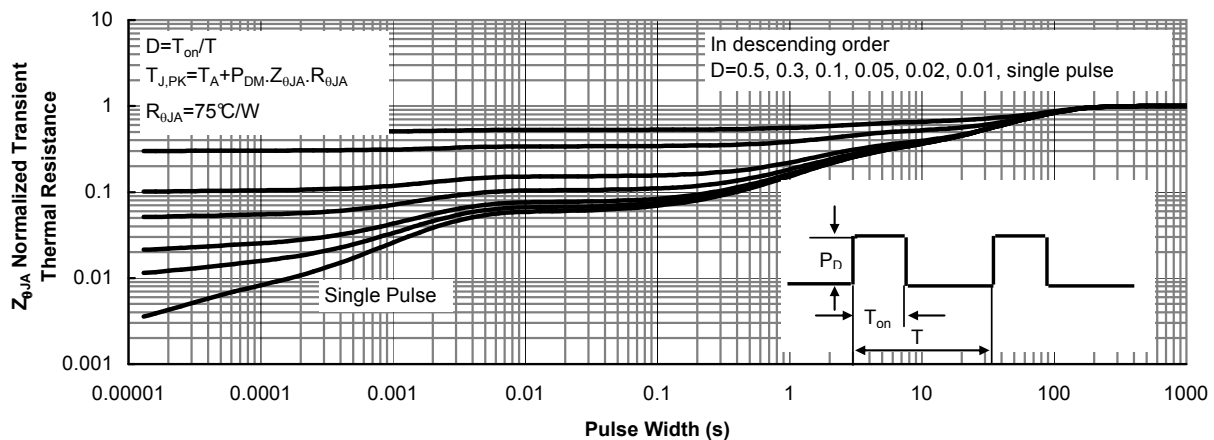


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)