

AO3424

N-Channel Enhancement Mode MOSFET

Feature

• 30V/2.0A, $RDS(ON) = 35m\Omega(MAX)$ @VGS = 10V.

 $R_{DS(ON)} \!=\!\! 40m\Omega(MAX) @V_{GS} \!=\! 4.5V. \label{eq:rescaled}$

 $R_{DS(ON)} \!=\! 55m\Omega(MAX) @V_{GS} \!=\! 2.5V.$

- \bullet Super High dense cell design for extremely low RDS(ON) .
- Reliable and Rugged.
- SC-59 for Surface Mount Package.

Applications

- Power Management
- Portable Equipment and Battery Powered Systems.

TA=25℃ Unless Otherwise noted

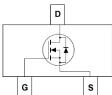
Absolute Maximum Ratings	TA=25℃ Unless Otherwise noted				
Parameter		Symbol	Limit	Units	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		Vgs	±12	V	
Drain Current-Continuous		I _D	2.0	A	

Electrical Characteristics TA=25 °C Unless Otherwise noted

Parameter	Symbol	Test Conditions	Min	Typ.	Max	Units
Off Characteristics						
Drain to Source Breakdown Voltage	BVDSS	VGS=0V, ID=250µA	30	-	-	V
Zero-Gate Voltage Drain Current	IDSS	VDS=30V, VGS=0V	-	-	1	μΑ
Gate Body Leakage Current, Forward	IGSSF	VGS=12V, VDS=0V	-	-	100	nA
Gate Body Leakage Current, Reverse	IGSSR	VGS=-12V, VDS=0V	-	-	-100	nA
On Characteristics						
Gate Threshold Voltage	VGS(th)	VGS=VDS, ID=250µA	0.6	-	1.5	V
Static Drain-source	RDS(ON)	VGS =10V, ID =5.8A	-	30	35	mΩ
On-Resistance		VGS =4.5V, ID =5A	-	33	40	mΩ
		VGS =2.5V, ID =4A	-	45	55	mΩ
Drain-Source Diode Characterist	ics and Maximum I	Ratings		•	•	
Drain-Source Diode Forward Voltage	VSD	VGS =0V, IS=1.25A			1.2	V

Dynam	ic				
Qg	Total Gate Charge	VDS=15V,VGS=10V,ID=2A	8.5	12	nC
Qgs	Gate-Source Charge		1.1		
Qgd	Gate-Drain Charge		1.8		
ton	Turn-on Time	VDD=15V,ID=2A,VGS=10V,RG=6Ω		40	nS
td(ON)	Turn-on Delay time		11		
tr	Turn-on Rise Time		17		
Td(off)	Turn-off Delay Time		37		
tf	Turn-off Fall Time		20		
toff	Turn-off Time			60	1

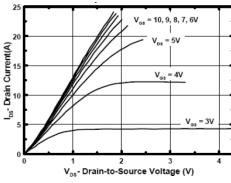


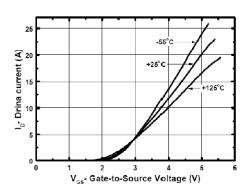


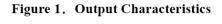


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Typical Characteristics







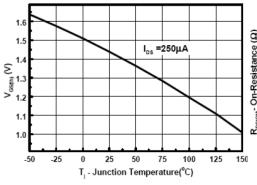


Figure 3. Gate Threshold Variation with Temperature

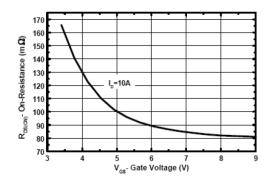


Figure 5. On-Resistance vs. Gate-to-Source Voltage Voltage

Figure 2. Transfer Characteristics

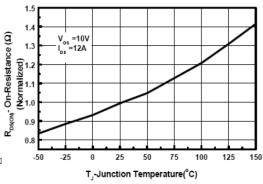


Figure 4. On-Resistance Variation with Temperature

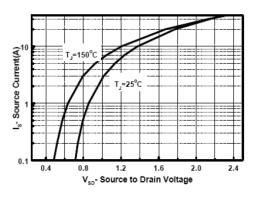


Figure 6. Source-Drain Diode Forward