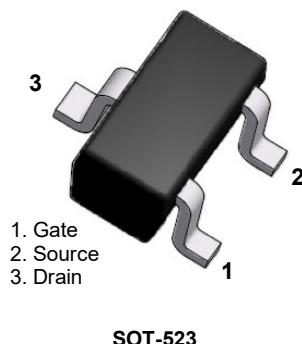


**150mW SOT-523 SURFACE MOUNT
Plastic Package
N-Channel MOSFET**

Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Continuous Gate-Source Voltage	$\pm 20\text{V}$	V
I_D	Continuous Drain Current	115	mA
P_D	Power Dissipation	150	mW
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient	833	$^\circ\text{C}/\text{W}$
T_{STG}	Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_J	Operating Junction Temperature	+150	$^\circ\text{C}$

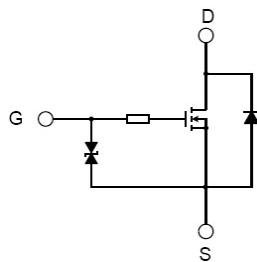
These ratings are limiting values above which the serviceability of the device may be impaired.



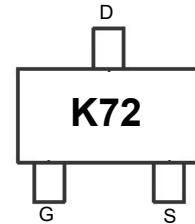
Specification Features:

- Low On-resistance
- Low Gate Threshold Voltage
- Low Input capacitance
- ESD Protected up to 1kV (HBM)
- RoHS Compliant
- Green EMC
- Matte Tin(Sn) Lead Finish
- Weight: approx. 0.002g

Electrical Symbol:



Device Marking Code:



Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Off Characteristics

Symbol	Parameter	Test Condition	Limits			Unit
			Min	Typ	Max	
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=10\mu\text{A}$	60			Volts
I_{GSS}	Gate-Body Leakage	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 20\text{V}$			± 1	μA
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}$			100	nA

On Characteristics

Symbol	Parameter	Test Condition	Limits			Unit
			Min	Typ	Max	
$V_{\text{th(GS)}}$	Gate-Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1		2.5	Volts
$I_{\text{D(ON)}}$	On-state Drain Current	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=7\text{V}$	500			mA
$R_{\text{DS(on)}}$	Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=500\text{mA}$			7.5	Ω
		$V_{\text{GS}}=5\text{V}, I_{\text{D}}=50\text{mA}$			7.5	Ω
g_{fs}	Forward Trans Conductance	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=200\text{mA}$	80		500	ms
$V_{\text{DS(on)}}$	Drain-Source On-Voltage	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=500\text{mA}$			3.75	V
		$V_{\text{GS}}=5\text{V}, I_{\text{D}}=50\text{mA}$			0.375	V
V_{SD}	Diode Forward Voltage	$I_{\text{S}}=250\text{mA}, V_{\text{GS}}=0\text{V}$			1	V

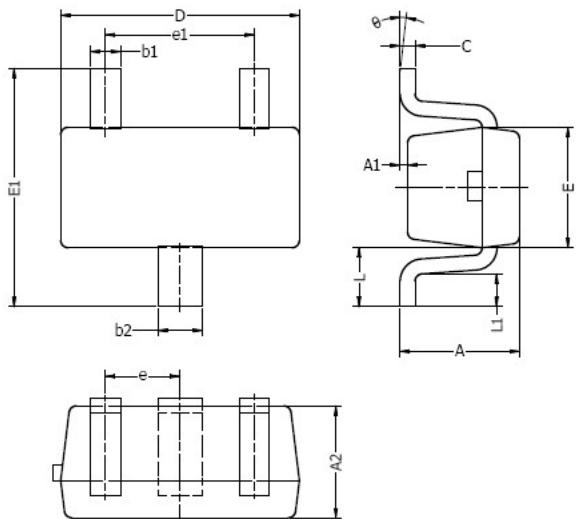
Dynamic Characteristics

Symbol	Parameter	Test Condition	Limits			Unit
			Min	Typ	Max	
C_{iss}	Input Capacitance		--	--	50	pF
C_{oss}	Output Capacitance	$V_{\text{DS}} = 25\text{V}, V_{\text{GS}} = 0\text{V}, f = 1.0\text{MHz}$	--	--	25	pF
C_{rss}	Reverse Transfer Capacitance		--	--	5.0	pF

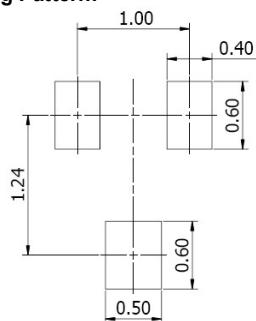
Switching Characteristics

Symbol	Parameter	Test Condition	Limits			Unit
			Min	Typ	Max	
$t_{\text{D(on)}}$	Turn-on Time	$V_{\text{DD}}=10\text{V}, R_{\text{L}}=20\Omega, I_{\text{D}}=500\text{mA}, V_{\text{GEN}}=10\text{V}, R_{\text{G}} = 10\Omega$	--	5.6	--	nS
$t_{\text{D(off)}}$	Turn-off Time		--	25	--	nS

SOT-523 Package Outline



Typical Soldering Pattern:



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.70	0.90	0.028	0.035
A1	0.00	0.10	0.000	0.004
A2	0.70	0.80	0.028	0.031
b1	0.15	0.25	0.006	0.010
b2	0.25	0.35	0.010	0.014
c	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
E	0.70	0.90	0.028	0.035
E1	1.45	1.75	0.057	0.069
e	0.50 TYP.		0.020 TYP.	
e1	0.90	1.10	0.035	0.043
L	0.40 REF.		0.016 REF.	
L1	0.10	0.30	0.004	0.012
θ	0°	8°	0°	8°

NOTES:

1. Above package outline conforms to JEITA EAIJ ED-7500A SC-75A.
2. Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.